EEM16: Logic Design of Digital Systems

Fall 2015 Midterm
Wednesday, October 28, 2015
Time Limit: 110 Minutes

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This exam contains 15 pages (including this cover page) and 7 problems. Check to see if any pages are missing. Enter all requested information on the top of this page.

You are required to show your work on each problem on this exam. The following rules apply:

- The exam is closed book. You are allowed one double-sided $8\frac{1}{2} \times 11''$ double-sided cheat sheet.
- Calculators are not allowed.
- Show the intermediate steps leading to your final solution for each problem. There will be NO partial credit for work done correctly using a wrong answer from a previous part of a question. For example, if part a) is wrong and part b) depends on part a), then part b) will be wrong. Therefore, be very careful and double check your work!
- You can use both sides of the sheets to answer questions.

Do not write in the table to the right.

Problem	Points	Score
1	10	6
2	15	14
3	20	3
4	10	6
5	15	2
6	15	0
7	15	15
Total:	100	46

Problem 1 (10 points)
Reduce the following expression using Boolean algebra postulates and theorems. Show intermediate steps and mention the rules used at each step.

$$f(w,x,y,z) = \overline{\bar{x}\bar{y} + z} + z + xy + wz$$

$f(w,x,y,z) = (x'y'+z)' + z + xy + wz$

$= (x'y')'z' + z(w+1) + xy$ — De Morgan's Law + Factoring

$= (x'y')'z' + z + xy$ — De Morgan's Law

~~$(x'y')'z' + z$~~

$= (x''+y'')z' + z + xy$

$= xz' + yz' + z + xy$ — Distributive Property, Double Negation

~~$= xz'(y+y') + yz'(x+x') + z + xy(z+z')$~~

~~$= xyz' + xy'z' + xyz' + x'yz' + z + xyz + xyz'$~~

~~$= xyz' + xy'z' + x'yz' + z + xyz + xyz'$~~

~~$= xz' + xz'(y+y) + x(yz+yz') + x'yz'$~~

continued

$= xz' + yz' + z(y+y') + xy$

$= xz' + yz' + zy + y'z + xy$

$= xz' + y(z+z') + y'z + xy$

$= xz' + y(x+1) + y'z$

$= xz' + y + y'z$

$= xz' + y(z+z') + y'z$

$= xz' + yz + yz' + y'z$

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Problem 2 (15 points)

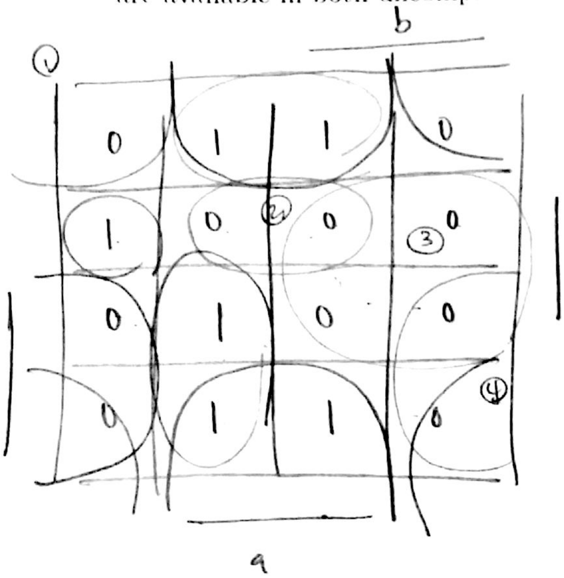
Consider the following function

$$f(a, b, c, d) = \Sigma m(1, 3, 4, 9, 11, 13).$$

7 (a) (8 points) Use K-maps to minimize **both** the sum of products and products of sums form. Write the simplified Boolean expressions.

7 (b) (7 points) Implement the function using minimal number of gates. You can use either NOR gates or NAND gates with maximum number of four inputs. Assume that inputs are available in both uncomplemented and complemented form.

a)



SoP : 3 terms.

ac' + dab' + a'b'cd'

fmin = ac' + dab' + a'b'cd'

PoS : 4 Terms

(d+c) (c'+a'+d) (b'+c') (d'+a)

Fmax = (c+d) (c'+a'+d) (b'+c') (d'+a)

b) fmin uses 3 and gates w/ 2, 3, 4 inputs respectively

fmax uses and gates and 4 OR gates w/ 2, 3, 2, 2 inputs respectively.

(circuit on next page)

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Problem 3 (20 points)
Consider the following function

$$f(a,b,c,d) = \overline{(\bar{a}b + d)(\bar{b} + \bar{d})}$$

$[(a'b+d)(b'+d')]'$

$a'bb' + a'bd' + db' + dd'$

$[a'bd' + db']'$

(a) (6 points) Find minimal sum-of-products and products-of-sums expressions for the switching expression f.

(b) (8 points) Determine the prime implicants and the essential prime implicants for this expression.

(c) (6 points) Does this function have a unique minimal sum-of-products? If not, list any other minimal sum-of-products expressions. Does this function have a unique minimal product-of-sums? If not, list any other minimal product-of-sums expressions.

a) $f(a,b,c,d) = [(a'b+d)(b'+d')]'$

$= [a'bb' + a'bd' + db' + dd']'$

$= [0 + a'bd' + db' + 0]'$

$= [a'bd' + db']'$

$= (a'bd')'(db')'$

$= (a'' + b' + d'')(d' + b'')$ ✓ De Morgan's law

$f_{min, POS} = (a + b' + d)(d' + b)$

$= ad' + ab + b'd' + bb' + dd' + bd$

$= ad' + ab + b'd' + \ldots$

$f_{min, SOP} = ad' + ab + b'd' + bd$

$= ad' + ab + b'd' + bb' + d'd' + bd'$

$= ad' + ab + b'd' + d' + bd'$

$= ad' + d' + ab + d'(b+b')$

$= ad' + d' + ab$

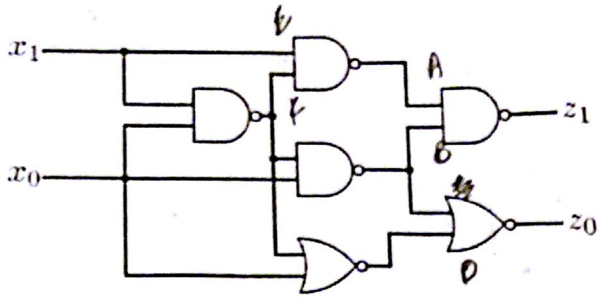
$= a(d'+1) + ab$

$= a + ab = a(b+1) = a = f_{min, SOP}$

✗

Problem 4 (10 points)

Analyze the NAND-NOR network shown in the figure below. Obtain switching expressions for the outputs.



(6)

$z_1 = \sout{A+B}\ (AB)'$

$A = (EF)'$ $B = (F x_0)'$

$E = x_1$ $B = ((x_1 x_0)' x_0)'$

$F = (x_1 x_0)'$

$A = (x_1 (x_1 x_0)')'$

$z_1 = [(x_1 (x_1 x_0)')' \cdot [(x_1 x_0)' x_0]]'$

$z_1 = [(x_1 [x_1' + x_0'])' \cdot [(x_1' + x_0'') x_0]]'$

$z_1 = [(\sout{x_1 x_1'} + x_1 x_0')' \cdot [(x_1' x_0 + \sout{x_0 x_0})]]'$

$z_1 = [[x_1 x_0']' \cdot [x_1' x_0 + x_0]']]'$

$z_1 = [(x_1' + x_0'')((x_1' x_0)'(x_0)')]'$

$z_1 = [(x_1' + x_0)(x_1' x_0)'(x_0')]'$

$z_1 = [(x_1' x_0' + \sout{x_0 x_0'})(x_1' x_0)']' = [(x_1' x_0')(x_1' x_0)']'$

$= [(x_1' x_0')[x_1'' + x_0']]'$

$= [\sout{x_1 x_1'} x_0' + x_0' x_1']'$

$= [x_0' x_1']'$

$= x_0'' + x_1''$

$z_1 = x_0 + x_1$

$D = (x_0 + F)'$

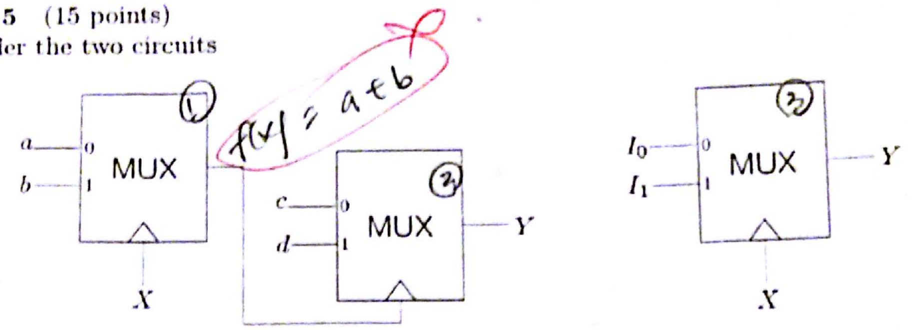
$z_0 = (B + D)'$

$z_0 = ((x_1 x_0') x_0)' + (x_0 + F)'$

$z_0 = (((x_1 x_0') x_0)' + (x_0 + (x_1 x_0)'$

Problem 5 (15 points)

Consider the two circuits



(a) (8 points) Find I_0, I_1, so that both circuits are equivalent.

(b) (7 points) Write the Boolean expression of Y in terms of X, a, b, c, and d.

a) MUX #1

X	f(x)
0	a
1	b

→ MUX #2

f(x)	Y
0 ~~a+b~~	c
1	d

a	b	a+b
0	0	b
1	0	1
0	1	1
1	1	1

$f(x) = X'a + Xb$

$Y = f(x)'c + f(x)d$

Therefore,

b) $Y = (X'a)'c + (Xb)d$

$Y = (X'' + a')c + (Xbd)$

$Y = cX + a'c + bdX$

OR $Y = X(c+bd) + a'c$

a) ~~Y = X(c+bd)~~

$Y = X(c+bd) + a'c$

if $X=0$, then $Y = a'c$

if $X=1$, then $Y = c+bd+$

$I_0 = a'c$

$I_1 = a'c + c + bd$

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