EEM16 Midterm

AURORA BEVERLEY YEH

TOTAL POINTS

74 / 80

QUESTION 1

4.3 (C) 7 / 7

√ - 0 pts Correct

Problem 120 pts

1.1 (a-c) 8 / 12

√ - 1 pts Question1 (b): 4 pts. Missing or having extra essential prime implicant, or incorrect term

√ - 3 pts Question1 (c): 4 pts. Wrong terms

1 "AB" is not an essential prime implicant, since if without it, all the 1s are already covered by the rest terms.

2 It starts from 0, not 1

1.2 (d-f) 7/8

√ - 1 pts Question1 (f): 3 pts. Incorrect term to remove the risk.

QUESTION 2

2 Problem 2 20 / 20

√ - 0 pts Correct

QUESTION 3

3 Problem 3 19 / 20

√ - 2 pts (f) incorrect real number or bias

+ 1 Point adjustment

(f) partial

QUESTION 4

Problem 4 20 pts

4.1 (a) 8 / 8

√ - 0 pts Correct

4.2 (b) 5/5

√ - 0 pts Correct

Midterm Exam

Name (Last, First): Yeh, Aurora Student Id #: 305110110

Do not start working until instructed to do so.

- 1. You must answer in the <u>space provided</u> for answers after every question. We will ignore answers written anywhere else in the booklet. <u>All pages in this booklet must be accounted</u> for otherwise it will not be graded.
- 2. You are permitted 1 page of notes 8.5x11 (front and back).
- 3. You may not use any electronic device.

Following table to be filled by course staff only

	Maximum Score	Your Score
Question 1		
Question 2		
Question 3		
Question 4		
TOTAL	100	

Question #1

Consider the following Karnaugh Map for the Boolean function, Y. A blank truth table is provided for your convenience.

AB 8										
			"00"		"	01"	,	'11"դ	"10"	
	"00	17	0		0		1	1	1	ı
CDD	"01	1 2	C			1		Х	X	ië.
CD	"11	11	()		0		1	1)	3
C	"10	91	C	1		(1)		X	0	
	A		B 5	С		D	-	Y	Dual	
1	0		0 .	0		0	,	0	0 × × 00 × 0 00 1	
2 [0		0	0 0		1		1	×	
3	0		0 1			0		1	×	
ч	0		0 1			1		0	0	
5	0	L	1	0		0		0		
6	0	_	1	0		1		1	1	
70	0		1	1		0	1	1	×	
7.	0		1	1		1		0	0	
્વ	1	_	0	0		0			1 1	
10	1		0			1		X	10	
17	1		0	1		0	-	0	- 0	
12	1		0	1	_	1	_			
13	1	_	1	0		0	1	1	_	
14	1		1	0		1	1	X	00	
15	1	_	1	1	_	0	4	X	0	
6	1	<u>l</u> _	1	1		1		1		

(a) Circle the prime implicants on the map.

How many prime implicants are there?

(b) Write the Boolean (sum-of-product) expression of the essential prime implicants of (b) (if any).

EssentialPrimeImplicants = (~C^D) (A^R) (A^C) (A^C) (A^C) (A^C)

(c) Complete the following statement

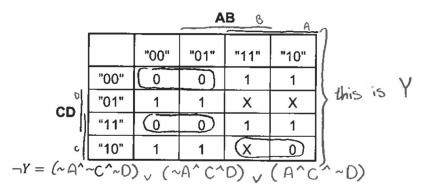
$$\neg Y = \prod_{A} M(2,3,6,7,9,12,13,162)$$

$$(C_{v} - D)^{(-A_{v} - B)^{(-A_{v} - C)^{(-A_{v} - C)^{(-A$$

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(d) Express as a minimal sum of product, ¬Y. The K-map is provided for your convenience.



No

(e) Is the dual of this function itself? Circle one: Yes Justify your answer:

whon you invert the truth table and reverse the terms, everything except the don't cares match, but we don't cave about those

(f) If the logic is implemented using its minimal sum-of-product expression, is there a risk of a static hazard?

(Yes) Circle one:

No

Uh what?

Under what input conditions?

the last term (~A^C^~D)

the last term (-A^C^-D) it's not overlapping any other essential prime implicant

What product term(s) would you add to remove the risk? $(\beta^{^{\circ}}C^{^{\circ}}\sim D)$

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2-input

Question #2

Answer:

Answer:

(a) Rewrite the following Boolean equation as a sum-of-products or product-of-sum. Hint: apply DeMorgan's theorem.

$$W = \neg (\neg (c \lor \neg (a \land \neg b)) \lor (d \land \neg e))$$

$$= \sim (\sim (c \lor \sim a \lor b) \lor (d^{\wedge} \sim e))$$

$$= \sim (\sim (c \lor \sim a \lor b) \lor \sim (\sim d \lor e))$$

$$= \sim ((c \lor \sim a \lor b) \land (\sim d \lor e))$$

$$W = (c \lor \sim a \lor b) \land (\sim d \lor e)$$

(b) Rewrite the following Boolean equation as a sum-of-products or product-of-sum.

$$X = (c \lor (a \land b)) \lor (d \land e)$$

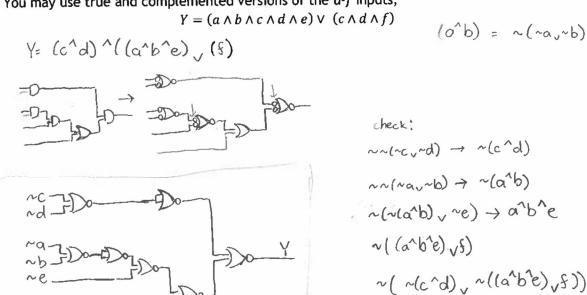
$$(c) \lor (a \land b) \lor (d \land e)$$

$$\sim (\sim (c \lor (a \land b)) \land \sim (d \land e))$$

$$\sim ((c \lor \sim (a \land b) \land (\sim d \lor e)) \rightarrow 27$$

$$x = (c)_{V}(a^{h}b)_{V}(d^{h}e)$$

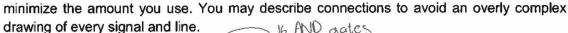
(c) Implement the following function using NOR gates only and the fewest, NOR gates possible. You may use true and complemented versions of the a-f inputs,

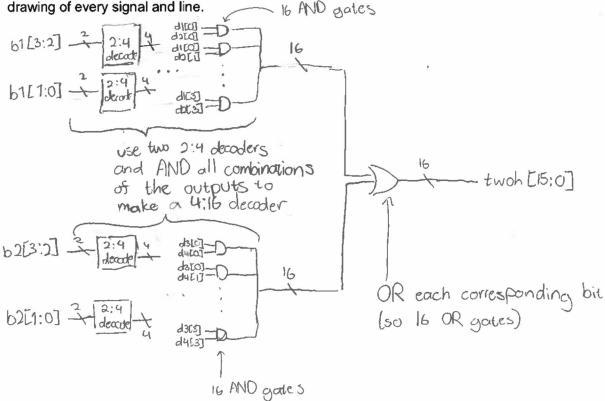


(c^d) ^ ((a^b^e),(s)) \

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Question #3	
The following 12-bit word can be used to represent different numbers depending	on the
encoding *** \$\frac{1}{2} \frac{1}{2} \fra	-2048 check
(a) If the word is 2's complement, what is the corresponding integer? -1066	+512 h)24 +256 + 36 +128 + 9
(b) If we convert the word into base-4, what is the represented number?	+ 64 + 16 + 106
(c) If we take answer in (b), write the 3's complement of the base-4 number. 100 22 1 not 4's complement so don't 11	-2048 1 982 1068
(d) If the word is <u>unsigned</u> fixed point 6.6, what is the corresponding number?	- X = ~ X +
What is the absolute accuracy of this representation? Vi28	
(e) What is this word in Hexadecimal? <u>bd6</u>	
(f) If the word is 6E5 floating point number (IEEE format S+EEEE+MMMMMM)	•
What is the bias? 15	5
What is the corresponding real number? -1.01010	
g) In base-20 system and using 3 "vigits" (base-20 digits) in 20's complement:	1616 -> 4.0.16
How would one represent a base-10 integer -1616? 15.19.4	-
What is the most positive value in base-10 integer that can be represented?	0123456789
9.19.19 -> 9.400+ 19.20+ 19= 3600 + 380+19 =	15.19.3 = 1
203-1 = 4000-1	-5.400+19.20+4
	= -2000 + 380 + 4
	= -1616

(a) Two 4-bit binary inputs, b1[3:0] and b2[3:0], are needs to be decoded into a single 2-hot output vector, twoh[15:0]. This 2-hot vector has up to two positions of 1's and the remainder are 0's. If b1 is the same as b2, then only a single 1 is asserted. Design this logic. You may use 2:4 Decoders as building blocks and any number of AND, OR, or INV gates. Try to

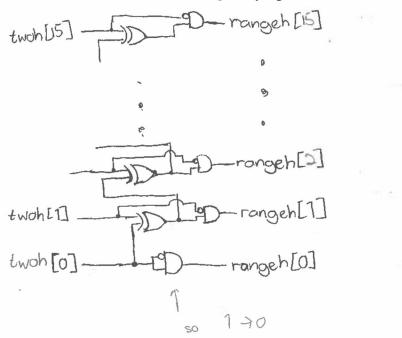




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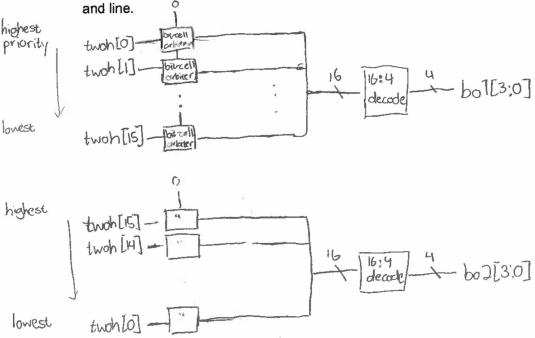
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(b) Next, you are tasked to code the *twoh[15:0]* signals into a vector that indicates a range, similar to a thermometer code. The 16-bit input, *twoh[15:0]*, produces 16-bit outputs, *rangeh[15:0]*, whereby the positions in between the 2 asserted signals of the *twoh[15:0]* are all 1's. For instance, with b1[3:0]=4'b0110 and b2[3:0]=4'b0010, rangeh[15:0]=16b'0000_0000_0011_1110. You may use any building block we have covered in lecture as well as any number of AND, OR, or INV gates. Again you can describe connections to avoid drawing every signal and line.



XOR acts like a toggle

(c) Finally, encode inverse the function of (a) by accepting the *twoh[15:0]* signal as input and output 2 binary values *bo1[3:0]* and *bo2[3:0]* to indicate the binary position of the two hot bits. You may use any building block we have covered in lecture as well as any number of AND, OR, or INV gates. Again you can describe connections to avoid drawing every signal and line.



top half-let most significant 1 from right pass through, then decode bottom-let least sig. 1 from left pass through, then decode

