

UCLA
Electrical and Computer Department
ECE 100
Second Midterm

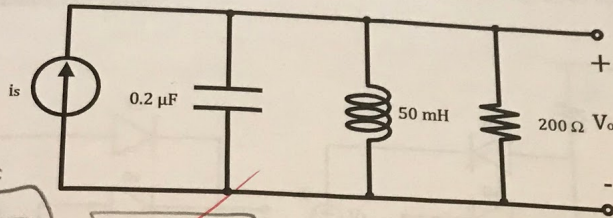
Last Name: Roberts

First Name: Grant

UID: 405 150 937

Problems	Points
1	17 /20
2	9 /14
3	17 /20
4	0 /6
5	12 /20
6	20 /20
Total	75 /100

1. For the circuit shown below, find and plot magnitude and phase of $\frac{V_o}{I_s}$.
Specify the resonance frequency, -3 dB frequencies and also the BW.



Resonance frequency
 $\omega_0 = \frac{1}{\sqrt{LC}}$
 $= \frac{1}{\sqrt{50 \times 10^{-3} \times 0.2 \times 10^{-6}}}$

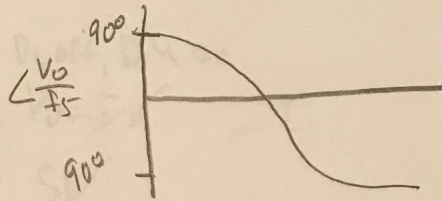
$f_0 = \frac{1}{2\pi \sqrt{50 \times 10^{-3} \times (0.2 \times 10^{-6})}} = 15915 \text{ Hz}$

$Z_{eq} = \frac{1}{\frac{1}{R} + j(\omega C - \frac{1}{\omega L})}$ $V_o = I_s \cdot Z_{eq}$

$|\frac{V_o}{I_s}| = \frac{1}{\sqrt{\frac{1}{R^2} + (\omega C - \frac{1}{\omega L})^2}} \rightarrow -1$

$\angle \frac{V_o}{I_s} = \angle \text{num} - \angle \text{den}$
 $= 0 - \angle \text{den}$
 $= -\arctan\left(\frac{\omega C - \frac{1}{\omega L}}{R}\right)$

$= -\arctan\left(\frac{\omega C - \frac{1}{\omega L}}{R}\right) \rightarrow$

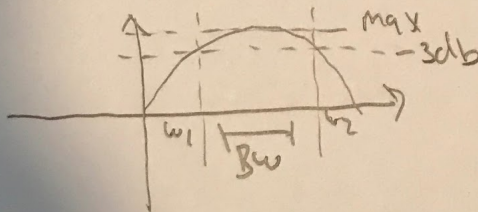


-3dB frequencies = half-way cutoff

$|H_{\omega}| = \frac{1}{\sqrt{2}} \cdot \text{max}$

$\omega_{1,2} = \frac{1}{2RC} \pm \sqrt{\left(\frac{1}{2RC}\right)^2 + \frac{1}{LC}}$

$\text{max} = \frac{1}{R}$

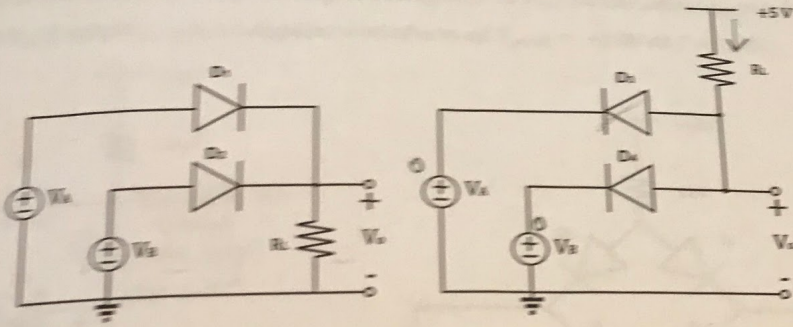


$Q = 2\pi f_0 CR$

$BW = \frac{f_0}{Q}$

$BW = \frac{1}{2\pi CR} = \frac{1}{2\pi(0.2 \times 10^{-6})(200)}$

2. The circuits shown below is a kind of logic circuit. V_A and V_B independently can be $0V$ or $+5V$. There are 4 possible combinations: 1) $V_A = 0V, V_B = 0V$; 2) $V_A = 0V, V_B = +5V$; 3) $V_A = +5V, V_B = 0V$; 4) $V_A = +5V, V_B = +5V$. Show the output voltages for each case, assuming diodes are ideal.



$V_A = 0, V_B = 0$
 $V_o = 0$, by inspection.

$V_A = +5, V_B = 0$
 $V_o = 5V$

$V_A = 0, V_B = +5$
 $V_o = 5V$

① $V_o = 0$
 $V_B = 0$

② D_4 off; D_3 on
 $V_o = 5V$

③ D_3 off; D_4 on
 $V_o = 5V$ -5

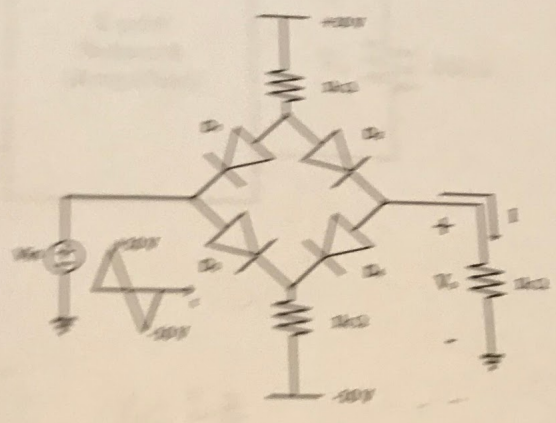
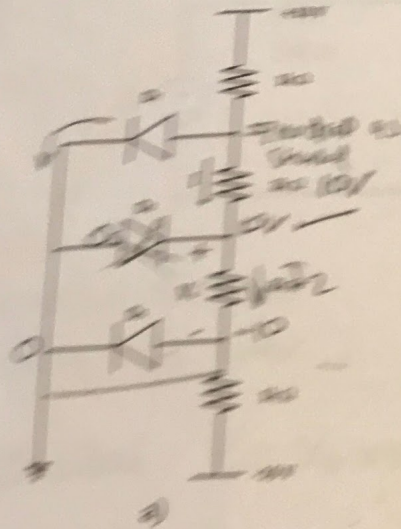
④ $5V$

c Gates
 circuit diagram
 its equivalent
 (itches) if the
 input is low.
 circuit diagram
) gate. [Hint:
 rved by an in
 circuit diag
 ND gate. b
 en and clos
 : high. c. R

V_{DS} ta
 14V.
 e hav
 igure
 han
 char
 Th
 ar
 v

2. Accounting/ideal diodes:

- a) find the value of I and V_o for circuit (a).
- b) for circuit (b):
 - 1) identify the output voltage vs input voltages (V_o vs V_{in}) on the same xy plane.
 - 2) identify the output current I vs input voltages (I vs V_{in}) on the same xy plane.
 - 3) plot $I(t)$ with V_{in} as a triangular waveform of $V_{peak} = +10V$ at $f = 10kHz$.

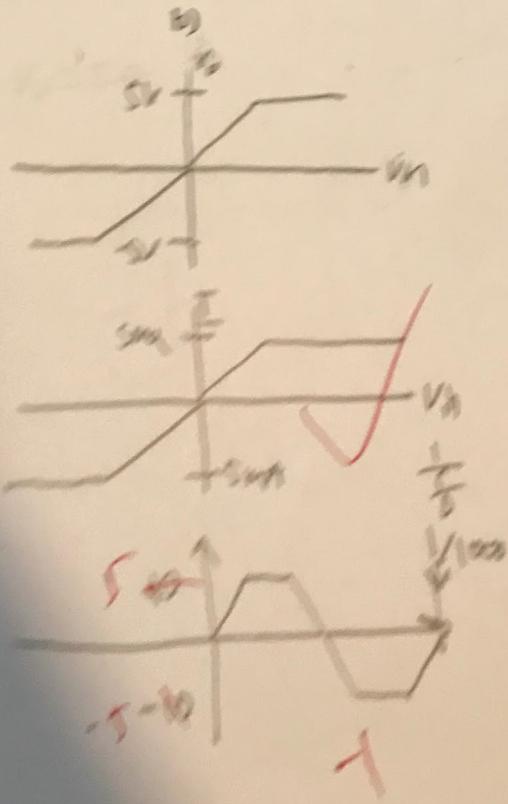


When D_1, D_2 are ON, V_o is off

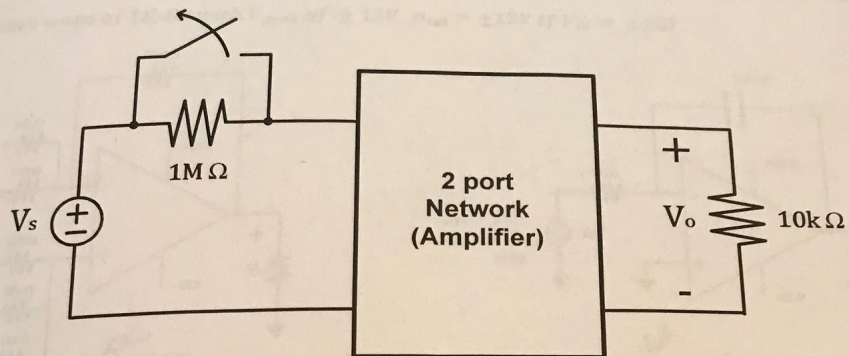
V_o is forced by V_{in} by D_1, D_2 ON.

$$\frac{10 - 10}{20} = \frac{20}{20} \Rightarrow I = 0$$

$$V_o = \frac{20}{20} = 1V$$



4. The output voltage of the two port network with the switch closed is 100mV. If we open the switch, the output drops to 50mV. (A way they measure the input resistance of an amplifier). Find the input impedance of the network.



Switch closed: $V_o = 100\text{mV}$

Open switch: $V_o = 50\text{mV}$

Switch closed: $V_o = V_s \cdot 10\text{k}\Omega$

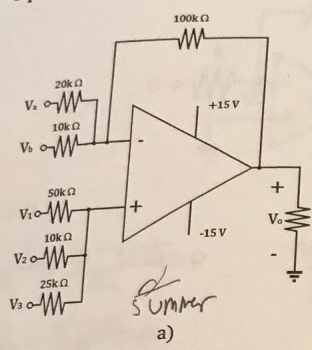
$V_o = I \cdot Z$

Switch open: $I =$

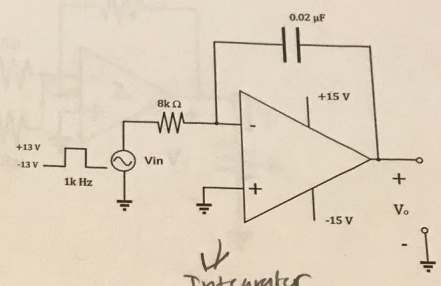
$V_o = I \cdot Z$

-6

5. a) Find the output voltages with respect to the input voltage $v_o(v_a, v_b, v_1, v_2, v_3)$ for amplifier a).
 b) For b), find the expression of $V_o(t)$ in terms of $V_{in}(t)$ and also sketch $V_o(t)$ when $V_{in}(t)$ is a square wave of 1K Hz with V_{peak} of $\pm 13V$. $v_{sat} = \pm 13V$ if $V_{cc} = \pm 15V$.



Summer a)



Integrator b)

$V_+ = V_-$

$$\frac{V_+ - V_1}{20k} + \frac{V_+ - V_2}{10k} + \frac{V_+ - V_3}{25k} = 0$$

$$\frac{V_+}{50k} + \frac{V_2}{10k} + \frac{V_3}{25k} = \frac{V_+}{50k} + \frac{V_1}{10k} + \frac{V_1}{25k}$$

$$= V_1 + 5V_2 + 2V_3 = V_1 + 5V_+ + 2V_+$$

$$= 8V_+ \Rightarrow V_+ = \frac{1}{8} V_1 + \frac{5}{8} V_2 + \frac{1}{4} V_3$$

$$\frac{V_+ - V_a}{20k} + \frac{V_+ - V_b}{10k} + \frac{V_+ - V_0}{100k} = 0$$

$$\frac{V_+ - V_a}{20k} + \frac{V_+ - V_b}{10k} + \frac{V_+}{100k} = \frac{V_0}{100k}$$

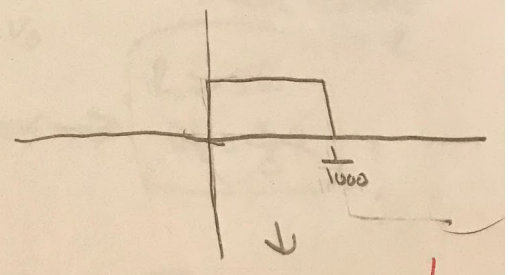
$$5(V_+ - V_a) + 10(V_+ - V_b) + V_+ = V_0$$

plug
 format of the to plug
 in and get final value

-2

$$V_o(t) = -\frac{1}{RC} \int v_{in}(t) dt$$

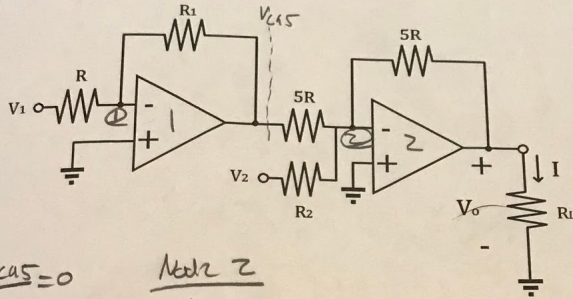
$$= -\frac{1}{(8 \times 10^{-3})(0.02 \times 10^{-6})} \int v_{in}(t) dt$$



$f = 1K Hz$
 $T = \frac{1}{f}$

-6

6. a) Find R_1 and R_2 in terms of R if we want to have $V_0 = 3V_2 - 5V_1$.
 b) If $V_1 = 2V, V_2 = 1V$ and $R_L = 1k\Omega$, find the current I if R_1 and R_2 equal to the values found in a).



① $V_+ = V_- = 0$

Node 1: $\frac{0 - V_1}{R} + \frac{0 - V_{out1}}{R_1} = 0$

$-\frac{V_1}{R} - \frac{V_{out1}}{R_1} = 0$

$-\frac{V_1}{R} = \frac{V_{out1}}{R_1}$

$-\left(\frac{R_1}{R}\right)V_1 = V_{out1}$

Node 2

$V_+ = V_- = 0$

$\frac{0 - V_{out1}}{5R} + \frac{0 - V_2}{R_2} + \frac{0 - V_0}{5R} = 0$

$-\frac{V_{out1}}{5R} - \frac{V_2}{R_2} = \frac{V_0}{5R}$

$-V_{out1} - \frac{5RV_2}{R_2} = V_0$

$\frac{R_1}{R}V_1 - \frac{5RV_2}{R_2} = V_0$

Think R_2 may have got directions mixed up resulting in negative resistance for R_2

$R_1 = -5R$
 $R_2 = -\frac{5}{3}R$

② ~~V_+~~

$I = \frac{V_0}{R_L}$

$V_2 = 1V$

$V_1 = 2V$

$R_L = 1k$

$I = \frac{(3(1) - 5(2))}{1000}$

$= \frac{3 - 10}{1000} = \frac{-7}{1000} = -7mA$

$-\frac{5R}{R}V_1$

$= -5V_1 - \left(-\frac{3}{5R} \cdot 5RV_2\right) -$

$= -5V_1 + 3V_2 = V_0$