

University of California

Los Angeles

Computer Science Department

CSM51A/EEM16 Midterm Exam #1
Winter Quarter 2019
February 6th 2019

This is a closed book exam. Absolutely nothing is permitted except pen, pencil and eraser to write your solutions. Any academic dishonesty will be prosecuted to the full extent permissible by university regulations.

Time allowed 100 minutes.

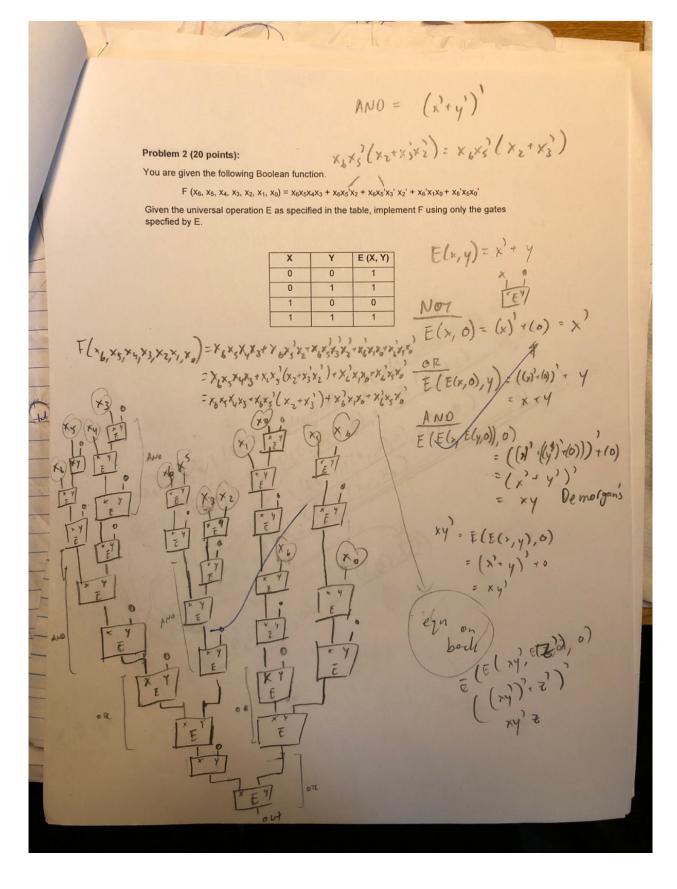
Points
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Problem 1 (20 points):

Given the network below, find the critical path and calcuate critical path delay. Assume the values of (x_3, x_2, x_1, x_0) are initially (1,1,1,1) and they change to (0,0,1,0) in the next clock cycle. Now, choose a gate on the citical path which maximally decreases overall delay when the gate decreases its delay by 20%. Finally, find the critical path in the new network and its length.

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Continued from Front Problem 1 (Extra Page): 70% deven & I > 0.87 = 0,8 (8.261) New Critical Posth calculations ADGIS & ADI = 0.1870 40,224,0 40,208 8 0.11978 CAted path is 87:11 AM Grand My CEH 3 = 0.605 From My CEH 3 = 0.568 1 x 7 A -> D -> 6->]-> 20 X -> A -> D-> J -> & but time delay is now 0.6198



Problem 2 (Extra Page): Tull egy

Problem 3 (20 points):

For a K-map, M denotes the number of prime implicants of the K-map, and N denotes the number of essential prime implicants of the K-map. Draw a 4×4 K-map that has the largest value of P=M-N among all the 4×4 K-maps.

For example, in the following 4×4 K-map, M=3, N=2, P=M-N=1

ing 4×4 K-map, M=3, N=2, P=M-N=1. X ₀	VA GOS
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Problem 4 (20 points):

Given an input stream X, we want to recognize interchangably patterns A and B. We recognize A first, then B, followed by A again, then B again and so on.

For example,

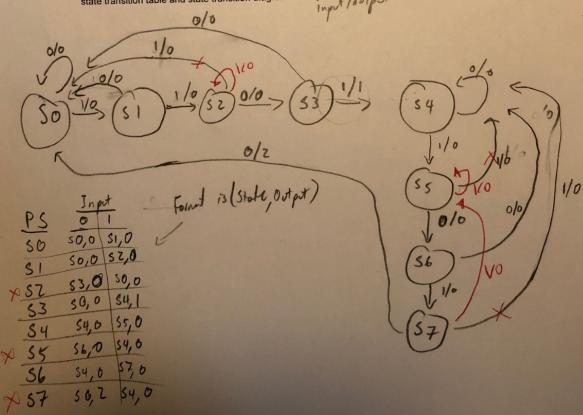
1. Assume X= 01011010010101, A=101 and B=001.

We will first recognize A, then look for B. Please note that we ignore the second '101' (A) in X and we only search for B once we have found A. After finding B, we again search for A.

2. Assume that we have X= 1011, A = 101 and B=011

We recognize A, but we do not recognize B as we only start looking for B once we have detected A. In other words, A and B do not overlap.

Now, you are given any input stream X. Design a finite state machine such that the system outputs 1 when it recognizes pattern A= '1101' and outputs 2 when it recognizes pattern B= '1010' after recognizing pattern A. In all other cases the machine should output 0. Show the state transition table and state transition diagram.



0-234567890112346 Problem 5 (20 points): Perform the following conversions: a) (B2451)₁₆ -> (x)₈ b) (354)₇-> (y)₅ 0010 0100 0101 001 010 001 010 (BS421) ~> (SPS131)8 2 147 = 3(7²) +5(7¹) + 4(7°) (186),0 110 010 010 001 010 001 1011 0010 0100 0101 0001 B (1221) 5 = 1×53+2×52+2×5+1×50 (1221) (354