

# [CS M51A FALL 18] MIDTERM EXAM

Date: 10/30/18

- The midterm is closed book, and up to 4 sheets (= 8 pages) of summary notes are allowed. You can use a calculator but not smart phones.
- For multiple choice questions, wrong answers may have a negative score value so choose carefully. It is possible that questions can have multiple answers.
- Please show all your work and write legibly, otherwise no partial credit will be given.
- This should strictly be your own work; any form of collaboration will be penalized.

Name : \_\_\_\_\_

Student ID : \_\_\_\_\_

Problem	Points	Score
1	10	10
2	15	15
3	15	9
4	15	15
5	20	20
6	10	4
7	<del>15</del>	<del>2</del>
Total	100 94	75

**Problem 1 (10 points)**

1. (4 points) How many bits are required to encode a color spectrum capable of supporting 16 million colors using:

a. Decimal digits in BCD

16,000,000

4 bits per digit

32 bits

8 digit places

b. Hexadecimal representation

$$16^6 = 16,777,216 \text{ possible \#s}$$

So only 6 bits needed

Which representation is more efficient? Why?

Hexadecimal is more efficient since it can represent more numbers with less bits needed.

2. (6 points) Fill in the missing entries in the table.

Radix	Digit vector $\underline{x}$	Value $x$ in decimal
16	(5, 1, 7)	1303
8	(5, 1, 7)	335
7	4532	1640

$$517_{16} \rightarrow x_{10}$$

$$5 \times 16^2 + 1 \times 16^1 + 7 \times 16^0 = 1303_{10}$$

$$517_8 \rightarrow x_{10}$$

$$5 \times 8^2 + 1 \times 8^1 + 7 \times 8^0 = 335$$

Handwritten conversion steps for 1640 to base 7:

$$\begin{array}{r} 234 \text{ R}2 \\ 7 \overline{)1640} \\ \underline{-1638} \\ 2 \\ 33 \text{ R}3 \\ 7 \overline{)234} \\ \underline{-231} \\ 43 \\ 43 \text{ R}5 \\ 7 \overline{)43} \\ \underline{-28} \\ 15 \\ 15 \text{ R}4 \\ 7 \overline{)15} \\ \underline{-14} \\ 1 \end{array}$$

em 2 (15 points)

$a + b = b + a$	$ab = ba$	Commutativity
$a + (bc) = (a + b)(a + c)$	$a(b + c) = (ab) + (ac)$	Distributivity
$a + (b + c) = (a + b) + c = a + b + c$	$a(bc) = (ab)c = abc$	Associativity
$a + a = a$	$aa = a$	Idempotency
$a + a' = 1$	$aa' = 0$	Complement
$1 + a = 1$	$0a = 0$	
$0 + a = a$	$1a = a$	Identity
$(a')' = a$		Involution
$a + ab = a$	$a(a + b) = a$	Absorption
$a + a'b = a + b$	$a(a' + b) = ab$	Simplification
$(a + b)' = a'b'$	$(ab)' = a' + b'$	DeMorgan's law

Given  $E(a, b, c, d) = (ab + c)'(ac + (b' + c' + a'cd)') + a((b + c)(b + d) + c)'$ , which of the following represents the same function as  $E(a, b, c, d)$ ? Show all your work.

1.  $a + b + c + d'$
2.  $a' + b + c$
3.  $b + c' + d$
4.  $a'b'c'd$
5.  $ab'c'$
6.  $b'cd'$

$$\begin{aligned}
 & (ab + c)'(ac + (b' + c' + a'cd)') + a((b + c)(b + d) + c)' \\
 & ((ab)' \cdot c')(ac + (bc(a + c' + d))) + a((b + c)'(b + d)' \cdot c') \\
 & [(a' + b')c'] [ac + abc + bcd] + [a(b'c' + b'd'c')] \\
 & [a'c' + b'c'] [ac + abc + bcd] + ab'c' + ab'd'c' \\
 & 0 + 0 + 0 + 0 + 0 + 0 + ab'c' + ab'd'c' \\
 & ab'c' + ab'd'c' \\
 & ab'c'(1 + d') \\
 & ab'c'
 \end{aligned}$$

DeMorgan's  
Distributive, Complement  
Distributive  
Distributive, Complement  
Distributive  
Complement

$ab'c'$  #5

**Problem 3 (15 points)**

Show if the gate G, described by  $G(x, y, z) = \text{one-set}\{3, 4, 6, 7\}$ , can implement NOT and AND gates. Assume that 0 and 1 are available. If it can, then use G gates to implement the following expression and show the corresponding network of G gates

$$E(a, b, c) = (a + b')(b + c')$$

$$G(x, y, z) = x'yz + xy'z + xyz' + xyz = x'yz + xy'z + xz + xy$$

	y'z		yz	
x	00	01	11	10
0	0	1	1	0
1	1	0	1	1
	y		z	

$$G(x, y, z) = xz' + yz$$

$$G(1, 0, z) = z' + 0 = z' \quad \text{NOT Gate } \checkmark$$

$$G(0, y, z) = 0 + yz = yz \quad \text{AND Gate } \checkmark$$

Yes, it can implement  
{AND, NOT}

$$E(a, b, c) = (a + b')(b + c')$$

$$c' \Rightarrow G(1, 0, c) = c' \quad \text{OR } \Rightarrow \text{NAND - NAND}$$

$$b' \Rightarrow G(1, 0, b) = b'$$

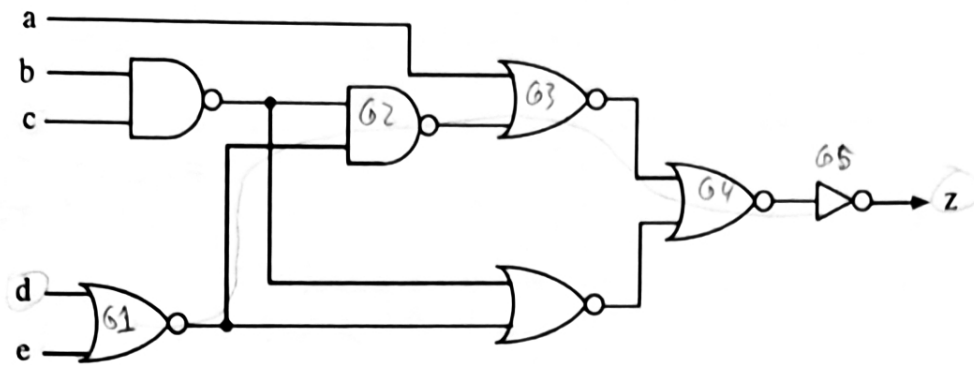
$$G(1, 0, G(0, a, G(1, 0, b))) \Rightarrow [ab']'$$

$$G(1, 0, G(0, \dots))$$

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em 4 (15 points)

With the help of the table below, determine the low to high propagation delay  $t_{pLH}(d, z)$  of the output  $z$  of the network shown below. Assume the network output has a load of 6.



Gate Type	Fan-in	Propagation Delays (ns)		Load Factor
		$t_{pLH}$	$t_{pHL}$	
NOT	1	$0.02 + 0.038L$	$0.05 + 0.017L$	1.0
NAND	2	$0.05 + 0.038L$	$0.08 + 0.027L$	1.0
NOR	2	$0.06 + 0.075L$	$0.07 + 0.016L$	1.0

G1-2NOR → G2-2NAND → G3-2NOR → G4-2NOR → G5-NOT  
 L=2                      L=1                      L=1                      L=1                      L=6

$$\begin{aligned}
 &0.06 + 0.075L & 0.08 + 0.027L & 0.06 + 0.075L & 0.07 + 0.016L & 0.02 + 0.038L \\
 = &0.21 & 0.107 & 0.135 & 0.086 & 0.248 \\
 = &\boxed{0.786 \text{ ns}} & & & & 
 \end{aligned}$$

### Problem 5 (20 points)

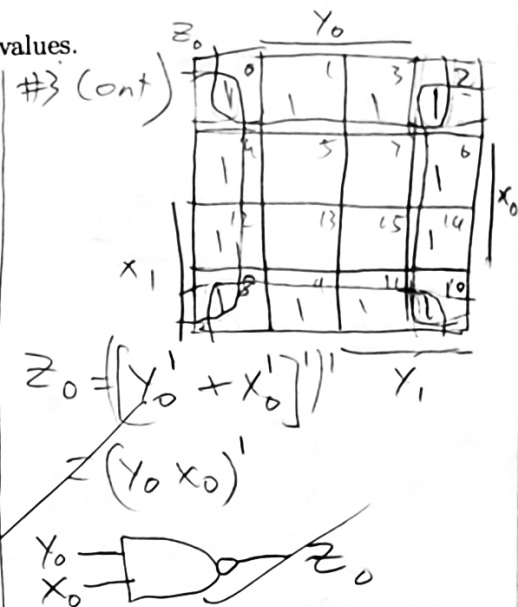
Obtain a two-level gate network of the following system.

Inputs:  $x, y \in \{0, 1, 2, 3\}$   
 Outputs:  $z \in \{0, 1, 2, 3\}$   
 Function:  $z = \{3xy + 1\} \text{ mod } 4$

1. (2 points) Complete the switching table using binary encoding for all values.

$x \backslash y$	0	1	2	3
0	1	1	1	1
1	1	0	3	2
2	1	3	1	3
3	1	2	3	0

	$x_1$	$x_0$	$y_1$	$y_0$	$z_1$	$z_0$
0	0	0	0	0	0	1
1	0	0	0	1	0	1
2	0	0	1	0	0	1
3	0	0	1	1	0	1
4	0	1	0	0	0	1
5	0	1	0	1	0	0
6	0	1	1	0	1	1
7	0	1	1	1	1	0
8	1	0	0	0	0	1
9	1	0	0	1	1	1
10	1	0	1	0	0	1
11	1	0	1	1	1	1
12	1	1	0	0	0	1
13	1	1	0	1	1	0
14	1	1	1	0	1	1
15	1	1	1	1	0	0



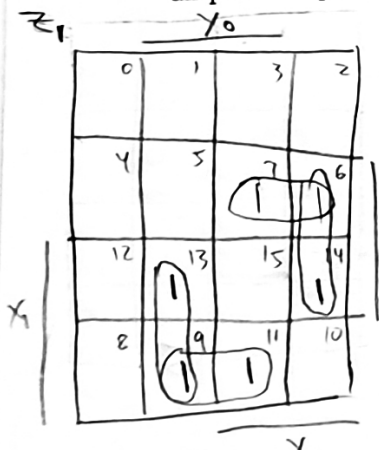
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2. (5 points) Show the switching expressions of  $z_1$  and  $z_0$  in sum of minterms form.

$$z_1 = x_1' x_0 y_1 y_0' + x_1' x_0 y_1 y_0 + x_1 x_0' y_1' y_0 + x_1 x_0' y_1 y_0 + x_1 x_0 y_1' y_0 + x_1 x_0 y_1 y_0'$$

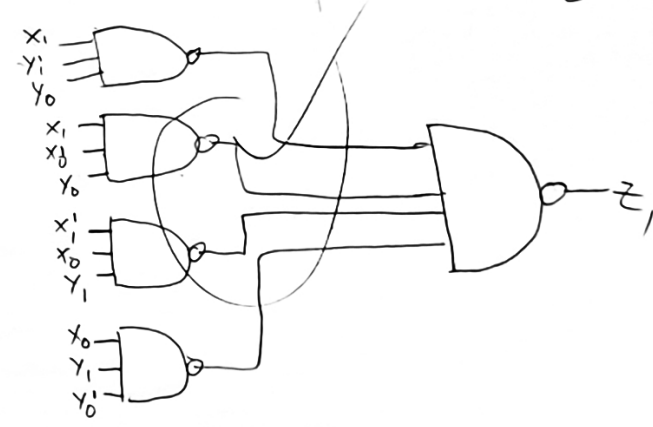
$$z_0 = x_1' x_0' y_1' y_0' + x_1' x_0' y_1' y_0 + x_1' x_0' y_1 y_0' + x_1' x_0' y_1 y_0 + x_1' x_0 y_1' y_0' + x_1' x_0 y_1' y_0 + x_1' x_0 y_1 y_0' + x_1' x_0 y_1 y_0 + x_1 x_0' y_1' y_0' + x_1 x_0' y_1' y_0 + x_1 x_0' y_1 y_0' + x_1 x_0' y_1 y_0 + x_1 x_0 y_1' y_0' + x_1 x_0 y_1' y_0 + x_1 x_0 y_1 y_0' + x_1 x_0 y_1 y_0$$

3. (8 points) Show the minimal sum of products expressions of  $z_1$  and  $z_0$ . In each case, show a K-map, indicate all prime implicants, and all essential prime implicants. Show NAND-NAND networks.



$$z_1 = x_1 y_1' y_0 + x_1 x_0' y_0 + x_1' x_0 y_1 + x_0 y_1 y_0'$$

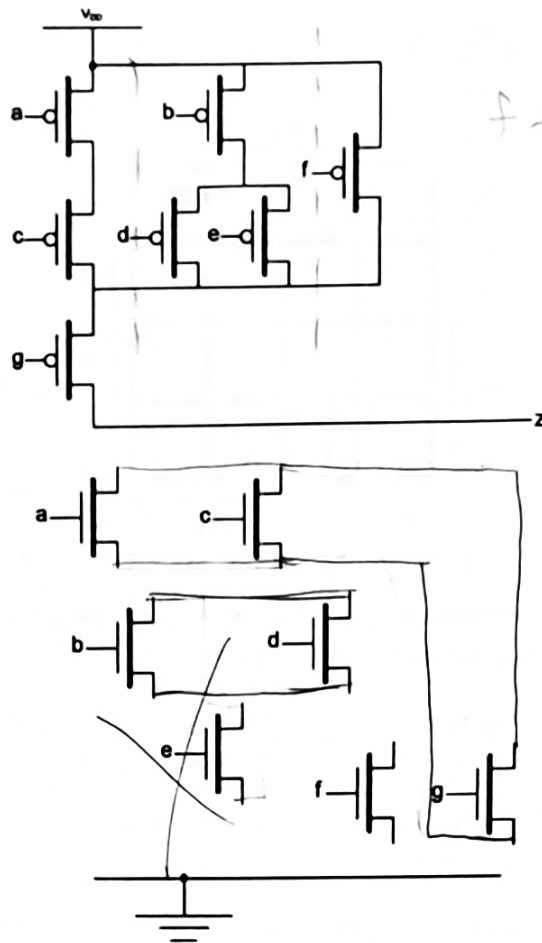
$$z_1 = [(x_1 y_1' y_0)' (x_1 x_0' y_0)' (x_1' x_0 y_1)' (x_0 y_1 y_0')']'$$



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em 6 (10 points)

are given the following partial CMOS network.



$$f + [b(d+e)] + acg$$

1. (5 points)

Write the expression for the pull-up network. From this, derive the expression for the pull-down network using switching algebra.

2. (5 points) Connect NMOS transistors to complete the circuit according to the pull-down expression. Please only add missing wires.

1)  $a'c'g$ ,  $b'(d'+e)$ ,  $f'$   $z = a'c'g + b'(d'+e) + f'$

$$z' = [a'c'g + b'd' + b'e' + f']' = (a'c'g)'(b'd')'(b'e) f = (a+c+g)(b+d)(b+e)f$$

$$z' = (a+c+g)(b+d)(b+e)f$$

**Problem 7 (15 points)**

For  $f(x_3, x_2, x_1, x_0) = (x_3 + x_2' + x_1 + x_0)(x_3 + x_2' + x_1' + x_0)(x_3' + x_2 + x_1 + x_0')(x_3' + x_2' + x_1 + x_0)(x_3' + x_1 + x_0')(x_3' + x_2' + x_1' + x_0)$

1. (2 points) Fill out the following K-map.

	$x_0$				
	0	1	2	3	
$x_3$	0	1	2	3	$x_2$
	4	5	6	7	
	8	9	10	11	
	12	13	14	15	
	$x_1$				

$x_2'x_0$   
 $x_3'x_1x_0'$   
 $x_3'x_2'x_1$

2. (4 points) Which of the given expressions are prime implicants of the function given above? Circle all that apply. Write down any prime implicants that are missing.

- (a)  $x_1$
- (b)  $x_3x_1$
- (c)  $x_3'x_2'$
- (d)  $x_3'x_1$
- (e)  $x_3'x_0$
- (f)  $x_2x_1$
- (g)  $x_2'x_0$
- (h)  $x_1x_0$
- (i)  $x_1x_0'$
- (j)  $x_3'x_2'x_1$
- (k)  $x_2x_1x_0$
- (l)  $x_3x_2x_1x_0$

$x_3'x_1x_0'$

3. (2 points) Write down the complete set of essential prime implicants.

$x_2'x_0$  /  $x_3'x_1x_0'$

4. (1 point) Write the minimal sum of products expression for  $f$ . Is it unique?

$x_2'x_0 + x_3'x_1x_0'$  , yes it is unique