

[CS M51A FALL 15] MIDTERM EXAM

Date: 11/3/15

20

- The midterm is closed books and notes. Tablets and smartphone are not allowed.
- You can use calculators and have up to 2 sheets (= 4 pages) of summary notes.
- Please show all your work and write legibly, otherwise no partial credit will be given.
- This should strictly be your own work; any form of collaboration will be penalized.

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Problem	Points	Score
1	15	15
2	15	15
3	10	8
4	15	15
5	10	10
6	15	15
7	20	18
Total	100	96

Problem 1 (15 points)

1. (6 points) Given the following simplification of a boolean expression, identify all right and wrong steps and briefly explain what is wrong for each error.
(For example, (10) \rightarrow (11) wrong application of the Identity rule, (11) \rightarrow (12) correct)

$$\begin{aligned} E_1(w, x, y, z) &= (((w + x + x'y')y + z)' + wx' + y')' & (1) \\ &= ((w + x + x'y')'y'z' + wx' + y')' & (2) \\ &= ((w + x + x'y')'y'z' + (w + y')(x' + y'))' & (3) \\ &= ((w + x' + y')'y'z' + (w + y')(x' + y'))' & (4) \\ &= (w'xyy'z' + (w + y')(x' + y'))' & (5) \\ &= (0 + (w + y')(x' + y'))' & (6) \\ &= wy + xy & (7) \end{aligned}$$

(1) \rightarrow (2) Wrong application of De Morgan's Law ($\left(\begin{smallmatrix} \text{should be} \\ [(w+x+x'y')y]'z' \end{smallmatrix} \right)$)

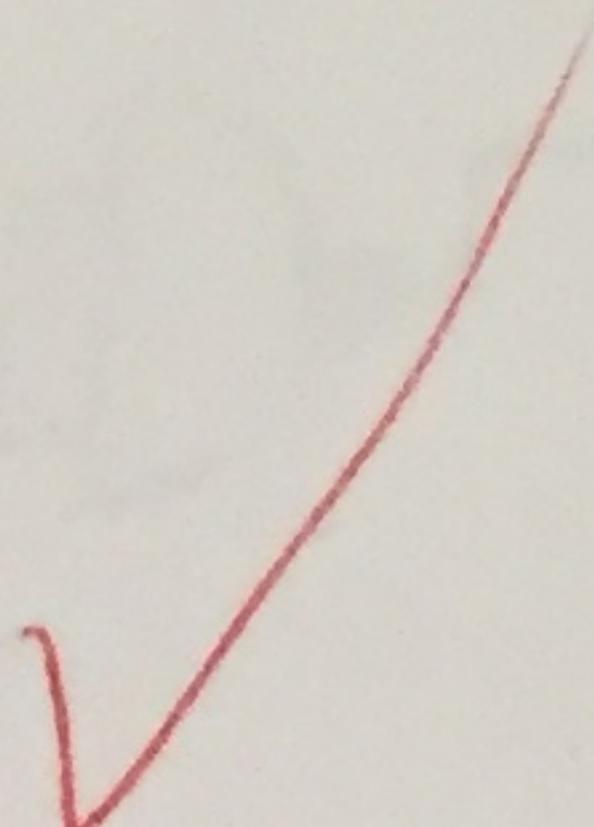
(2) \rightarrow (3) Correct

(3) \rightarrow (4) Wrong application of Simplification rule ($\left(\begin{smallmatrix} \text{should be} \\ (w+x+y') \end{smallmatrix} \right)$)

(4) \rightarrow (5) Correct

(5) \rightarrow (6) Correct

(6) \rightarrow (7) Wrong application of De Morgan's Law
 $\left(\begin{smallmatrix} \text{should be} \\ w'y + xy \end{smallmatrix} \right)$



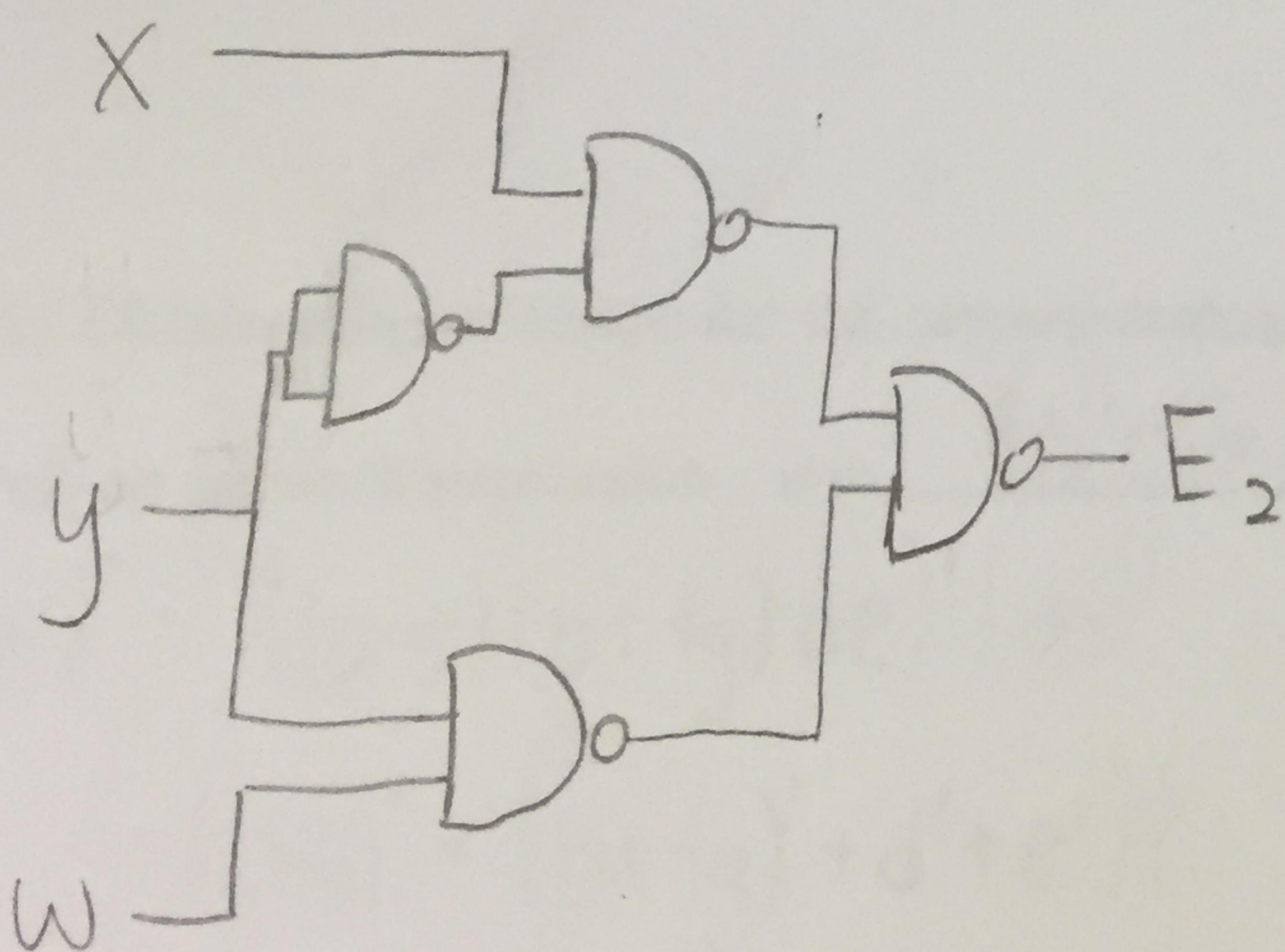
2. (5 points) Obtain the minimal sum of products form for $E_2(w, x, y, z)$ using the identities of Boolean algebra. Show all the steps in your derivation.

$$E_2 = xy' + xzw + yw$$

$$\begin{aligned}
 E_2 &= xy' + xzw + yw \\
 &= xy' + xzw'y + xzw'y' + yw \\
 &= xy' + xy'zw + yw + ywzx \\
 &= xy'(1 + zw) + yw(1 + xz) \\
 &= \boxed{xy' + yw}
 \end{aligned}$$

✓

3. (4 points) Using the expression obtained for E_2 from the previous step, obtain the NAND network that uses ONLY NAND gates. Inverted inputs are not available, and no constant inputs are allowed.

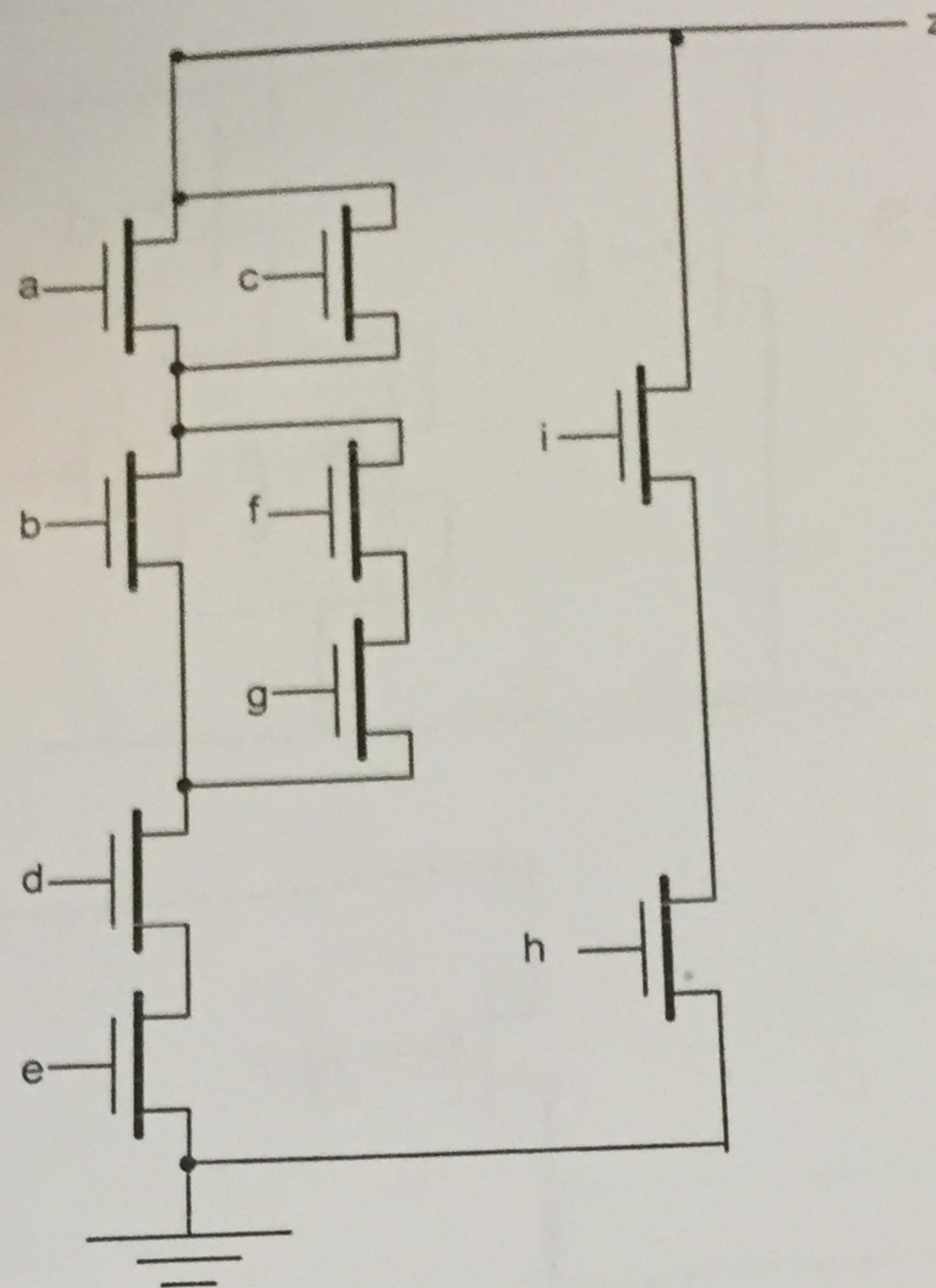


$$\begin{aligned}
 &\left((x(yy'))' (yw)' \right)' \\
 &= (x(yy'))' + (yw)' \\
 &= (x(y'+y'))' + (yw)' \\
 &= xy' + yw
 \end{aligned}$$

✓

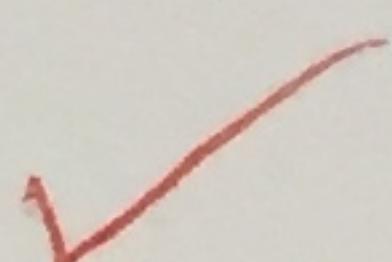
Problem 2 (15 points)

The following pull-down network is part of a complex CMOS gate that we want to implement.



1. (8 points) (a) Write the expression for the pull-down network.

Pull-down network expression : $z' = \overline{(a+c)(b+fg)de} + ih$



- (b) Obtain the expression for the corresponding pull-up network.

Pull-up network expression : $z = \overline{(a'c' + b'(f'+g') + d'+e')}(i'+h')$

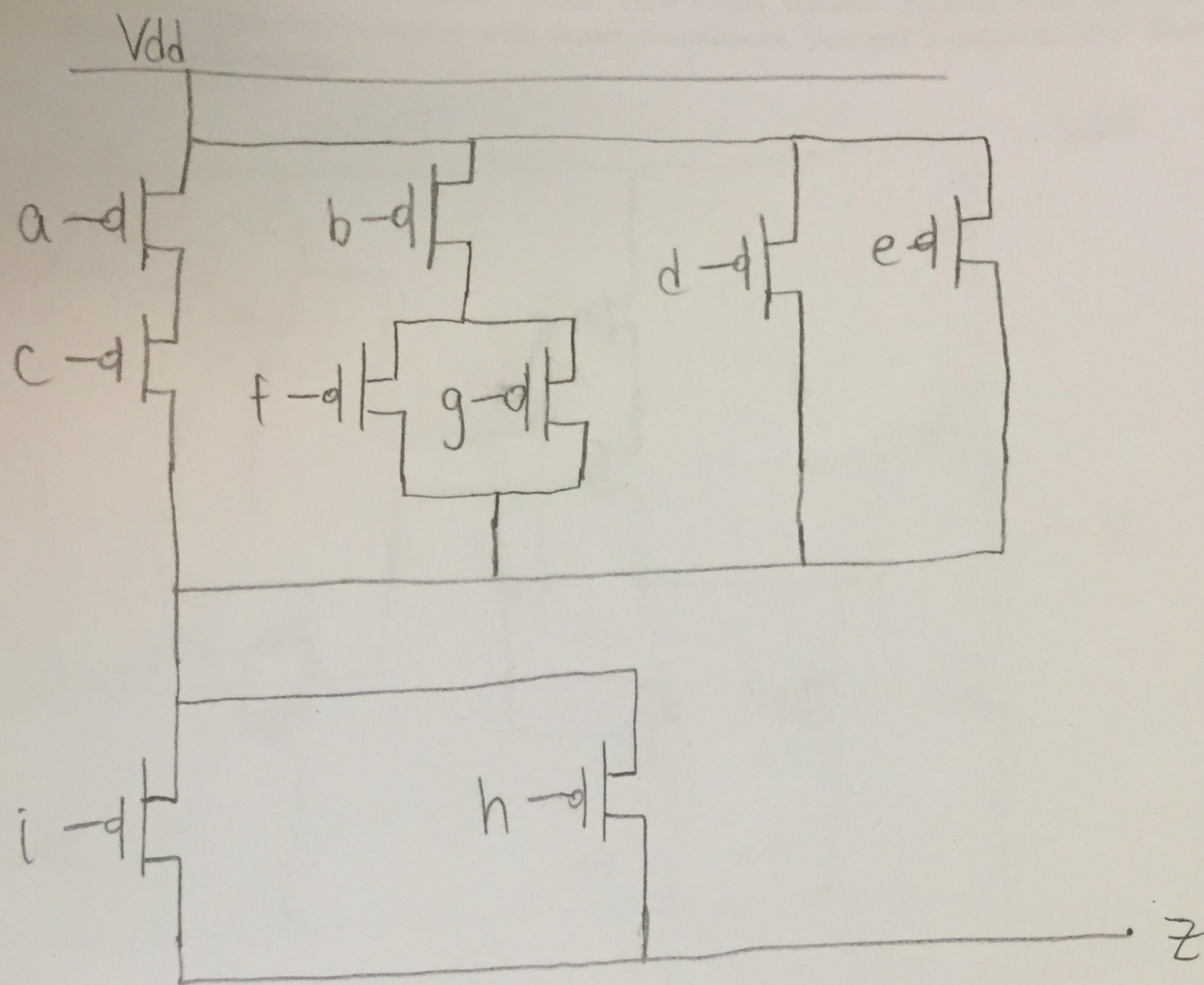
$$(z')' = ((a+c)(b+fg)de)'(ih)'$$

$$= ((a+c)' + (b+fg)' + d'+e')(i'+h')$$

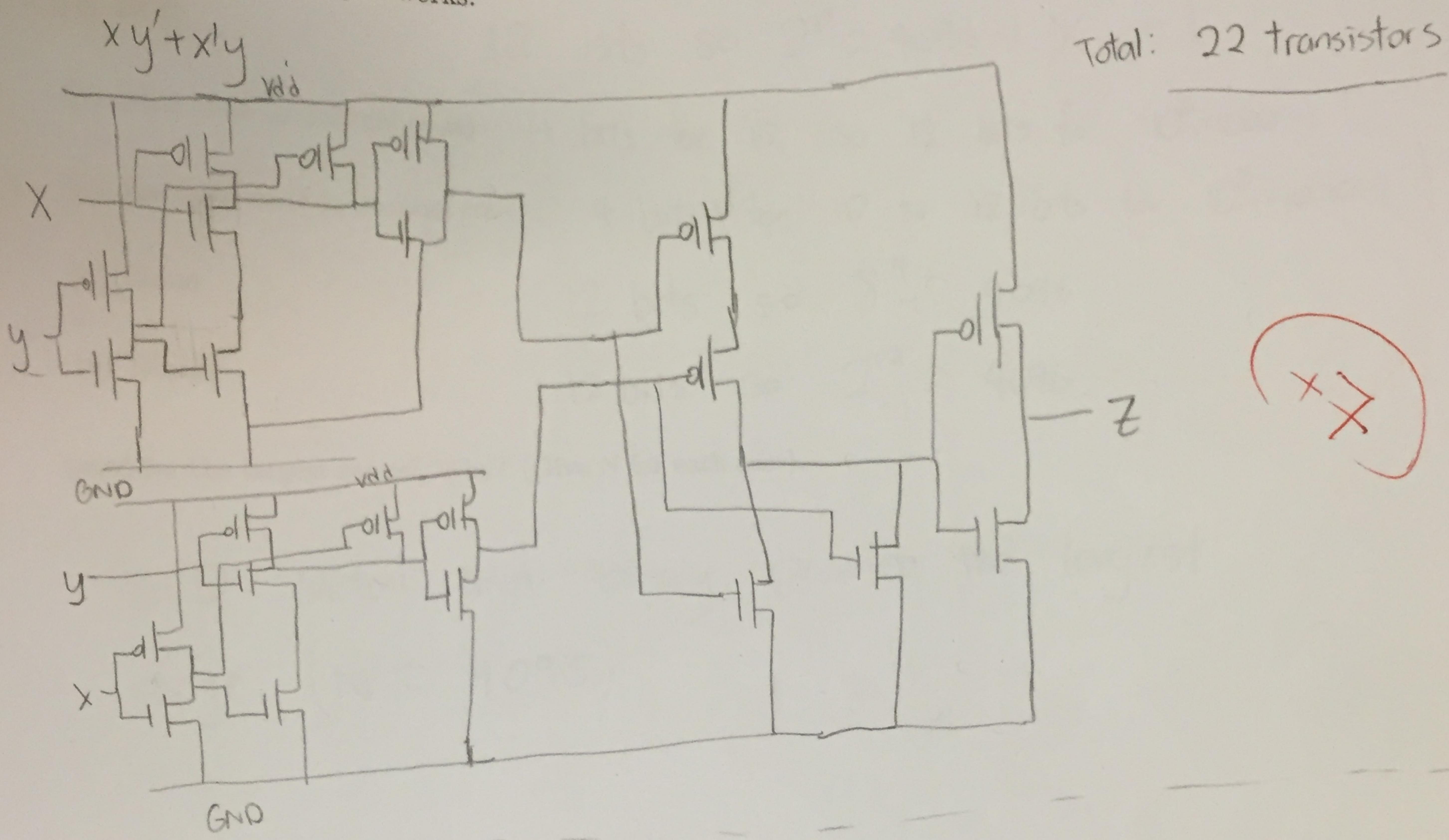
$$= (a'c' + b'(f'+g') + d'+e')(i'+h')$$



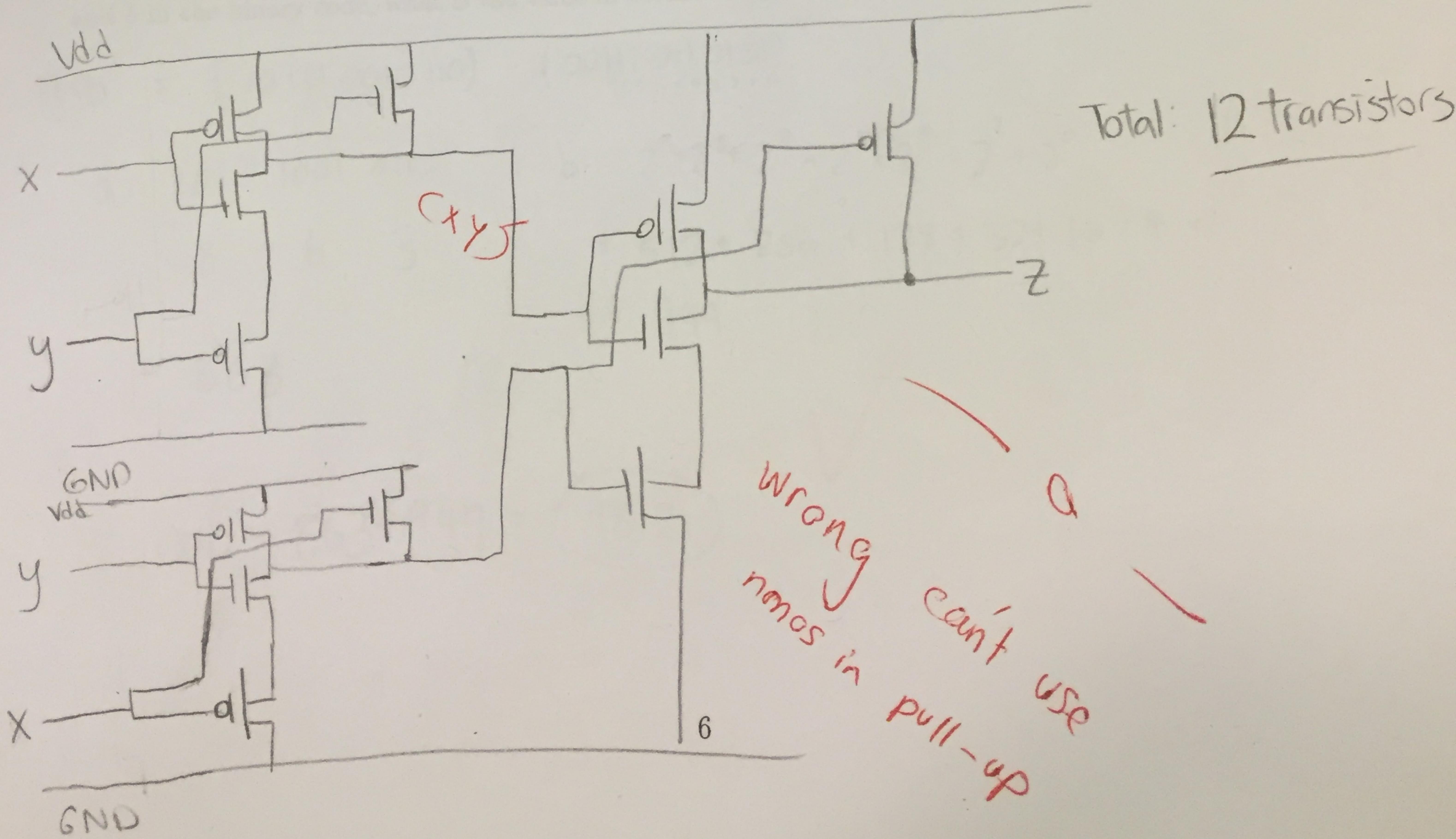
(c) Draw the pull-up network.



2. (7 points) Draw a CMOS network that implements $f(x,y) = xy' + x'y$ (2-input XOR). x' and y' are not available as inputs. Do not use transmission gates. How many transistors does your solution have?
 OPTIONAL: If you find another solution with fewer transistors, you get 3 extra points. Show your reduced design and explain why it works.



Better Solution: works because we don't use double NOT's
 exploit complementary logic



Problem 3 (10 points)

8

1. (5 points) A 12-bit vector represents a set of positive integers $\{0, \dots, N\}$. Which of the following coding alternatives

(a) BCD

$$12 \text{ bits so } 2^{12} = 4096 - 1 \quad X - 1$$

(b) 2421 code (a decimal code) 4 bits for 10, so 12 bits for $10^3 - 1 = 1000 - 1$

(c) Excess-3 code (a decimal code) 4 bits for 10 so 12 bits for $10^3 - 1 = 1000 - 1$

(d) Octal

$$12 \text{ bits so } 8^4 - 1 \approx 4096 - 1$$

(e) Binary

$$12 \text{ bits so } 2^{12} - 1 \approx 4096 - 1$$

provides the largest range? Why? (Give N for each case).

BCD Octal and binary provide the largest range ($N = 4095$)

2. (5 points) Let $a = (101110010110)$ and $b = (001110110101)$. If a represents a number in the Excess-3 code and b in the binary code, what is the value in decimal of their sum $a + b$? Show all your work.

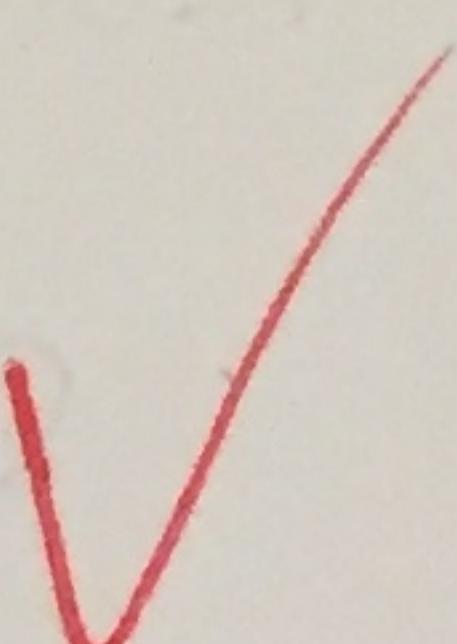
$$a+b = (101110010110) + (001110110101)$$

$$\begin{array}{r} 101110010110 \\ 001110110101 \\ \hline 101110010110 \\ 8 \quad 6 \quad 3 \end{array}$$

$$b: 2^9 + 2^8 + 2^7 + 2^5 + 2^4 + 2^2 + 2^0$$

$$\begin{aligned} &= 512 + 256 + 128 + 32 + 16 + 4 + 1 \\ &= 949 \end{aligned}$$

$$= 863$$



$$a+b = 863 + 949 = \boxed{1812}$$

Problem 4 (15 points)

15

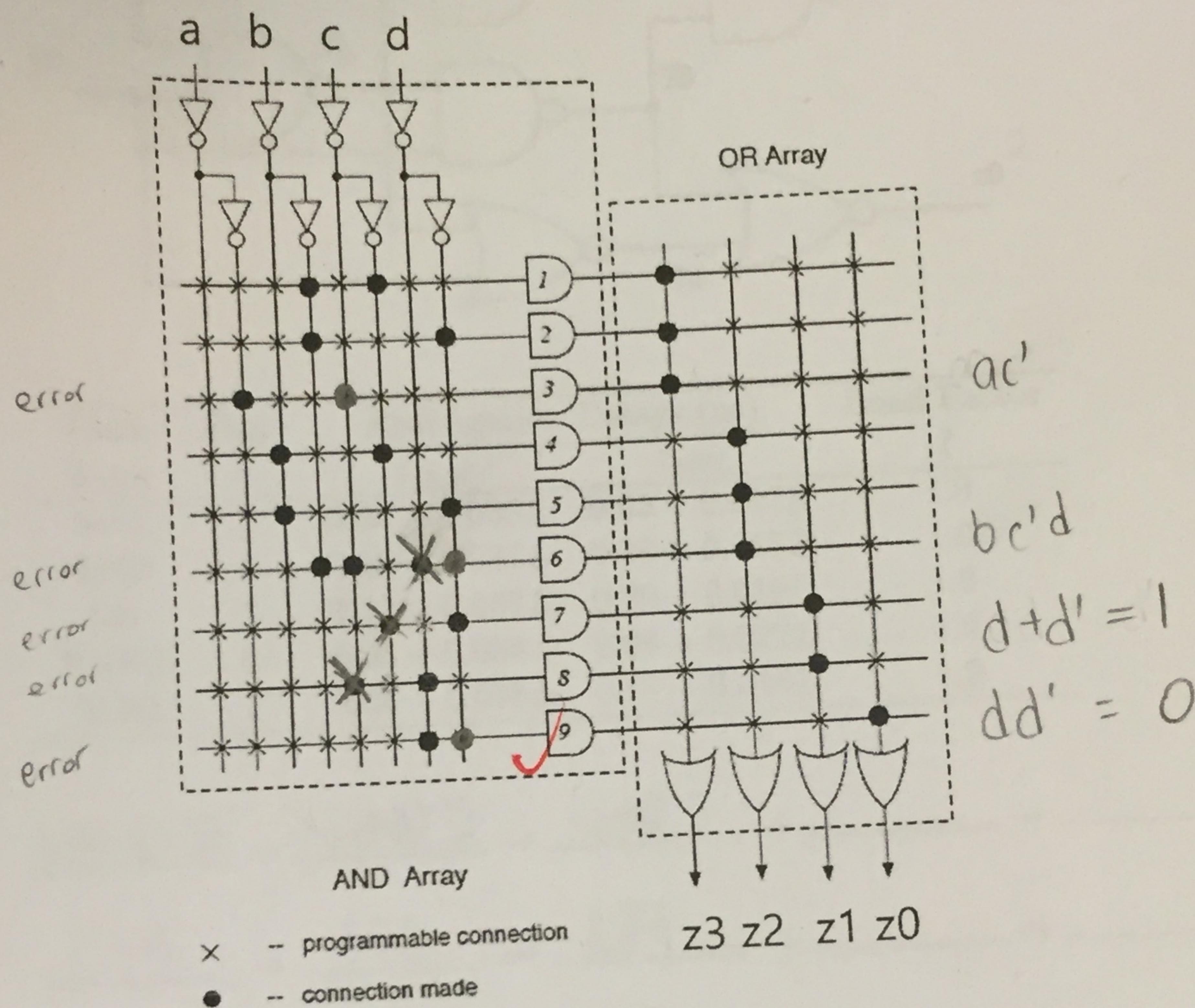
We would like to verify that the PLA implementation shown here implements the following switching functions:

$$z_3 = bc + bd + ac'$$

$$z_2 = b'c + b'd + bc'd$$

$$z_1 = 1$$

$$z_0 = 0$$



1. (7 points) Analyze the PLA shown above and show the output expressions.

$$z_3 = bc + bd + a$$

$$z_2 = b'c + b'd + bc'd$$

$$z_1 = cd + c'd'$$

$$z_0 = d'$$

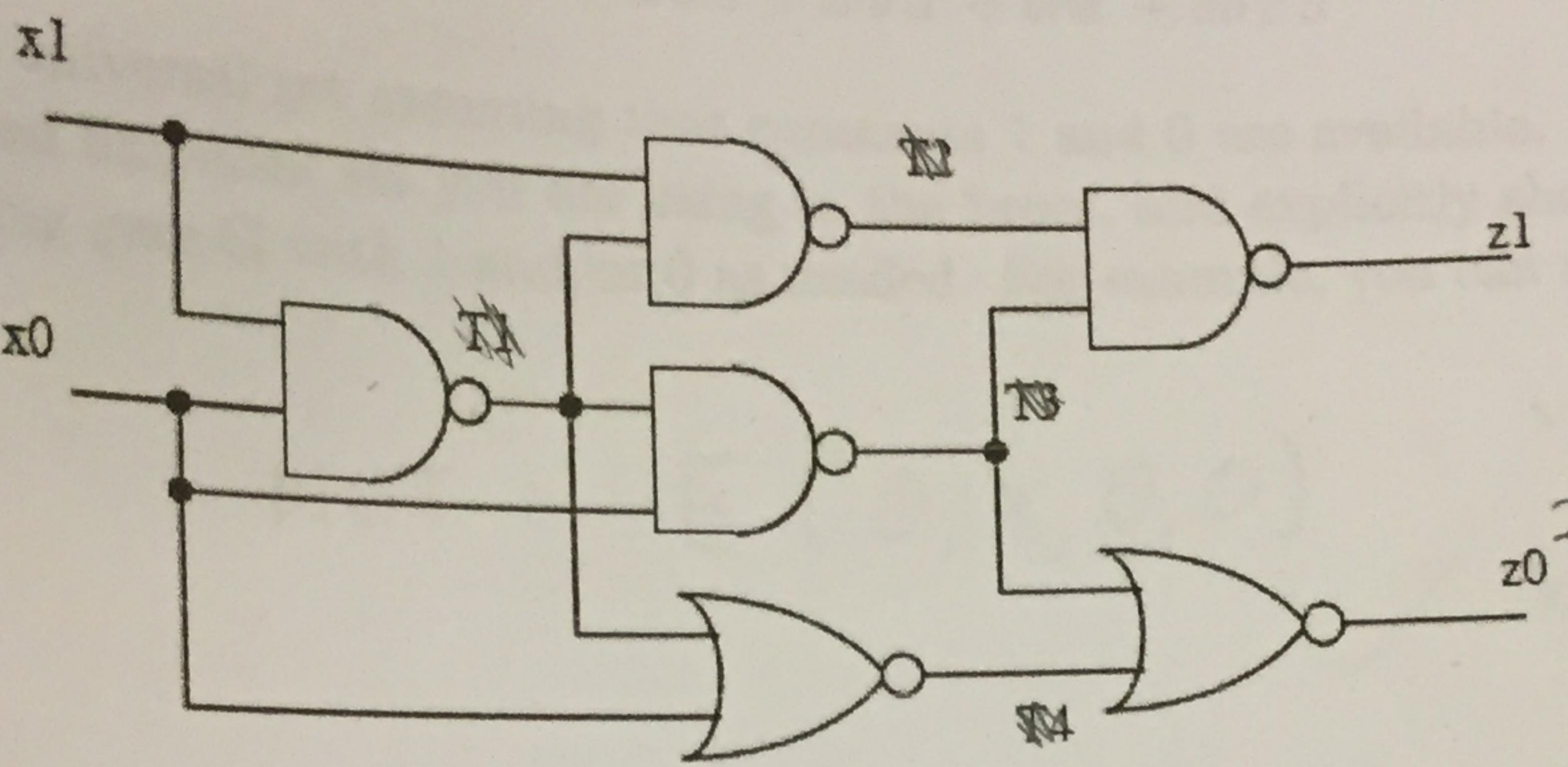
✓

2. (8 points) Is the PLA implementation correct? If not, find errors and show the correct implementation
(cross out wrong connections and insert correct ones)

No, errors on line 3, 6, 7, 8, 9

Problem 5 (10 points)

Calculate the propagation delay $t_{pLH}(z_0)$ when x_0 changes. Assume that z_0 's load value is 2. Fill in the blanks below with the appropriate values. You don't need to fill all the blanks.



Gate Type	Fan-in	Propagation Delays (ns)		Load Factor I
		t_{pLH}	t_{pHL}	
NOT	1	$0.02 + 0.038L$	$0.05 + 0.017L$	1.0
AND	2	$0.15 + 0.037L$	$0.16 + 0.017L$	1.0
OR	2	$0.12 + 0.037L$	$0.20 + 0.019L$	1.0
NAND	2	$0.05 + 0.038L$	$0.08 + 0.027L$	1.0
NOR	2	$0.06 + 0.075L$	$0.07 + 0.016L$	1.0

Gate type & Fan-in

NAND 2 → NAND 2 → NOR 2 → _____ → _____ → _____

LH / HL

LH → HL → LH → _____ → _____ → _____

Output load L

3 → 2 → 2 → _____ → _____ → _____

Prop. Delay

0.05 + 0.038(3) → 0.08 + 0.027(2) 0.06 + 0.075(2) ✓ → _____ → _____

Problem 6 (15 points)

A gate G is defined by the following expression

$$E(a, b, c, d) = cd + a'b'c' + a'b'd' + bcd' + ab'c'd$$

15

Show that gate G forms a universal set assuming that constants 1 and 0 are available.

Specify a pre-established universal set you are using in the proof, and explicitly show the implementation for each element in the set using gate G with 1 and/or 0 as needed. For example, you can assign $a=0$ and $b=1$ in the expression E.

abcd	
0000	1
0001	1
0010	1
0011	1
0100	0
0101	0
0110	1
0111	1
1000	0
1001	1
1010	0
1011	1
1100	0
1101	0
1110	1
1111	1

NOT : $E(0, x, 0, 0)$

$x=0 \quad E(0,0,0)=1$

$x=1 \quad E(0,1,0,0)=0$

OR : $E(E(0,x,0,0), y, 1, 0) = E(x', y, 1, 0)$

$x=0 \quad y=0 \quad E(1,0,1,0)=0$

$x=0 \quad y=1 \quad E(1,1,1,0)=1$

$x=1 \quad y=0 \quad E(0,0,1,0)=1$

$x=1 \quad y=1 \quad E(0,1,1,0)=1$

Since gate G can implement NOT and OR,

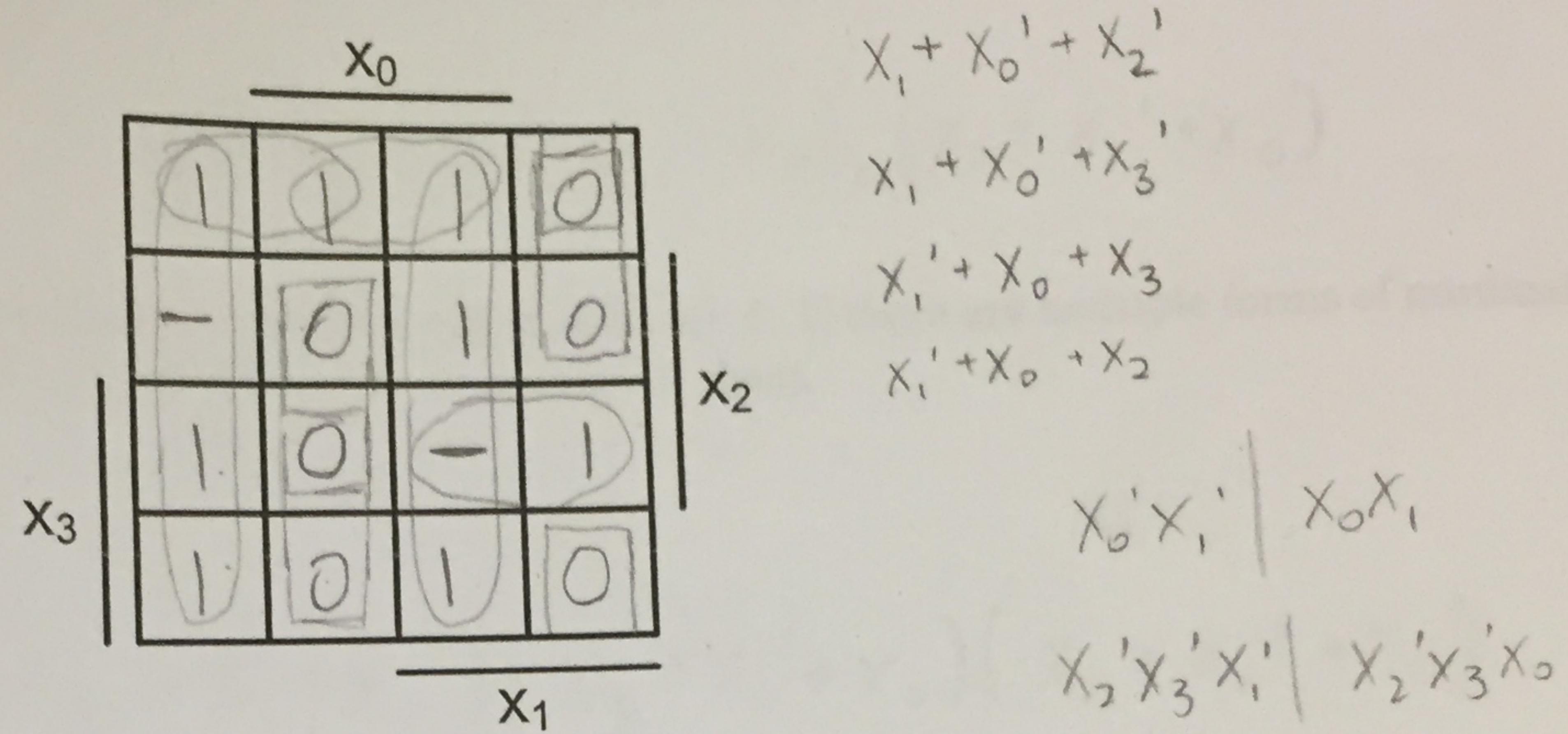
G forms a universal set

Problem 7 (20 points)

For the switching function $f(x_3, x_2, x_1, x_0)$, we are given the information below for the dc-set and zero-set.

18

- ($x_3 + x_2 + x_1' + x_0$) ($x_3 + x_2' + x_1 + x_0'$) dc-set = (4, 15)
 13 10 9 6 5 2
 zero-set = zero-set of function
1. (2 points) Fill out the following K-map.



2. (4 points) Which of the given expressions are prime implicants of the function given above? Circle all that apply. Do not circle implicants that are not prime.

- (a) x_3x_1
 (b) x_3x_2'
 (c) $x_3'x_1$

- (d) $x_1'x_0'$
 (e) x_1x_0
 (f) $x_3'x_2'x_1'$

- (g) $x_3'x_2'x_0$
 (h) $x_3x_2x_1$
 (i) $x_3x_2x_0'$

- (j) $x_3'x_2x_1'x_0$
 (k) $x_3x_2'x_1x_0$
 (l) $x_3'x_2x_1x_0'$

3. (3 points) Write down the complete set of essential prime implicants.

$$x_0'x_1', x_0x_1, x_1x_2x_3 \quad -\mid$$

4. (2 points) Write down the minimal sum of products expressions for f . If there are multiple forms of minimal sum of products expressions, you only need to write down one of them.

$$f = x_0'x_1' + x_0x_1 + x_1x_2x_3 + x_1'x_2'x_3'$$

5. (4 points) Which of the given expressions are prime implicants of the function given above? Circle all that apply. Do not circle implicants that are not prime.

- (a) $(x_3 + x_2' + x_1)$
(b) $(x_3' + x_2')$
(c) $(x_2' + x_1 + x_0')$

- (d) $(x_3' + x_1 + x_0')$
(e) $(x_3 + x_2' + x_0)$
(f) $(x_3 + x_1' + x_0)$

- (g) $(x_2 + x_1' + x_0)$
(h) $(x_3 + x_1 + x_0)$
(i) $(x_3' + x_2' + x_1)$

- (j) $(x_3' + x_1')$
(k) $(x_3 + x_2 + x_1 + x_0')$
(l) $(x_3 + x_2' + x_1' + x_0)$

6. (3 points) Write down the complete set of essential prime implicants.

$$(x_2' + x_1 + x_0'), (x_3' + x_1 + x_0'), (x_3 + x_1' + x_0), (x_2 + x_1' + x_0)$$

7. (2 points) Write down the minimal product of sums expressions for f . If there are multiple forms of minimal product of sums expressions, you only need to write down one of them.

$$(x_2' + x_1 + x_0')(x_3' + x_1 + x_0')(x_3 + x_1' + x_0)(x_2 + x_1' + x_0)$$

