

# EE 3 Final Exam w'13 Solutions

1. a) series resistors  $\Rightarrow 20 \Omega$   
 $20 \parallel 20 = 10 \Omega$   
 $10 \Omega + 10 \Omega = 20 \Omega$

2 b) parallel impedances  $j\omega L$  and  $(R + \frac{1}{j\omega C})$   
 $\Rightarrow \frac{j\omega L (R + \frac{1}{j\omega C})}{R + j\omega L + \frac{1}{j\omega C}} = \frac{1 + j10\omega}{10 + j(\omega - 1/\omega)}$

2  $\therefore Z_{eq} = 10 + \frac{1 + j10\omega}{10 + j(\omega - 1/\omega)}$

- c) For the second part,  $R_{eq} = 20 \Omega$

1  $\therefore v_2 = 20 i_2$

since  $v_1 = v_2 \frac{N_1}{N_2}$ ,  $v_1 = \frac{1}{2} 20 i_2 = 10 i_2$

1  $\frac{i_2}{i_1} = \frac{1}{2}$ ;  $i_2 = \frac{i_1}{2}$ ;  $v_1 = 10 i_1 / 2 = 5 i_1$

1 By KVL  $\Delta = 20 i_1 + 5 i_1$ ;  $i_1 = \frac{1}{25}$ ;  $v_1 = \frac{1}{5}$

1 Thus  $i_2 = \frac{1}{50}$ ;  $v_2 = \frac{2}{5}$

1 The parallel resistors equally divide the current

$i_3 = i_4 = \frac{1}{100}$

- 1 d) ~~Power~~ <sup>Voltage</sup> is stepped up to reduce resistive losses in transmission lines and then stepped down for safety in local distribution.

2. a)  $10\ \Omega$  resistor prevents damaging currents through motor &  $1\text{k}\Omega$  prevents large currents through base

We require  $V_{BE} > V_{th}$  for current to flow from  $C \rightarrow E$ .

b) A DC motor would change directions with the current changing directions. But in any case the transistor would only be active in positive phases.

We want  $e^{-T/RC}$  to be  $\sim 1$  for  $T = 1/f_c = 10^{-3}$

so  $RC \lesssim 10^{-4}$  would work (value = .9)  
 $C = 1\ \mu\text{F}$

c) wavefront spreading, transducer losses, obstructions

need  $\frac{R_f}{R_i} = 10$ . Given other resistor

values,  $R_i = 1\text{k}\Omega$ ,  $R_f = 10\text{k}\Omega$  would be good

d) Basis for most digital logic (computers)  
most amplifiers use them

(could name particular classes of consumer products  $\rightarrow$  radio, TV, etc.)

3. a)  $L = 100^2 = 10^4$

Thus  $G = 10^4$

b) Always put the lowest noise figure amplifiers first

Start with gains 10 & 10:

$$F = 8 + \frac{8-1}{10} = 8.7$$

Then add gain 100

$$F = 8.7 + \frac{10-1}{100} = 8.7 + 0.09 = 8.79$$

c) Then noise figure for one link is

$$F = 10^4 + \frac{8.79-1}{10^4} = 10^4 + 7.79 \times 10^{-4} = 8.79 \times 10^4$$

For the second link

$$F = 8.79 \times 10^4 + \frac{8.79 \times 10^4 - 1}{1} \approx 2 \times 8.79 \times 10^4$$

Thus 10 links are needed for a degradation by a factor of 10.

d) Digital repeaters increase error rate linearly with the number of links, rather than decreasing SNR. This is far more favorable when the error rate per link is low. (We can additionally use multiple modulation formats) Analog systems were used until relatively recently due to cost; Moore's law has pushed it down.

[ If in (c) amplifier is first,  $F_1 = 8.79 + \frac{10^4-1}{10^4} = 9.79$   
 $F_2 = 9.79 + \frac{9.79-1}{1} = 9.79 + 8.79$ ;  $F_n = 9.79 + (n-1)8.79$   
 $n \geq 11$  ]