

Assume the temperature T is 300K and the semiconductor is silicon unless otherwise specified. 1. $(20')$

A short-based Si p-n junction (width $W_p = W_n = 1 \mu m$) with cross-sectional area A = 0.001 cm⁻² is formed with $N_a = 10^{16} \text{cm}^{-3}$ and $N_d = 10^{18} \text{cm}^{-3}$. Calculate:

(a) Built-in potential, V_{bi} . (5^{*})

For a normal P-N junction we have

$$
V_{bi} = kT \ln \left(\frac{N_a N_d}{n_i^2}\right) \approx 0.81V
$$

(b) What are the minority carrier concentrations in P and N quasi-neutral region in equilibrium? (5') In p quasi neutral region, the minority carrier concentration is given by

$$
n_0 \approx \frac{n_i^2}{N_a} = 2.25 \times 10^4 cm^{-3}
$$

while in n quasi neutral region

$$
p_0 \approx \frac{n_i^2}{N_d} = 2.25 \times 10^2 cm^{-3}
$$

EE121B Mid Term

$$
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$$

(c) When a forward bias of 0.4V is applied, what is the current density for the electrons at the edge of the depletion in the P region? Assume $\mu_n = 600 \text{ cm}^2/\text{V-S}$, $\mu_p = 200 \text{ cm}^2/\text{V-S}$, and $\tau_n = \tau_p = 25 \text{ }\mu\text{s}$.

 $(5')$

The width of the depletion in P and N region is given by

$$
x_n = \sqrt{\frac{2\epsilon_{Si}(V_{bi} - 0.4) N_a}{q(N_a + N_d)} N_d} \approx 2.3nm
$$

$$
x_p = \sqrt{\frac{2\epsilon_{Si}(V_{bi} - 0.4) N_a}{q(N_a + N_d)} N_a} \approx 230nm
$$

The neutral region width is can be calculated by

$$
d_n = W_n - x_n
$$

\n
$$
d_p = W_p - x_p
$$

\n
$$
J_n(x_p) = \frac{qD_n n_i^2}{N_a d_p} \left(\exp \frac{qV}{kT} - 1 \right) \approx 3.7 \times 10^{-3} A/cm^2
$$

The current flows out of p region.

$$
\delta n = \frac{n_i^2}{N_a} \left(\exp \frac{qV}{kT} - 1 \right) \approx 1.1 \times 10^{11} cm^{-3}
$$

(d) Ignore the recombination/generation current, when the P-N diode is reverse-biased, what is the saturation current? (5)

As reverse bias $>> kT$, the saturation current can be approximated

$$
x_p \approx \sqrt{\frac{2\epsilon_{Si}(V_{bi} + V_R)}{q(N_a + N_d)} \frac{N_a}{N_a}} \approx 322nm, x_n \approx 3.2nm
$$

$$
I_0 \approx qA \left(\frac{D_n n_i^2}{N_a(W_n - x_n)} + \frac{D_n n_i^2}{N_a(W_p - x_p)}\right) \approx 8.2 \times 10^{-13} A
$$

Name: ____________________ UID: ____________________ 2. $(20')$

A long-based Si p-n junction with cross-sectional area $A = 0.001$ cm⁻² is formed with $N_a = 10^{17}$ cm⁻³ and N_d = 10¹⁷ cm⁻³. Assume μ_n = 600 cm²/V-S, μ_p = 200 cm²/V·S, and τ_n = τ_p = 25 μ s.

(a) At forward bias of 0.5V, calculate the diffusion charge stored in the n and p quasi-neutral region $(5')$

 \overline{a}

The diffusion charge stored in the quasi neutral region is given by

$$
Q_{n,diff} = qA \int n \cdot dx \approx qA \frac{n_i^2}{N_a} \exp \frac{qV}{kT} \int_0^\infty \exp \left(-\frac{x}{L_n}\right) dx \approx qA \frac{n_i^2}{N_a} \exp \frac{qV}{kT} L_n \approx 1.7 \times 10^{-12} \text{ C}
$$

$$
Q_{p,diff} = qA \int p \cdot dx \approx qA \frac{n_i^2}{N_a} \exp \frac{qV}{kT} \int_0^\infty \exp \left(-\frac{x}{L_p}\right) dx \approx qA \frac{n_i^2}{N_a} \exp \frac{qV}{kT} L_p \approx 9.9 \times 10^{-13} \text{ C}
$$

$$
(\delta n = \delta p \approx \frac{n_i^2}{N_d} \exp \frac{qV}{kT} \approx 5.44 \times 10^{11} \text{cm}^{-3}
$$

\n
$$
L_n = \sqrt{D_n \tau_n} \approx 1.9 \times 10^{-2} \text{cm}
$$

\n
$$
L_p = \sqrt{D_p \tau_p} \approx 1.1 \times 10^{-2} \text{cm}
$$

(b) Find the depletion width and depletion capacitance of this diode when it is forward biased at 0.5V

 $(5')$

The built-in potential is given by

$$
V_{bi} = kT \ln \left(\frac{N_a N_d}{n_i^2}\right) \approx 0.81 V
$$

And the depletion width is

$$
W_{\rm d} = \sqrt{\frac{2\epsilon_{Si}(V_{bi} - V_F)}{qN_aN_d/(N_a + N_d)}} \approx 90.1 \,\text{nm}
$$

Therefore, the depletion capacitance is

$$
C_{dep} = \frac{A\epsilon_{Si}}{W_D} \approx 1.16 \times 10^{-10} F
$$

(c) Now at t=0, the bias current is reversed and find $\frac{dV(t)}{dt}\Big|_{t=0}$ (10)

After time t=0, the current start to discharge the capacitance, as well as the recombination (which has the same current under the forward bias), i.e.,

$$
\frac{dV(t)}{dt}\bigg|_{t=0} = \frac{2I}{C_{dep} + C_{diff}} \approx \frac{2qAn_i^2}{C_{dep} + \frac{(Q_n + Q_p)}{KT}} \left(\frac{D_p}{N_a L_p} + \frac{D_n}{N_a L_n}\right) \left[\exp\left(\frac{qV_F}{kT}\right) - 1\right]
$$

 $\approx 10^3 V/s$

$$
I(V_F = 0.5V) = qAn_i^2 \left(\frac{D_p}{N_d L_p} + \frac{D_n}{N_a L_n}\right) \left[\exp\left(\frac{qV_F}{kT}\right) - 1\right] \approx 1.1 \times 10^{-7} A
$$

3. $(20')$

Consider the npn BJT below.

(a) Sketch the current components of the NPN bipolar transistor between the emitter, base and collector. (5)

Name: ____________________ UID: ____________________ 4. $(20')$

A Si NPN BJT has emitter, base, and collector doping levels of 10^{19} cm⁻³, 10^{18} cm⁻³, and 10^{17} cm⁻³, respectively. Assume both the quasi-neutral base and emitter width is 500nm, and the collector width is large. Approximately consider the widths of quasi-neutral region do not change with depletion width. Assume electron and hole mobility of 100 and 50 cm²/V-s, respectively, in all regions and that the carrier lifetimes are $1 \mu s$ everywhere.

(a) When it is biased in the normal active mode, with an emitter-base voltage of 0.6V, if we have negligible base recombination, calculate the emitter current density, emitter injection efficiency, and base transport factor. (5['])

The emitter current density can be calculated as

$$
J_E = q n_i^2 \left(\frac{D_n}{W_B N_{AB}} + \frac{D_p}{W_E N_{DE}} \right) \cdot \left(\exp\left(\frac{q V_{BE}}{kT} \right) - 1 \right) \approx 2.26 \times 10^{-2} A \cdot cm^{-2}
$$

Emitter injection efficiency

$$
\gamma_{BE} \approx \frac{1}{1 + \left(\frac{D_n W_E N_{DE}}{D_p W_B N_{AB}}\right)^{-1}} \approx 0.95
$$

As we ignore the recombination in the base region, base transport factor

 $B \approx 1$

(b) Find the emitter(collector) injection efficiency when the collector-base junction is forward biased $(V_F=0.4V)$ and emitter-base junction is reverse biased. $(5')$ Since the collector has a long neutral region, collector injection efficiency is given by

$$
\gamma_{\rm BC} \approx \frac{1}{1 + \left(\frac{D_n L_{pC} N_{DC}}{D_p W_B N_{AB}}\right)^{-1}} \approx 0.814
$$

And $L_{\text{pc}} = \sqrt{D_p \tau_p} = 1.1 \times 10^{-3} cm$

$$
J_C \text{ or } J_{C,I} = q n_i^2 \left(\frac{D_n}{W_B N_{AB}} + \frac{D_p}{L_{pC} N_{DC}} \right) \cdot \left(\exp\left(\frac{q V_{BC}}{kT} \right) - 1 \right) \approx 1.2 \times 10^{-5} A \cdot cm^{-2}
$$

(c) When it is biased in the saturation mode (V_E=0V, V_B=0.6V, V_c=0.2V) use Ebers-Moll model to find the emitter, base and collector current density.
$$
(10^{\circ})
$$

Let's define the direction of the current the same way as the one in active mode.

Using Ebers-Moll model and previous calculated J_E and J_C , we have

$$
J_E = J_{E,N} - \alpha_{BC} J_{C,I}
$$

$$
J_C = -J_{C,I} + \alpha_{BE} J_{C,I}
$$

As $B \approx 1$ for both cases, we have

$$
J_E = J_{E,N} - \gamma_{BC} J_{C,I} \approx 2.26 \times 10^{-2} A/cm^2
$$

\n
$$
J_C = -J_{C,I} + \gamma_{BE} J_{C,I} \approx 2.15 \times 10^{-2} A/cm^2
$$

\n
$$
J_B = J_E - J_C \approx 1.1 mA/cm^2
$$

5. $(20')$

The PIN silicon diode consists of a P-region with doping of $N_a=10^{17}$ cm⁻³, an intrinsic region and an N-region with doping of N_d =10¹⁸cm⁻³, as plotted in the figure. Assume the width of the intrinsic region $W_i = 1 \,\mu m$.

(a) Sketch the band diagram of this PIN diode (5')

 N_d W, $\mathcal{N}_1 + \mathcal{N}_2$ N_a $-X_{n}$ $\ddot{\mathbf{O}}$ Ec ΕF Eν

$UID:$

(b) At equilibrium, calculate the total depletion width under depletion approximation and the depletion capacitance $(5')$

Built-in potential $V_{bi} = kT \cdot \ln \left(\frac{N_a N_d}{n^2} \right)$ $\left(\frac{a^{N}d}{n_i^2}\right) \approx 0.87V$

(Under depletion approximation, we can have

$$
\frac{1}{2}\frac{qN_a}{\varepsilon_{Si}}x_p^2 + \frac{1}{2}\frac{qN_d}{\varepsilon_{Si}}x_n^2 + \frac{qN_d x_n}{\varepsilon_{Si}}w_i = V_{bi}
$$

Using charge neutrality, we have

$$
qN_d x_n = qN_a x_p = Q
$$

So

$$
\frac{1}{2} \frac{Q^2}{q N_a \varepsilon_{Si}} + \frac{1}{2} \frac{Q^2}{q N_d \varepsilon_{Si}} + \frac{Q}{\varepsilon_{Si}} w_i = V_{bi}
$$

Solving for Q

$$
Q = q\varepsilon_{Si} \left(\frac{N_a N_d}{N_a + N_d}\right) \left[-\frac{w_i}{\varepsilon_{Si}} + \sqrt{\frac{w_i^2}{\varepsilon_{Si}^2} + \frac{2V_{bi}}{q\varepsilon_{Si} \frac{N_a N_d}{N_a + N_d}}} \right] \approx 9.1 \times 10^{-5} C/m^2)
$$

On the other hand, we can do the calculation with approximation when Q is small and w_i is large,

$$
\frac{qN_d x_n}{\varepsilon_{Si}} w_i \gg \frac{1}{2} \frac{Q^2}{qN_d \varepsilon_{Si}}, and \frac{1}{2} \frac{Q^2}{qN_d \varepsilon_{Si}}
$$

We have $Q \approx \frac{V_{bi}}{V}$ $\frac{v_{bi}}{w_i} \varepsilon_{Si} \approx 9.1 \times 10^{-5} C/m^2$

Thus, total depletion width is

$$
x_n + x_p + w_i = \frac{Q}{qN_d} + \frac{Q}{qN_a} + w_i \approx 1.006 \,\mu m
$$

Therefore,

$$
C_{\rm dep} = \frac{\varepsilon_{Si}}{x_n + x_p + w_i} = 1.04 \times 10^{-8} \, F/m^2
$$

(c) When the PIN diode is reversely biased at *VR*=0.5V, what is the total capacitance across the diode?

 $(5')$

Using the same approximation,

$$
Q \approx \frac{V_{bi} + V_R}{w_i} \varepsilon_{Si} \approx 1.43 \times 10^{-4} \text{ C/m}^2
$$

$$
x_n + x_p + w_i = \frac{Q}{qN_d} + \frac{Q}{qN_a} + w_i \approx 1.01 \text{ }\mu\text{m}
$$

$$
C_{\text{dep}} = \frac{\varepsilon_{Si}}{x_n + x_p + w_i} \approx 1.04 \times 10^{-8} \text{ F/m}^2
$$

(d) Where and how would we want to use such a PIN diode? (5')

In such a diode, we can expect the depletion width is much wider as normal diode, and the depletion cap remains roughly constant. It is very useful as a light sensor/photodetector or as RF switch.

