

EE 115A

Midterm Exam
Fall 2006

Your Name:

Solutions

Name of Person to Your Left:

Name of Person to Your Right:

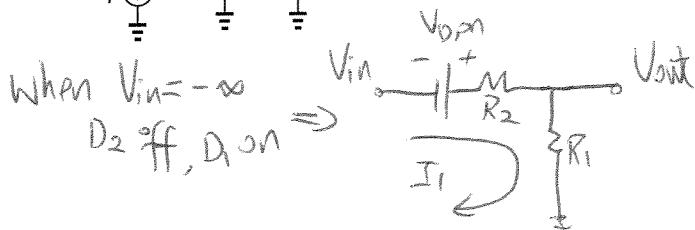
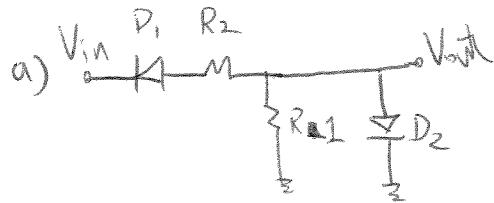
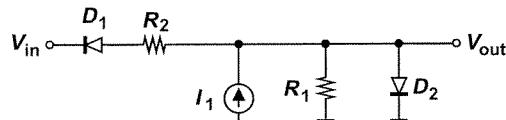
Time Limit: 2 Hours

1. 10
2. 10
3. 10
4. 10
5. 10

Total: 50

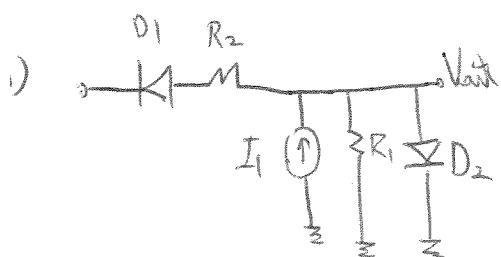
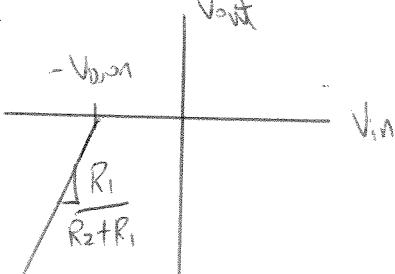
(10)

1. Assuming a constant-voltage model,

(a) plot V_{out} as a function of V_{in} if $I_1 = 0$. Show the details of your calculations.(b) plot V_{out} as a function of V_{in} if I_1 is a constant, positive, ideal current source. Show the details of your calculations. Assume $I_1 R_1 > V_{D, on}$.

$$I_1 = \left(\frac{V_{in} + V_{D, on}}{R_2 + R_1} \right), \quad V_{out} = \left(\frac{V_{in} + V_{D, on}}{R_2 + R_1} \right) R_1$$

As $V_{in} \uparrow$ and hits $-V_{D, on}$ $I_1 = 0$ hence D₁ turns off and stays off so $V_{out} = 0$.



For $V_{in} > 0$, D₁ is off and I₁ flows into R₁ and D₂ so $V_{out} = V_{D, on}$

As $V_{in} \downarrow$ and hits 0 D₁ turns on and D₂ is still on.

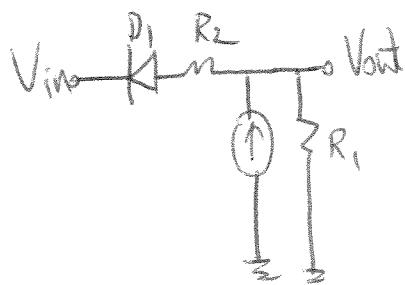
so let's see when D₂ turns off: Writing a KCL at output we have

$$I_1 = \frac{V_{out}}{R_1} + \frac{V_{out} - (V_{D, on} + V_{in})}{R_2} + I_{D2}, \text{ since when } D_2 \text{ is on } V_{out} = V_{D, on} \text{ so } I_1 \text{ becomes}$$

$$I_1 = \frac{V_{D, on}}{R_1} + \frac{(-V_{in}) + I_{D2}}{R_2} \Rightarrow I_{D2} = I_1 + \frac{V_{in}}{R_2} - \frac{V_{D, on}}{R_1}, \text{ when } I_{D2} = 0 \text{ D}_2 \text{ is off and this happens @ } \frac{V_{in}}{R_2} = -I_1 + \frac{V_{D, on}}{R_1}$$

$$\frac{V_{in}}{R_2} = -I_1 + \frac{V_{D, on}}{R_1} \Rightarrow V_{in} = \frac{R_2}{R_1} V_{D, on} - I_1 R_2$$

b) When D_2 turns off we have the following circuit.



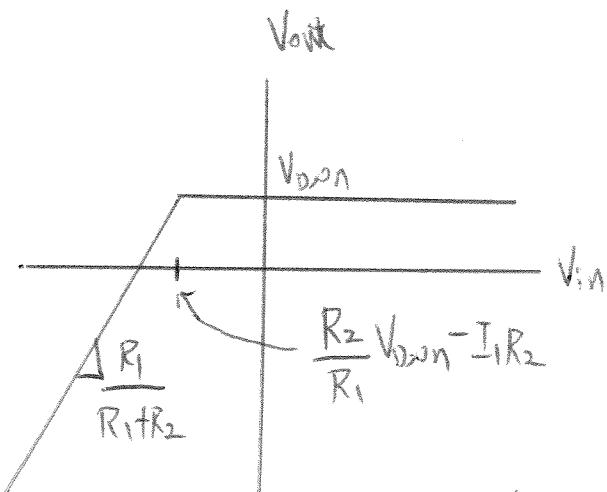
KCL @ output gives us

$$I_1 = \frac{V_{out}}{R_1} + \frac{V_{out} - (V_{D,ON} + V_{in})}{R_2}$$

$$\Rightarrow \frac{V_{out}}{R_1} + \frac{V_{out}}{R_2} = I_1 + \frac{(V_{D,ON} + V_{in})}{R_2}$$

$$V_{out} = I_1 (R_1 // R_2) + \frac{(V_{in} + V_{D,ON}) (R_1 // R_2)}{R_2}$$

$$V_{out} = \frac{I_1 R_2 R_1}{R_1 + R_2} + \frac{(V_{in} + V_{D,ON}) R_1}{R_1 + R_2}$$

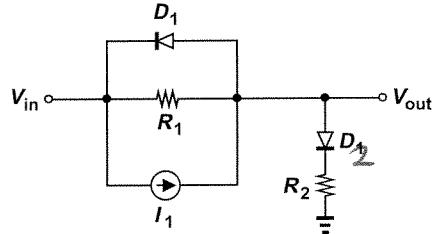


2. Assuming a constant-voltage model,

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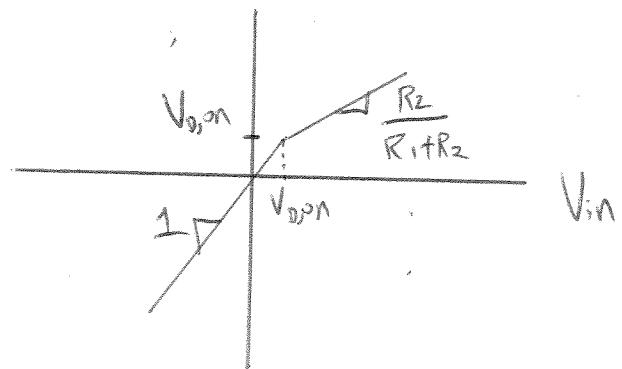
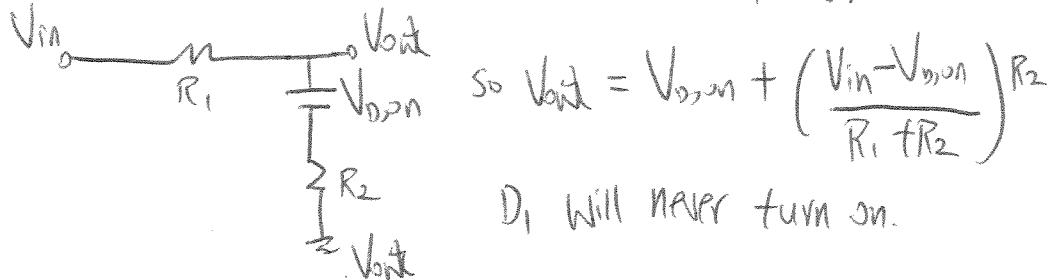
(a) plot V_{out} as a function of V_{in} if $I_1 = 0$. Show the details of your calculations.

(b) plot V_{out} as a function of V_{in} if I_1 is a constant, positive, ideal current source. Show the details of your calculations. Assume $I_1 R_1 > V_{D,ON}$.

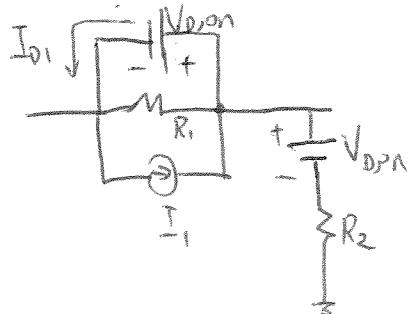


a) When V_{in} is at $-\infty$ D_2 off and D_1 off so $V_{out} = V_{in}$

As $V_{in} \uparrow$ and hits $V_{D,ON}$ D_2 turns on and we have the situation below



b) When V_{in} is at $-\infty$ D_2 off but D_1 is on since I_1 provides a current, so $V_{out} = V_{in} + V_{D,ON}$
As $V_{in} \uparrow$ and hits zero D_2 turns on and we have the circuit below.



KCL at output gives us:

$$I_1 = \frac{V_{in} - V_{D,ON}}{R_1} + \frac{V_{D,ON}}{R_2} + I_{D1}$$

$$I_{D1} = I_1 + \frac{V_{D,ON} - V_{out}}{R_2} - \frac{V_{D,ON}}{R_1}$$

$$0 = I_1 + \frac{V_{D,ON} - V_{D,ON}}{R_1} - \frac{V_{out}}{R_2}$$

2.)
b)

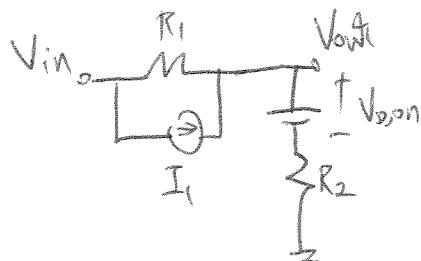
Solving for V_{out} gives us

$$V_{out} = I_1 R_2 + \frac{V_{D,ON} (R_1 - R_2)}{R_1}, D_1 \text{ turns off.}$$

$$\text{Since } V_{out} = V_{D,ON} + V_{in}, V_{in} = I_1 R_2 + \frac{V_{D,ON} (R_1 - R_2)}{R_1} - V_{D,ON}$$

$$V_{in} = I_1 R_2 - \frac{V_{D,ON} R_2}{R_1}$$

Afterwards we have this circuit and



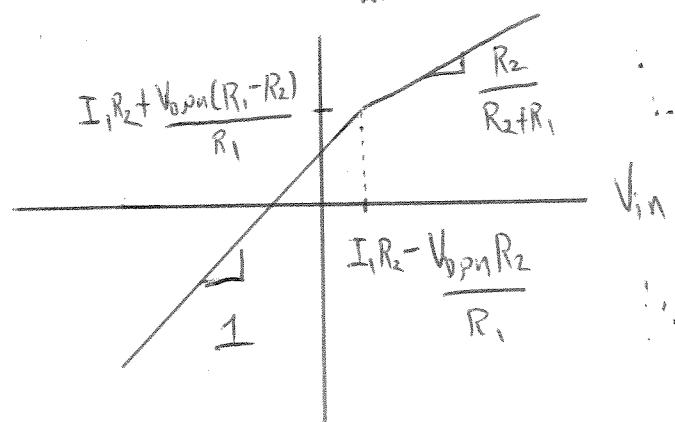
KCL at V_{out} gives us:

$$I_1 = \frac{V_{out} - V_{D,ON}}{R_2} + \frac{V_{out} - V_{in}}{R_1}$$

$$V_{out} \left(\frac{1}{R_2} + \frac{1}{R_1} \right) = I_1 + \frac{V_{D,ON}}{R_2} + \frac{V_{in}}{R_1}$$

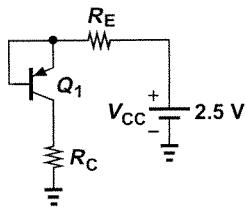
$$V_{out} = (R_2/R_1) I_1 + \frac{(R_2/R_1) V_{D,ON}}{R_2} + \frac{(R_2/R_1) V_{in}}{R_1}$$

$$V_{out} = \frac{R_2}{R_2 + R_1} V_{in} + \frac{R_1}{R_1 + R_2} V_{D,ON} + \frac{R_2 R_1}{R_1 + R_2} I_1$$

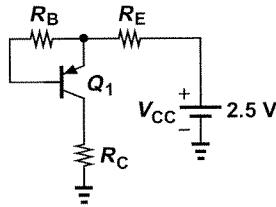


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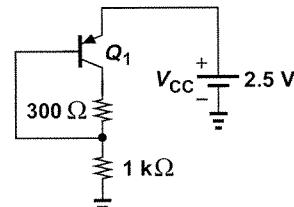
3. Determine the region of operation of Q_1 in each of the circuits shown below. Assume $I_S = 5 \times 10^{-16} \text{ A}$, $\beta = 100$, $V_A = \infty$. You need only show whether the transistor is in active region, saturated, or off. Simply indicate which region and why.



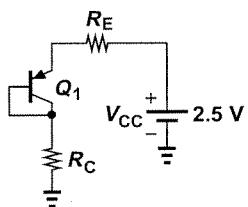
(a)



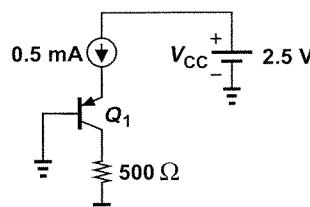
(b)



(c)



(d)



(e)

a) $V_{EB}=0$ off

b) For $V_{EB}>0$, I_B has to flow from emitter to base (into the base) which is impossible so, off

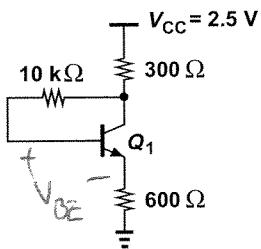
c) Collector is higher than base, Saturated
(maybe in soft saturation, but doesn't matter still saturated.)

d) $V_{CB}=0$, so forward active

e) Collector higher than base, Saturated

4. (a) Consider the circuit shown below, where $I_S = 6 \times 10^{-16} \text{ A}$, $\beta = 100$, and $V_A = \infty$. Calculate the operating point of Q_1 .

(10)



$$2.5 - I_E 300\Omega - \frac{I_E}{\beta} 10k\Omega - V_{BE} - I_E 600\Omega = 0$$

Since β is large, assume $I_c \approx I_E$ so

$$2.5 - I_c 300\Omega - \frac{I_c}{\beta} 10k\Omega - V_{BE} - I_c 600\Omega = 0$$

$$I_c = \frac{2.5 - V_{BE}}{0.6 + \frac{10}{100} + 0.3}, \text{ guess } V_{BE} = 0.75 \text{ and then iterates}$$

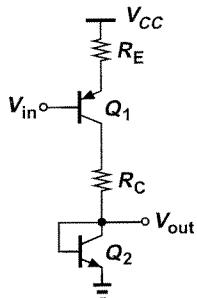
I_c converges to 1.75 mA

$$V_{BE} \approx 0.746 \text{ V}$$

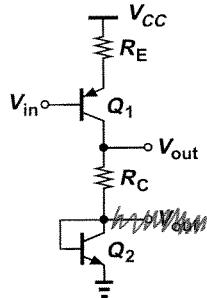
$$V_{CE} \approx 0.925 \text{ V}$$

5. Compute the voltage gain and the input impedance of the circuits shown below. Assume $V_A = \infty$ and $\beta \gg 1$. Place your answers within the boxes.

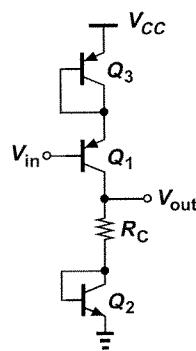
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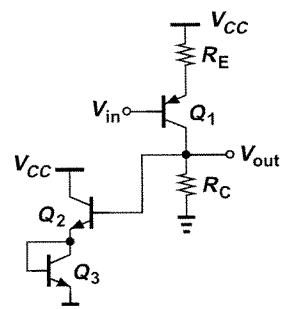
(a)



(b)



(c)



(d)

$$(a): A_v = \frac{-g_m}{R_E + g_m}$$

$$(a): R_{in} =: \frac{V_{th} + (1+\beta)R_E}{V_{th} + \beta R_E} \approx$$

$$(b): A_v = \frac{-(R_C + 1/g_m)}{R_E + 1/g_m}$$

$$(b): R_{in} =: \frac{V_{th} + (1+\beta)R_E}{V_{th} + \beta R_E} \approx$$

$$(c): A_v = \frac{-R_C + 1/g_{m_2}}{1/g_{m_3} + 1/g_{m_1}}$$

$$(c): R_{in} = V_{A1} + \beta \left(\frac{1}{g_{m_3}} \right)$$

$$(d): A_v = \frac{-(V_{A2} + \beta \frac{1}{g_{m_3}}) / R_C}{R_E + 1/g_{m_1}}$$

$$(d): R_{in} = V_{A1} + \beta R_E$$