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1- True or False?

T	In an NPN BJT the emitter is N ⁺ to ensure the BE current is dominated by free electrons.
F	In a PNP BJT the base layer is thin to ensure the rate of thermal ionization is sufficiently high.
F	N-type semiconductors are made by adding a dopant element from group V of the periodic table to a crystalline material of atoms from group III.
F	In a PN junction the width of the depletion region is larger on the P side.
T	A semiconductor becomes more conductive as the temperatures rises above the room temperature.

(10 points)

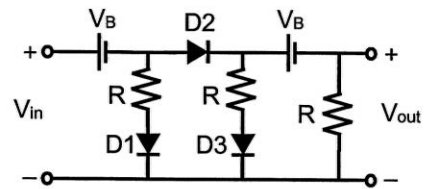
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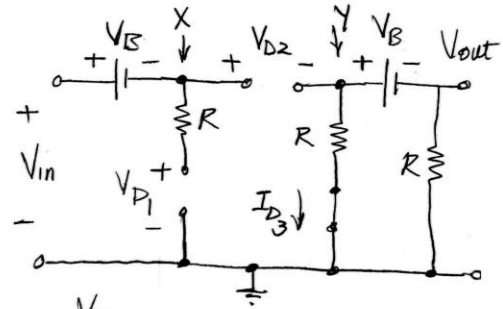
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2- Find V_{out} versus V_{in} , as V_{in} varies from $-\infty$ to $+\infty$, if the diodes have a turn on voltage of $V_{D_{on}}=0$ and are otherwise ideal (Use a piece-wise linear model). V_B is larger than 0.

(40 points)



* For $V_{in} = -\infty$ we assume $D1$ and $D2$ are OFF. With these two turned off it's reasonable to assume that V_{B2} will turn $D3$ ON.



$$I_{D3} = \frac{V_B}{2R} \rightarrow V_{out} = \frac{V_B}{2R} R - V_B = -\frac{V_B}{2}$$

$$\rightarrow V_y = V_{out} + V_B = \frac{V_B}{2}$$

$$V_x = V_{in} - V_B \rightarrow V_{D1} = V_{in} - V_B \text{ and } V_{D2} = V_x - V_y = V_{in} - \frac{3}{2} V_B$$

Verify the assumptions: $I_{D3} > 0$, $V_{D1} < 0$, $V_{D2} < 0$

- ① For $D1$ to turn ON: $V_{in} > V_B$
- ② For $D2$ to turn ON: $V_{in} > \frac{3}{2} V_B$
- ③ For $D3$ to turn OFF: I_{D3} should be negative, not possible.

So ① happens first.

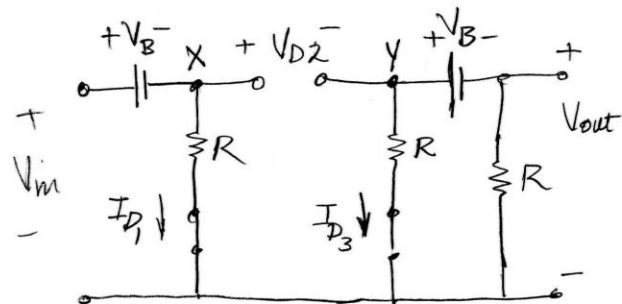
* When V_{in} exceeds V_B :

$$I_{D1} = \frac{V_{in} - V_B}{R} \quad V_x = V_{in} - V_B$$

$$I_{D3} = \frac{V_B}{2R} \text{ and } V_{out} = -\frac{V_B}{2}$$

$$V_y = \frac{V_B}{2} \rightarrow V_{D2} = V_x - V_y = V_{in} - \frac{3}{2} V_B$$

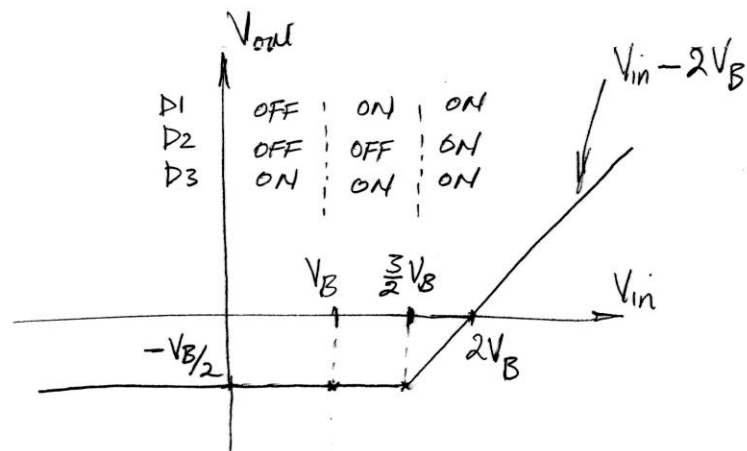
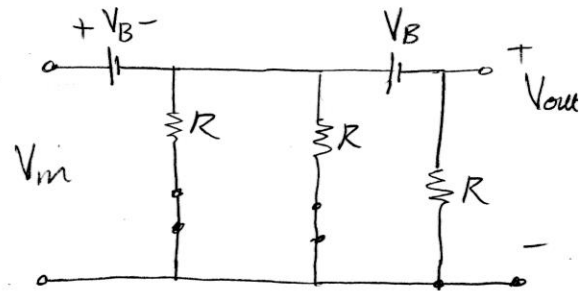
Clearly the next important point is when $D2$ turns ON.



* When V_{in} exceeds $\frac{3}{2}V_B$:

$$V_{out} = V_{in} - 2V_B$$

It's easy to see that the diodes will remain on after that.



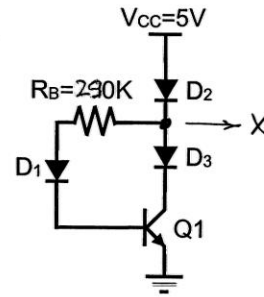
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3- Determine the terminal currents and the region of operation in this transistor circuit. In forward active mode $\beta_F=100$, $V_{BEon}=0.7V$, $V_{CEsat}=0.2V$, and in reverse active mode $\beta_R=3$, $V_{BCon}=0.7V$, $V_{ECsat}=0.2V$.
(25 points)

$$V_{D_{on}} = 0.7V$$



* If we start with the assumption of forward active mode, all diode will inevitably be turned on. Therefore:

$$V_X = 5V - V_{D_{on}} = 4.3V \rightarrow 4.3V = I_B \times 290k + 0.7V + 0.7V$$

$$\rightarrow I_B = \frac{4.3V - 0.7V - 0.7V}{290k} = \frac{2.9V}{290k} = \underline{\underline{0.01mA}} \rightarrow I_B > 0 \checkmark$$

$$\underline{\underline{I_C = 1mA}} \quad \underline{\underline{I_E = 1.01mA}}$$

$$V_B = 0.7V \quad V_E = 0 \quad V_C = 5V - 0.7V - 0.7V = 3.6V \rightarrow$$

$$V_{CE} = 3.6V > V_{CEsat} \checkmark \checkmark$$

$$I_{D_1} = I_B > 0 \checkmark$$

$$I_{D_2} = I_B + I_C > 0 \checkmark$$

$$I_{D_3} = I_C > 0 \checkmark$$

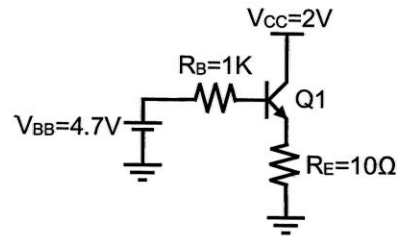
All initial assumption are correct and therefore the transistor is in the forward active mode and all diodes are on.

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4- Determine the terminal currents and the region of operation in this transistor circuit. In forward active mode $\beta_F=99$, $V_{BEon}=0.7V$, $V_{CEsat}=0.2V$, and in reverse active mode $\beta_R=3$, $V_{BCon}=0.7V$, $V_{ECsat}=0.2V$.



(25 points)

Assuming forward active mode:

$$\text{KVL @ base: } 4.7V = I_B \times 1k + 0.7V + \underbrace{(1+\beta)I_B}_{I_E} \times 10\Omega$$

$$\rightarrow I_B = \frac{4V}{1k \times 100 \times 10\Omega} = \frac{4V}{2k} = 2mA \rightarrow I_B > 0$$

$$I_C = 198mA \quad I_E = 200mA$$

$$\text{However: } V_C = 2V \text{ and } V_E = 10\Omega \times 200mA = 2V \rightarrow$$

$V_{CE} = 0 < V_{CEsat}$ X Transistor likely in saturation.

Resolve assuming saturation:

$$V_E = 2V - 0.2V = 1.8V \rightarrow$$

$$I_E = \frac{1.8V}{10\Omega} = 180mA$$

$$I_B = \frac{4.7V - 0.7V - 1.8V}{1k} = \frac{2.2V}{1k} = 2.2mA > 0 \checkmark$$

$$\rightarrow I_C = I_E - I_B = 177.8mA < \beta I_B = 99 \times 2.2mA \quad \checkmark \checkmark$$

Transistor is in saturation

