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Name: [redacted]
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EE115A Midterm

Tuesday, November 8, 2005

Name (person on left): [redacted] Name (person on right): [redacted]

(3 points for name on all pages, 2 points for names of persons on left and right)

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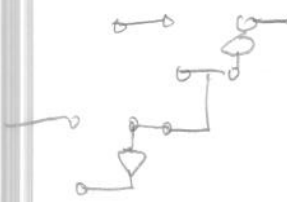
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2/2

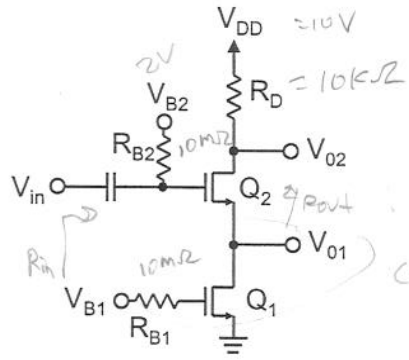
Note: For all problems, assume $\lambda = 0$ (infinite output resistance), each discrete capacitor has $C = \infty$, and the body effect is negligible. Assume the thermal voltage is $V_T = 25\text{mV}$.
0.025 V.

Problems 1 - 2:

$I_{D2} = I_{D1}$



CP



$V_{DS} \geq V_{GS} - V_T$

const curr source

$V_{DD} \geq V_{GS} - V_T$

Figure P1.

With some thoughtful design, the circuit in Figure P1 can be used as either a linearized common-source amplifier or source follower. Assume each transistor has identical $k_n'(W/L) = 1\text{ mA/V}^2$ and $V_t = 1.1\text{ V}$. $R_D = 10\text{ k}\Omega$, $R_{B1} = R_{B2} = 10\text{ M}\Omega$ and $V_{DD} = 10\text{ V}$. You have access to a 5V source and a 2V source (as well as ground and the 10V supply) to use as biases.

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Problem 1 (20 points):

19/20 good job!

Assume you want to use the circuit in Figure P1 as a source follower.

- a) (5 pts): From the following regions of operation of the MOS (off, deep triode, triode and saturation), which operating region is most desirable for Q_1 ? Why? \Rightarrow out of Q_2 's source
- Saturation, since we want a constant current flowing from $Q_2 \rightarrow Q_1$. Since, current is more stable after pinchoff, I_D won't fluctuate as widely as in triode or not at all when off.*

5/5

- b) (5 pts): What bias(es) would work for V_{B1} and V_{B2} ? Why?

$V_{B2} = 5V$ $V_{B1} = 2V$

Because to keep both MOS's in saturation, $V_{DS} \geq V_{GS} - V_{th}$. Therefore in general, the drain voltage has to be $>$ the gate voltage. If we use any other values, we will get conflicting current on the same line or $I = 0$.

5/5 good!

Assume you want to use the circuit in Figure P1 as a linearized common-source amplifier.

- c) (5 pts): From the following regions of operation of the MOS (off, deep triode, triode and saturation), which operating region is most desirable for Q_1 ? Why?
- Triode. Since we need maximum output swing across the drain at Q_2 , and want Q_1 to act like a linear resistor to stabilize the current by way of feedback, then Q_1 becomes*

4/5
max
deep

V_{O2} output

$r_{DS} = \frac{1}{\mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{th})}$

- d) (5 pts): Assume you select $V_{B2} = 2V$. What bias should you use for V_{B1} to maximize the gain? Why?

The smaller $r_{DS} = R_S$ is the greater the gain is ($A_v = \frac{-g_m R_D}{1 + g_m R_S}$)
Therefore $V_{B1} = V_{th}$ should be the highest voltage value possible, $= 10V = V_{DD}$, to minimize r_{DS} and max gain.

5/5

Problem 1 (20 points):

Assume you want to use the circuit in Figure P1 as a source follower.

- a) (5 pts): From the following regions of operation of the MOS (off, deep triode, triode and saturation), which operating region is most desirable for Q_1 ? Why?

- b) (5 pts): What bias(es) would work for V_{B1} and V_{B2} ? Why?

Assume you want to use the circuit in Figure P1 as a linearized common-source amplifier.

- c) (5 pts): From the following regions of operation of the MOS (off, deep triode, triode and saturation), which operating region is most desirable for Q_1 ? Why?

- d) (5 pts): Assume you select $V_{B2} = 2V$. What bias should you use for V_{B1} to maximize the gain? Why?

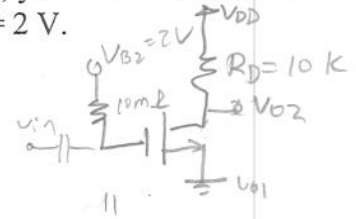
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Problem 2 (15 points):

When using the circuit in Fig. P1 as a common-source amplifier, you decide to increase the gain further by connecting V_{B2} directly to ground. Let $V_{B2} = 2\text{ V}$.



a) (5 pts) Calculate the gain at the quiescent point.

$$A_v = \frac{v_{out}}{v_{in}} = \frac{g_m v_{gs} R_D}{v_{gs}} \quad v_{in} = v_{gs}$$

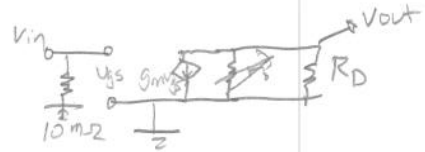
$$A_v = g_m R_D$$

$$= 0.9 \frac{\text{mA}}{\text{V}} (10 \text{ K}) = \boxed{9 \text{ V/V}}$$

$$I_D = \frac{1}{2} k_n' \frac{W}{L} (V_{GS} - V_t)^2 = \frac{1}{2} \left(\frac{1 \text{ mA}}{\text{V}^2} \right) (2 - 0 - 1.1)^2$$

$$g_m = k_n' \frac{W}{L} V_{ov} = (1 \text{ mA/V}^2) (2 - 0 - 1.1)$$

$$= 0.9 \text{ mA/V}$$



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b) (10 points) Calculate the maximum input voltage swing for which Q_2 is still saturated.

$V_{DD} = 10$

$$V_D = V_{GS} - V_t$$

$$I_D = \frac{V_{DD} - V_D}{10 \text{ K}} = \frac{1}{2} \left(\frac{1 \text{ mA}}{\text{V}^2} \right) (0.9)^2$$

$$\frac{10 - V_D}{10 \text{ K}} = 0.405 \text{ mA}$$

$$V_D = 5.95 \text{ V}$$

7/10

$$V_{DD} = 5.95 \text{ V}$$

$$V_{GS} - V_t = V_D - V_t = 0.9$$

$V_{DD} - V_{ov} = 5.05 \text{ V}$ output swing

$$A_v = \frac{-2(V_{DD} - V_{DD})}{V_{ov}} = \frac{-2(10 - 5.95)}{0.9} = 9$$

$$A_v = \frac{v_o}{v_i} =$$

$$v_{in} = \frac{v_{ov}}{A_v} = \frac{5.05 \text{ V}}{9} = \boxed{\pm 0.561 \text{ V input swing}}$$

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Problem 3 (30 pts, 10 points each for a, b, c):

Find the gain, input and output resistances for these circuits. Assume Q_2 is in saturation.

Hint: For (c), $R_{in} = R_B \parallel R_{in}$ (from all other elements).

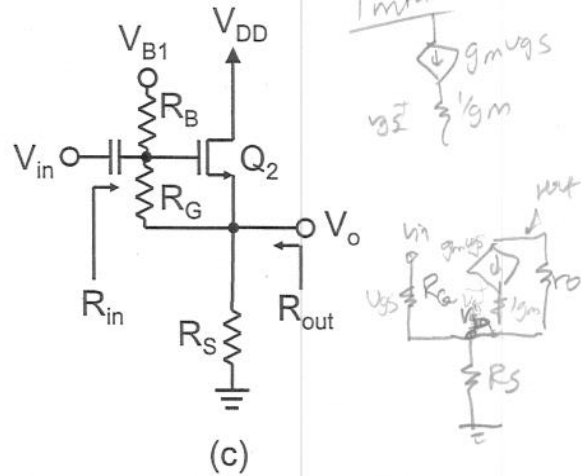
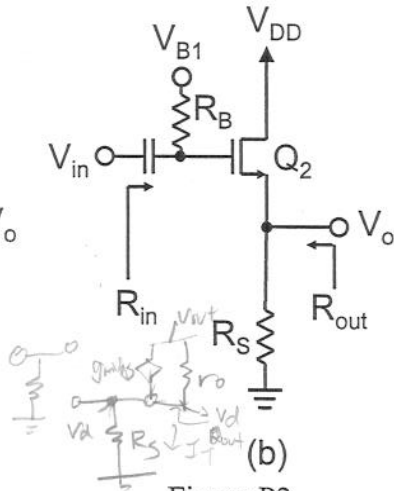
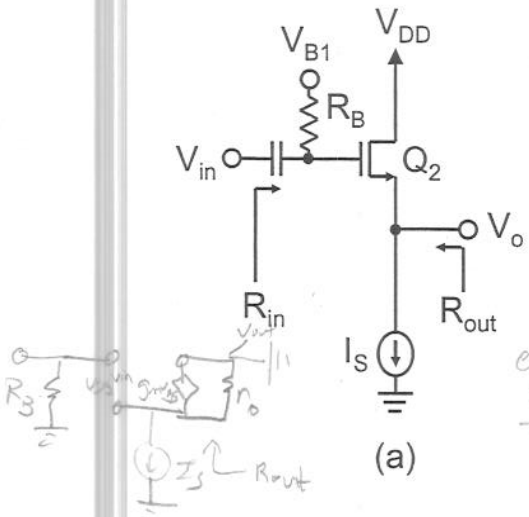


Figure P2.

a) $R_{in} = R_B$
 $R_{out} = r_o$ 3/10
 $A_v = \frac{v_o}{v_i} = \frac{g_m v_{gs} r_o}{g_m v_{gs}}$
 $A_v = r_o$

b) $R_{in} = R_B$
 $R_{out} = R_S \parallel \frac{1}{g_m}$
 $A_v = \frac{v_o}{v_i} = \frac{g_m v_{gs} (R_S \parallel \frac{1}{g_m})}{v_{gs}}$

c) $R_{in} = R_B \parallel R_{in}$
 $R_{in} = R_B \parallel (R_G + R_S)$
 $R_{out} = \frac{1}{g_m} \parallel R_G \parallel R_S$
 $A_v = \frac{v_{out}}{v_{in}} = g_m (r_o \parallel R_G \parallel R_S)$
 $\frac{v_{out} - v_{gs}}{1/g_m} = g_m v_{gs} \Rightarrow v_{out} = 2v_{gs}$
 $v_{in} - v_D = v_{gs}$
 $\frac{v_D}{R_S} = g_m v_{gs} + \frac{v_{out} - v_D}{r_o} + \frac{v_{in}}{R_B}$
 $v_{in} - v_D = v_{gs}$

$\frac{1}{g_m} = r_o$

$I_t = g_m v_{gs} + \frac{v_{out} - v_D}{r_o} = \frac{v_D}{R_S}$
 $v_D = I_t R_S$
 $I_t = g_m v_{gs} + \frac{v_{out} - I_t R_S}{r_o}$
 $A_v = g_m r_o$

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Problem 4: (20 pts):

Diode D_1 has $I_S = 1 \times 10^{-15}$ and $n = 1.1$. Find I_1 for $V_{DD} =$ (a) 10 V, and (b) 1 V. Points are awarded based on error from the exact answer.

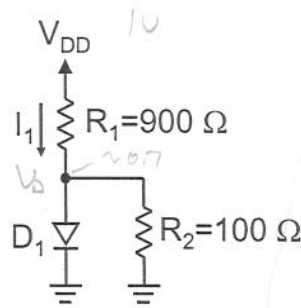


Figure P3.

$$I = I_S e^{V_D / nV_T}$$

$$= 1 \times 10^{-15} e^{V_D / (1.1)(25mV)}$$

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$$b) \frac{1 - V_D}{900} = 10^{-15} e^{V_D / 0.0275} + \frac{V_D}{100}$$

$$\frac{1 - 10V_D}{900} = 10^{-15} e^{V_D / 0.0275}$$

$$\frac{1 - 900 \times 10^{-15} e^{V_D / 0.0275} = V_D}{10}$$

set $V_D = 0.7V$

$V_D = 0.0897V$ ~? - can't be real

if off $V = 0.7V$

$$\frac{V_{DD} - 0.7V}{900\Omega} = I_1 = \frac{1 - 0.7}{900}$$

$$I = 0.33mA$$

if off

$$\frac{V_{DD} - 0}{900 + 100} = I_1 = \frac{1}{1000} =$$

$$I = 1mA$$

10/10

→ use iteration

a)

$$\frac{V_{DD} - V_D}{900\Omega} = I_1$$

$$I_1 = I_{D1} + I_{R2}$$

$$\frac{V_{DD} - V_D}{900\Omega} = 1 \times 10^{-15} e^{V_D / (1.1)(25mV)} + \frac{V_D}{100\Omega}$$

$$\frac{10 - V_D}{900\Omega} = 10^{-15} e^{V_D / 0.0275} + \frac{V_D}{100\Omega}$$

$$\frac{10 - V_D}{900} - \frac{9V_D}{900} = 10^{-15} e^{V_D / 0.0275}$$

$$\frac{1 - V_D}{90} = 10^{-15} e^{V_D / 0.0275}$$

$$1 - 90 \times 10^{-15} e^{V_D / 0.0275} = V_D$$

assume $V_D = 0.7V$

$$1 - 90 \times 10^{-15} e^{0.7 / 0.0275} = V_D = 0.989 = V_D$$

$$I_1 = \frac{V_{DD} - V_D}{900\Omega} = \frac{10V - 0.989V}{900\Omega} = 9.998mA$$

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need to consider $I_2 = I$ (through R_2)

10/10

Problem 5 (10 points):

Assume you have a transconductor with the current vs. voltage plot shown in Figure P4.

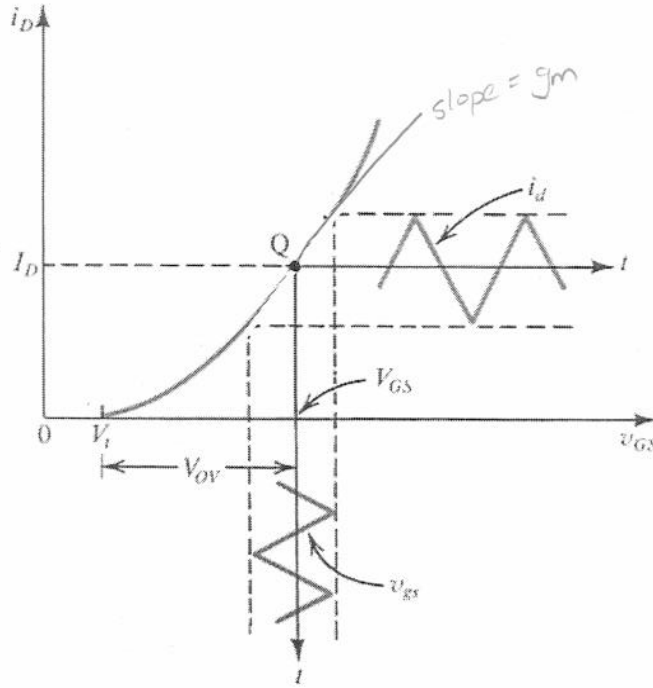


Figure P4.

a) (3 points) Express the transconductance as a function of the small-signal current i_d and small-signal voltage v_{gs} .

$$g_m = \frac{\partial i_d}{\partial v_{gs}} = \frac{k'_n W}{2L} (V_{GS} - V_t)$$

3/3

b) (3 points) Express the transconductance as a function of the large-signal current i_D and large-signal voltage v_{GS} .

$$g_m = \frac{\partial i_D}{\partial v_{GS}} \Big|_{v_{GS} = V_{GS}}$$

3/3

c) (4 points) Do the definitions lead to the same or different results at the bias point? Why?

4/4 Yes, the output current and input voltage swing at point Q, whether at small or large signal, swings off the slope (= g_m) at point Q, If large signal gets too large, the slope is no longer linear everywhere and starts differing from small signal [6]

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Problem 1:

a) A source follower (output taken at V_{o1}) works best when the source is connected to an ideal current source - the gain is then $= 1$. If Q_1 is saturated, it acts as a current source with the current set by V_{B1} .

Saturation

b) Since Q_1 should be saturated, $V_{o1} \geq V_{B1} - V_t$.

(this means $V_{B2} - V_{B1} \geq V_{B1} - V_t$ or $V_{B2} \geq V_{B1} + V_{B1} - V_t$). Since current is flowing through the transistor, $V_{B2} > V_t$ and thus $V_{B2} > V_{B1}$. We then try various values of V_{B1} .

V_{B1}	$V_{B2} = V_{DD} - \frac{1}{2} K_n \frac{W}{L} (V_{B1} - V_t)^2 R_D$	Current
0	4.5	5.95
2	-78	-66
5		

Version 1
Version 2

$V_{B2} > V_{B1} - V_t$ and

In either case, if $V_{B2} = 5V$,

Q_2 is saturated.

Thus $V_{B1} = 2V$, $V_{B2} = 5V$

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Problem 1 (cont)

To operate as a common-source amp, take the output at V_{o2} . To increase the gain, the source resistor should be placed between the source of Q_2 and ground, when a MOS is in deep triode, it acts as a voltage-controlled resistor.

Deep triode

In deep triode, $I_D \approx K_n \frac{W}{L} (V_{GS} - V_t) V_{DS}$

so $R_{DS} = \frac{V_{DS}}{I_D} \approx \frac{1}{K_n \frac{W}{L} (V_{GS} - V_t)}$

To increase the gain, you need to decrease R_{DS} so V_{GS} must be as high as possible.

$V_{B1} = 10V$

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Problem 2) Ver. 1

a) $g_m = 1.1 \text{ mA/V} \times (2-1) = 1.1 \text{ mA/V}$

$AV = -g_m R_D = -1.1 \times 10^6 = -1.1 \text{ V/V}$

b) input:



$V_{out} = V_{DD} - R_D \frac{K'_n}{2} \left(\frac{W}{L}\right) (V_{GS} - V_t)^2$

@ the edge of sat. $V_{out} = V_{GS} - V_t$

$\Rightarrow V_{out} = V_{DD} - R_D \frac{K'_n}{2} \left(\frac{W}{L}\right) V_{out}^2$

$10^6 \times \frac{10^{-4}}{2} V_{out}^2 + V_{out} - 10 = 0 \rightarrow 5.5 V_{out}^2 + V_{out} - 10 = 0$

$V_{out} = \frac{-1 + \sqrt{1 + 4 \times 5.5}}{2 \times 5.5} = 1.26$

$\Rightarrow V_{GS} = V_{out} + V_t = 1 + 1.26 = 2.26$

$\rightarrow A = V_{GS_{max}} - 2 = 0.26 \text{ V}$

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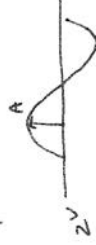
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problem 2) Ver. 2

a) $g_m = 1 \text{ mA/V} \times (2-1) = 0.9 \text{ mA/V}$

$AV = -g_m R_D = -0.9 \text{ mA/V} \times 10^6 = -0.9 \text{ V/V}$

b) input:



$V_{out} = V_{DD} - R_D \frac{K'_n}{2} \left(\frac{W}{L}\right) (V_{GS} - V_t)^2$

@ the edge of sat. $V_{out} = V_{GS} - V_t$

$\Rightarrow V_{out} = V_{DD} - R_D \frac{K'_n}{2} \left(\frac{W}{L}\right) V_{out}^2$

$10 \times \frac{1 \text{ mA/V}}{2} V_{out}^2 + V_{out} - 10 = 0$

$V_{out} = \frac{-1 + \sqrt{1 + 4 \times 5}}{10} = 1.32$

$\rightarrow V_{GS} = V_{out} + V_t = 1.32 + 1 = 2.32$

$A_{in} = V_{GS_{max}} - 2 = 0.32 \text{ V}$

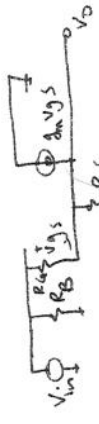
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Problem 3)

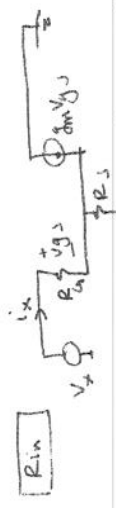
a) $R_{in} = R_B$
 $A_V = 1$
 $R_{out} = \frac{1}{g_{m2}}$

b) $R_{in} = R_B$
 $A_V = \frac{g_{m2} R_S}{1 + g_{m2} R_S}$
 $R_{out} = \frac{1}{g_{m2}} \parallel R_S$

gain



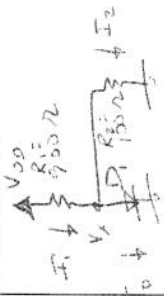
$V_{in} = v_{gs} + v_o$
 $-g_{m2} v_{gs} + \frac{v_o}{R_S} - \frac{v_o}{R_L} = 0$
 $\Rightarrow \frac{v_o}{R_S} = \frac{v_o}{R_L} + g_{m2} v_{gs}$
 $\Rightarrow \frac{v_o}{v_{gs}} = \frac{1}{\frac{1}{R_S} - \frac{1}{R_L}} = \frac{R_S R_L}{R_L - R_S}$
 $\Rightarrow \frac{v_o}{v_{in}} = \frac{v_o}{v_{gs} + v_o} = \frac{1}{1 + \frac{R_S R_L}{R_L - R_S} \frac{1}{g_{m2}}}$



$i_x = v_{gs} / R_{in}$
 $v_x = v_{gs} + (i_x \parallel g_{m2} v_{gs}) R_S = v_{gs} [1 + g_{m2} R_S + R_S / R_{in}]$
 $v_o = v_x \parallel R_L = \frac{v_x R_L}{R_L + R_{in}}$

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Problem 4:



a) $V_{DD} = 10V$. To get first approximation, use constant voltage drop model. IF we assume D_1 is FF, $V_{x1} = 1V$ so its safe to assume D_1 is on and $V_{x1} = 0.7V$. Thus $I_1 = \frac{V_{DD} - 0.7}{R_1} = 10.3mA$.

Upon further iteration, one would get

V_x	I_1	$I_2 = \frac{V_x}{R_2}$	$I_D = I_1 - I_2$	V_x	I_1	$I_2 = \frac{V_x}{R_2}$	$I_D = I_1 - I_2$
0.7	10.33mA	7mA	3.33mA	0.7	10.33mA	7mA	3.33mA
0.718	10.31mA	7.18mA	3.12mA	0.718	10.23mA	7.18mA	3.05mA
0.717	10.31mA	7.17mA	3.14mA	0.718	10.24mA	7.18mA	3.06mA

Version 1

Version 2

b) $V_{DD} = 1V$: IF we assume D_1 is FF, $V_x = 0.1V$, which is consistent, IF we assume D_1 is on, $I_2 > I_1$, which is impossible. (Note this assumes constant-voltage drop model. For an ideal model, the turn-on voltage = 0V, so D_1 could be on if you stuck with an ideal model.) then

$I_D = \frac{1V}{R_1} = 1mA$

How much current goes through D_1 ? For $V_x = 0.1V$,

$I_D = I_S (e^{V_x / V_T} - 1) \approx I_S e^{V_x / V_T}$

$I_D \approx \begin{cases} 1.4e^{-14} A & \text{Version 1} \\ 2.2e^{-14} A & \text{Version 2} \end{cases}$

These currents are negligible so $I_D \approx I_S = 1mA$

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Problem 5)

a) $g_m = \frac{i_d}{v_{gs}}$

b) $g_m \triangleq \frac{\partial i_D}{\partial v_{gs}}$

c) if v_{gs} is small enough so that the slope is roughly constant, both lead to the same result.