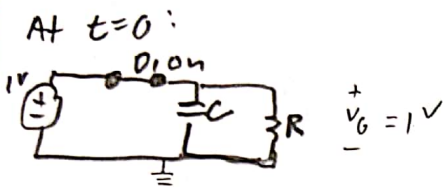
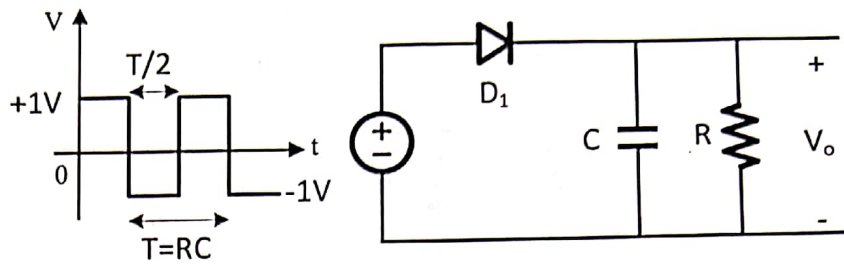
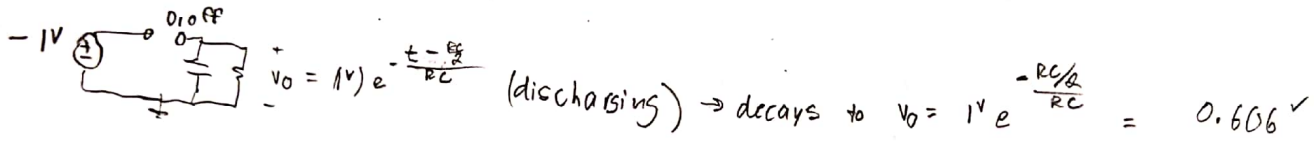


1. Plot the output voltage ( $V_o$ ) waveform for the peak detector shown below. The diode is ideal.

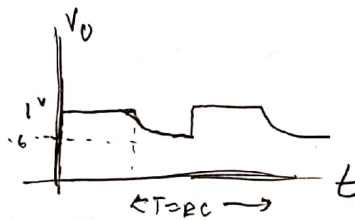
20



At  $t = \frac{RC}{2}$ :



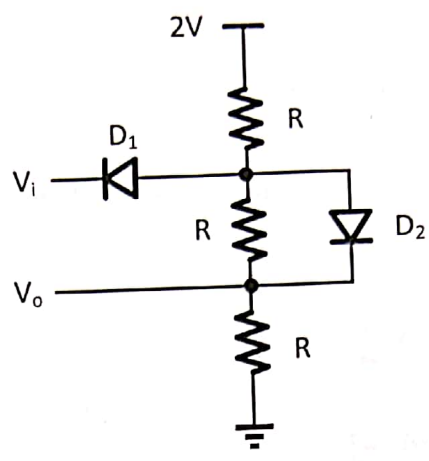
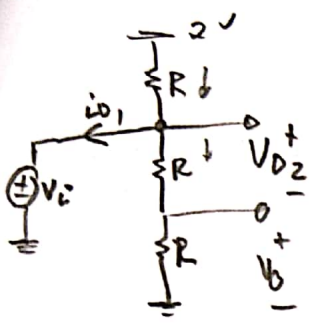
At  $t = RC$ :  $V_o = 1V$  again and the cycles repeat.



2. For the following circuit, plot the output-input characteristics. The diodes are ideal.

Sweep from  $V_i = -\infty$  to  $\infty$

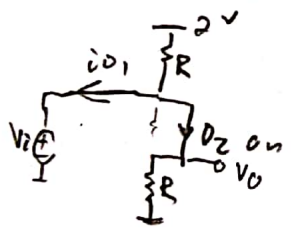
at  $V_i = -\infty$ ,  $D_1$  on,  $D_2$  off



KCL:  $i_{D1} = \frac{2-V_i}{R} - \frac{V_i}{2R} > 0$  ✓ turns off at  $V_i = \frac{4}{3}V$  ←  $2 - \frac{3}{2}V_i = 0$

voltage divider →  $V_{D2} = \frac{V_i}{2} < 0$  turns on at  $V_i \geq 0V$  and  $V_o = \frac{V_i}{2}$

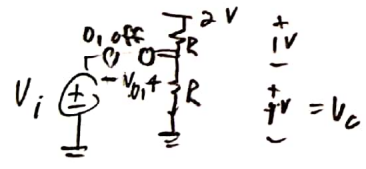
$D_2$  turns on first: (at  $V_i \geq 0V$ )



$i_{D1} = \frac{2-V_i}{R} - \frac{V_i}{R} > 0$  ✓ check  $i_{D2} = \frac{V_i}{R} > 0$  ✓

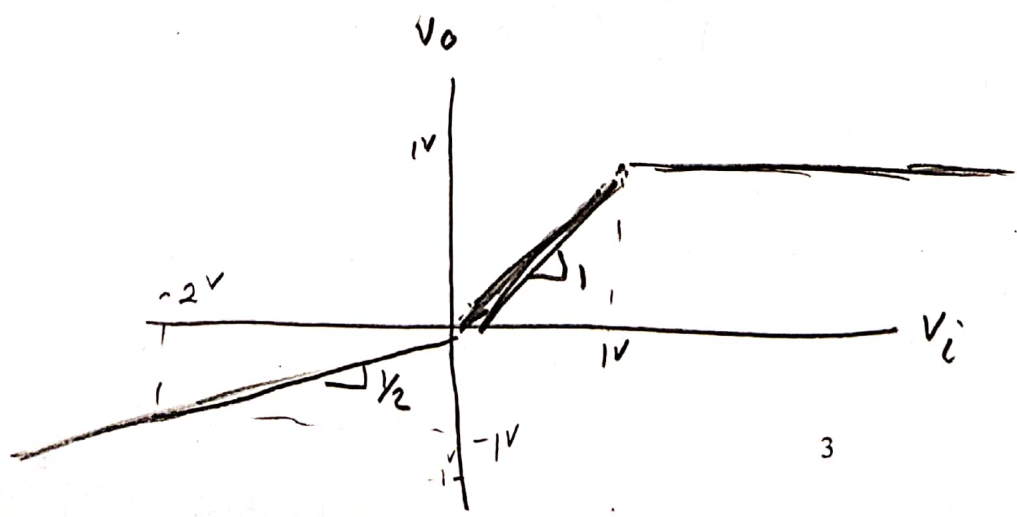
$D_1$  turns off when  $V_i = 1V$  and  $V_o = V_i$

at  $V_i > 1V$ :



voltage divider:  $V_o = 2V \left( \frac{R}{2R} \right) = 1V$

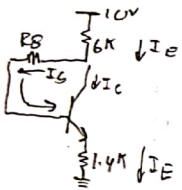
$i_{D2} = 1V \cdot R > 0$  ✓ remains on  
 $V_{D1} = -V_i < 0$  ✓ off



$$V_{BE} = \frac{kT}{q} = 25 \text{ mV}$$

3. A self-biased CE amplifier is shown below.
- Find the transistor DC operating points. Check what region of operation the transistor is biased at.
  - Calculate the small signal voltage gain  $\frac{v_o}{v_s}$ , and the input impedance of the amplifier,  $R_{in}$ . The resistor  $R_B$  may be ignored.
  - Calculate the small signal voltage gain  $\frac{v_o}{v_s}$  without ignoring  $R_B$ .
- $V_{BE,ON} = 0.6V$ ,  $\beta = 100$ , and  $V_A = \infty$ .

Assume FA.  
a) DC analysis:



$$\text{KVL: } 10V = 6k I_E + 202k I_B + V_{BE,ON} + 1.4k I_E$$

$$10V - 0.6 = 7.4k (\beta + 1) I_B + 202k \cdot I_B$$

$$\frac{9.4V}{(7.4k)(101) + 202k} = I_B$$

$$I_B = 0.0099 \text{ mA} \approx I_B \approx 0.01 \text{ mA}$$

$$\begin{aligned} I_E &\approx 1 \text{ mA} \\ I_C &= 0.99 \text{ mA} \end{aligned}$$

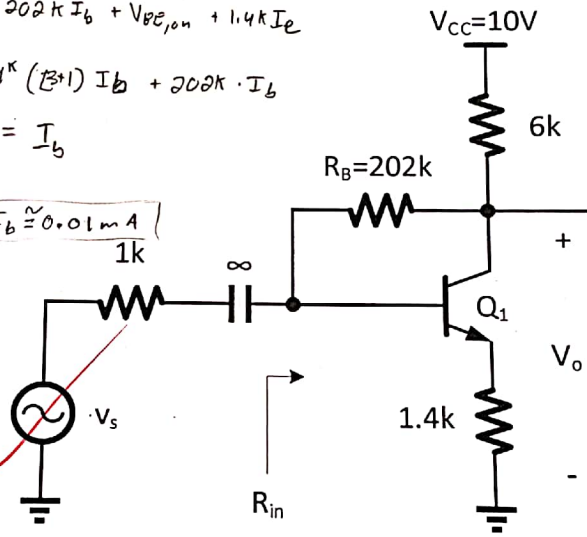
$$I_E = (\beta + 1) I_B \rightarrow$$

$$I_C = \beta I_B \rightarrow$$

$$V_{CE} = 10V - 6k \cdot I_E \Rightarrow V_{CE} = 4V$$

$V_{CE} = 4V > V_{CE,sat}$  ✓ Forward Active Mode.

Problem 3b,c on attached paper.



3.

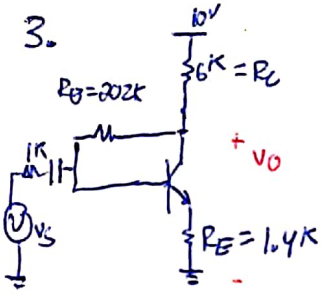
a) DC:

$$(I_B + I_C)R + I_B R_B + V_{BE(on)} + I_E R_E = 10V$$

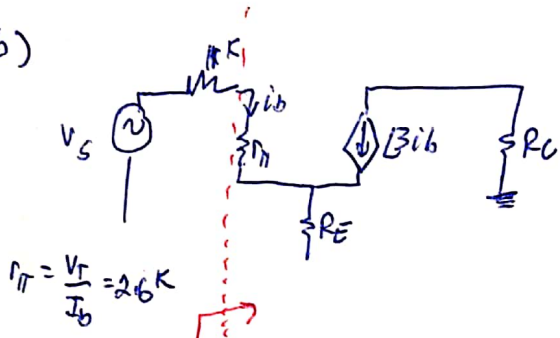
$$I_C \approx 1mA$$

$$I_B = \frac{I_C}{\beta}$$

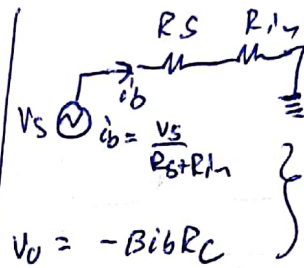
FA region ✓



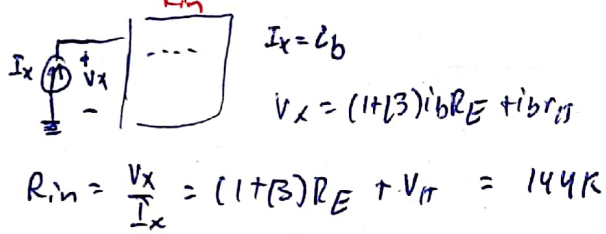
b)



$$r_{\pi} = \frac{V_T}{I_B} = 2.6K$$



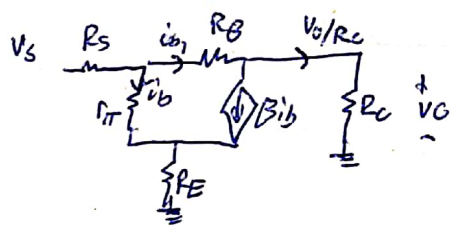
$$\frac{V_O}{V_S} = \frac{\beta R_C}{R_{in} + R_S} = \boxed{-4.1}$$



$$V_x = (1 + \beta) i_b R_E + i_b r_{\pi}$$

$$R_{in} = \frac{V_x}{I_x} = (1 + \beta) R_E + r_{\pi} = 144K$$

c)



$$i_{c1} = \beta i_b + V_O / R_C$$

outer loop:

$$V_S = R_S (i_b + i_{c1}) + R_B i_{c1} + V_O$$

left loop:

$$V_S = R_S (i_b + i_{c1}) + i_b r_{\pi} + (1 + \beta) i_b R_E$$

$$\frac{V_O}{V_S} = \boxed{-3.9}$$