

EE 10

Final Exam

Fall 2012

Your Name:

Solutions

Name of Person to Your Left:

Name of Person to Your Right:

Time Limit: 3 Hours

Open Book, Open Notes.

Where applicable, place answers inside designated boxes.

Make sure your name is checkmarked on the class list when you turn in your exam.

1. 10

2. 10

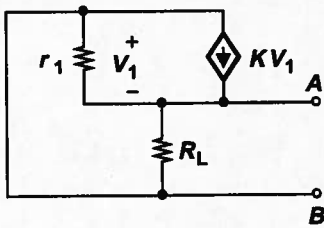
3. 10

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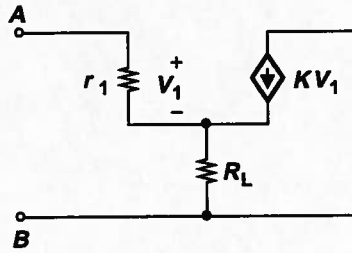
5. 10

Total: 50

1. For each circuit shown below, determine the resistance between terminals A and B.

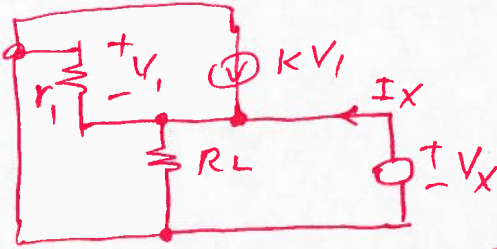


(a)



(b)

(a)



$$V_1 = -V_x, \quad KV_1 = -KV_x$$

$$\text{Current thru } R_L = V_x / R_L$$

$$\text{KCL: } -I_x + \frac{V_x}{r_1} + \frac{V_x}{R_L} + KV_x = 0$$

$$\Rightarrow \frac{V_x}{I_x} = \frac{1}{\frac{1}{r_1} + \frac{1}{R_L} + K}$$

(b)



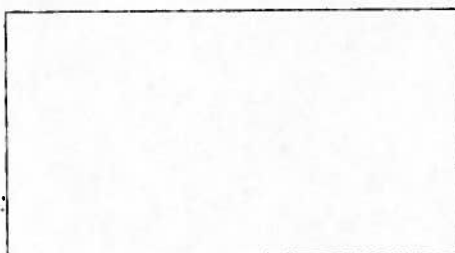
$$V_1 = I_x r_1$$

$$\text{KCL@A: } I_x + K I_x r_1 = \text{current flowing thru } R_L$$

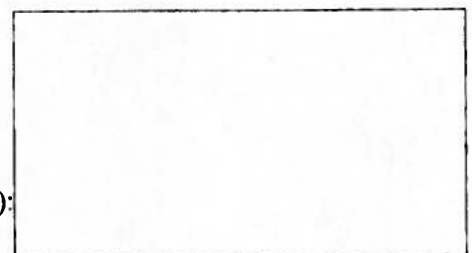
$$\text{KVL around input loop: } V_x = V_1 + V_{R_L} = I_x r_1 + (I_x + K I_x r_1) R_L$$

$$\Rightarrow \frac{V_x}{I_x} = r_1 + (1 + K r_1) R_L$$

Answer to (a):



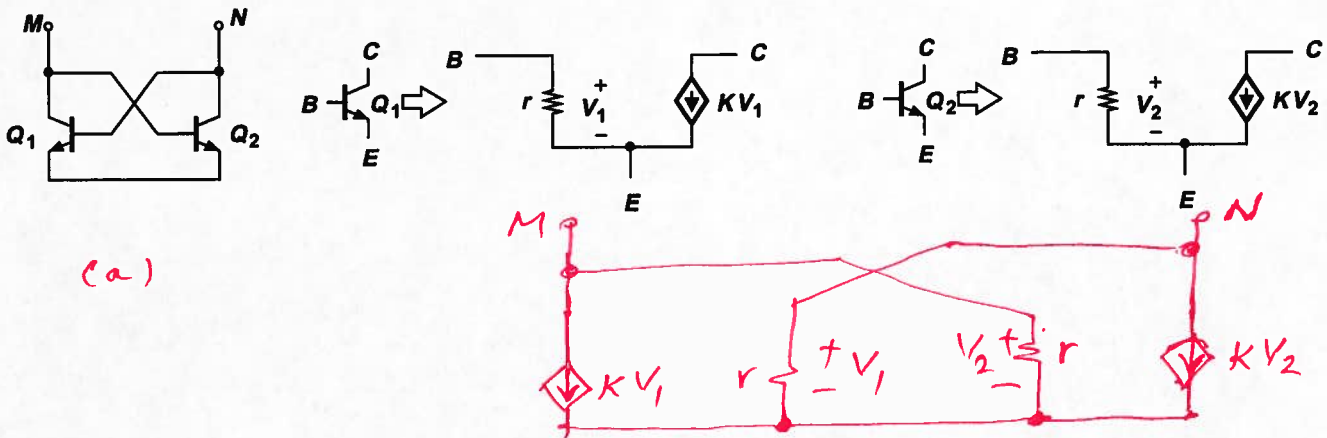
Answer to (b):



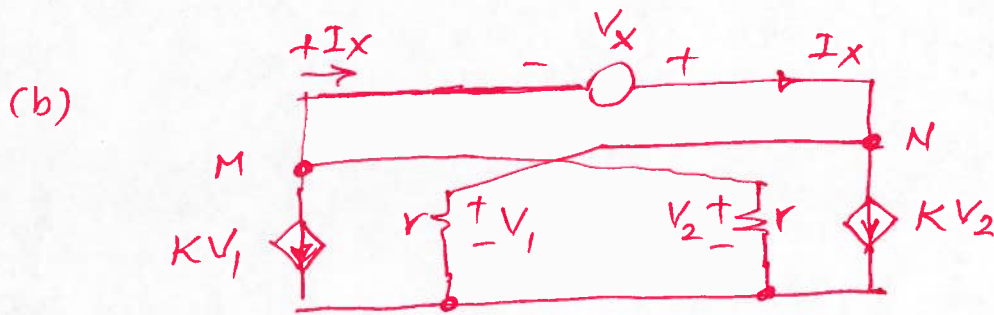
2. The circuit shown below consists of two transistors, Q_1 and Q_2 .

(a) Replace each transistor with its corresponding model. Note that each transistor has a resistance of r but the voltage across r is V_1 for Q_1 and V_2 for Q_2 . (K is the same for both transistors.)

(b) Now determine the resistance seen between terminals M and N . Your expression must not be a function of V_1 or V_2 . (Hint: you need to write one KVL and two KCLs.)



(a)



(b)

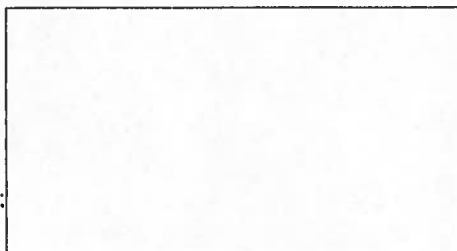
$$\begin{aligned}
 \text{KCL @ } M: \quad & +I_x + KV_1 + \frac{V_2}{r} = 0 \\
 \text{KCL @ } N: \quad & I_x - KV_2 - \frac{V_1}{r} = 0 \\
 \text{KVL:} \quad & V_x - V_1 = -\frac{V_2}{2}
 \end{aligned}
 \Rightarrow \left. \begin{aligned}
 K(V_1 + V_2) - \frac{1}{r}(V_1 + V_2) &= 0 \\
 \Rightarrow V_1 &= -V_2
 \end{aligned} \right\}$$

$$\Rightarrow V_x = V_1 - V_2 = 2V_1 = -2V_2$$

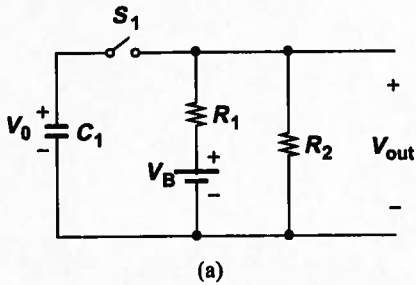
$$\Rightarrow I_x + KV_1 + \frac{V_2}{r} = 0 = I_x + K \frac{V_x}{2} - \frac{1}{r} \frac{V_x}{2} \Rightarrow$$

$$\boxed{\frac{V_x}{I_x} = \frac{-1}{\frac{K}{2} - \frac{1}{2r}}}$$

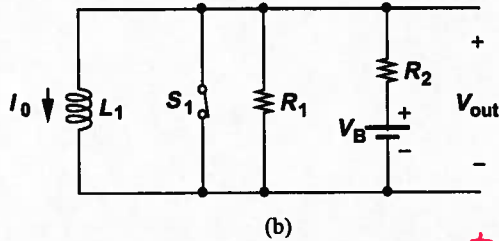
Answer to (b):



3. (a) In the circuit shown in (a), the capacitor has an initial voltage of V_0 and the switch turns on at $t = 0$. Determine V_{out} as a function of time.



(b) In the circuit shown in (b), the inductor has an initial current of I_0 and the switch turns off at $t = 0$. Determine V_{out} as a function of time.



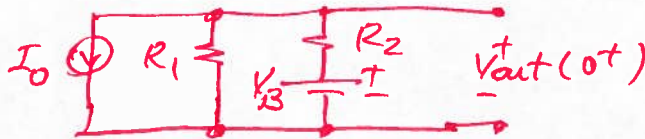
For both sections, $V_{out}(t) = V_{\infty} + (V_0 - V_{\infty})e^{-\frac{t}{\tau}}$

(a) $V_{out}(0^+) = V_0$, $V_{out}(\infty) = V_B \frac{R_2}{R_1 + R_2}$

$\tau = C_1 (R_1 || R_2)$

$\Rightarrow V_{out}(t) = V_B \frac{R_2}{R_1 + R_2} + (V_0 - V_B \frac{R_2}{R_1 + R_2}) e^{-\frac{t}{C_1 (R_1 || R_2)}} \quad t > 0$

(b) At $t=0^+$, replace L_1 with a current source equal to I_0 :



Using superposition, we have $V_{out}(0^+) = -I_0 (R_1 || R_2) + V_B \frac{R_1}{R_1 + R_2}$

At $t=\infty$, $V_{out} = 0$.

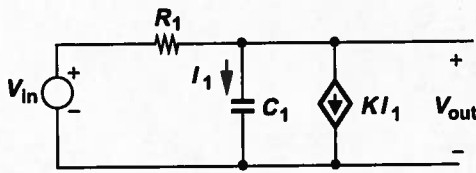
$\tau = \frac{L_1}{R_1 || R_2}$

$\Rightarrow V_{out}(t) = [-I_0 (R_1 || R_2) + V_B \frac{R_1}{R_1 + R_2}] e^{-\frac{t}{\frac{L_1}{R_1 || R_2}}} \quad t > 0$

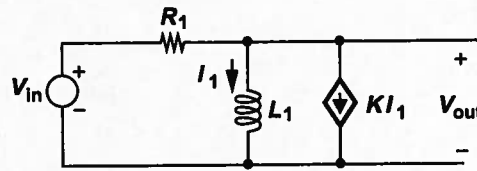
Answer to (a):

Answer to (b):

4. In the circuits shown below, the initial conditions are zero. If $V_{in} = V_0 u(t)$, determine V_{out} as a function of time.



(a)



(b)

(a) KCL at the output node:

$$\frac{V_{out} - V_{in}}{R_1} + C_1 \frac{dV_{out}}{dt} + KI_1 = 0$$

$$\Rightarrow (1+K)C_1 \frac{dV_{out}}{dt} + \frac{1}{R_1} V_{out} = V_{in}$$

First-order system with $\tau = R_1 C_1 (1+K)$

$V_{out}(0^+) = 0$, $V_{out}(\infty) = V_0$ because $I_1(\infty) = 0$.

$$\Rightarrow V_{out}(t) = V_0 \left[1 - e^{-\frac{t}{R_1 C_1 (1+K)}} \right] \quad t > 0$$

(b) It is simpler to find the inductor current first.

Since $V_{out} = L_1 \frac{dI_{L_1}}{dt}$, KCL at the output node yields:

$$\frac{L_1 \frac{dI_{L_1}}{dt} - V_{in}}{R_1} + I_{L_1} + KI_{L_1} = 0 \Rightarrow \frac{L_1}{R_1} \frac{dI_{L_1}}{dt} + (1+K)I_{L_1} = \frac{V_{in}}{R_1}$$

First-order system with $\tau = \frac{L_1}{R_1(1+K)}$

$$I_{L_1}(0^+) = 0$$

$$I_{L_1}(\infty) = \frac{V_0}{R_1(1+K)}$$

$$\Rightarrow I_{L_1}(t) = \frac{V_0}{R_1(1+K)} \left(1 - e^{-\frac{t}{\tau}} \right) \quad t > 0$$

$$\Rightarrow V_{out}(t) = L_1 \frac{dI_{L_1}}{dt} = V_0 e^{-\frac{t}{\tau}} \quad t > 0$$

Note!!

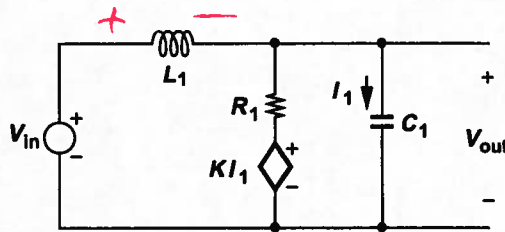
Answer to (a):

Answer to (b):

5. In the circuit shown below, the initial conditions are zero and $V_{in} = V_0 u(t)$.

(a) Derive a differential equation for V_{out} . Write the characteristic equation.

(b) If $V_0 = 0.5V$, $L_1 = 20 \text{ nH}$, $C_1 = 10 \text{ pF}$, $R_1 = 1 \text{ k}\Omega$, and $K = 200 \Omega$, determine V_{out} as a function of time.



(a) KCL at output :

Current through C_1 : $C_1 \frac{dV_{out}}{dt} = I_1$ Current through $R_1 = \frac{V_{out} - K I_1}{R_1} = \frac{V_{out} - \frac{K}{R_1} C_1 \frac{dV_{out}}{dt}}$

Current through L_1 : $\frac{V_{out}}{R_1} - \frac{K}{R_1} C_1 \frac{dV_{out}}{dt} + C_1 \frac{dV_{out}}{dt}$

\Rightarrow Voltage across inductor = $L_1 \frac{dI_{L_1}}{dt} = L_1 \frac{d}{dt} \left[\frac{V_{out}}{R_1} + C_1 \left(1 - \frac{K}{R_1}\right) \frac{dV_{out}}{dt} \right]$

KVL across V_{in} , L_1 , C_1 :

$$V_{in} = \frac{L_1}{R_1} \frac{dV_{out}}{dt} + L_1 C_1 \left(1 - \frac{K}{R_1}\right) \frac{d^2 V_{out}}{dt^2} + V_{out}$$

charac. eq. : $L_1 C_1 \left(1 - \frac{K}{R_1}\right) s^2 + \frac{L_1}{R_1} s + 1 = 0$

(b) $V_{out}(\infty) = V_0$ because $I_1(\infty) = 0$.

$$s_{1,2} = \underbrace{-6.25 \times 10^7}_{\alpha} \pm \underbrace{2.5 \times 10^9}_{\omega_d} j$$

$\Rightarrow V_{out}(t) = (A_1 \cos \omega_d t + A_2 \sin \omega_d t) e^{-\alpha t} + V_0$

$V_{out}(t=0) = 0 \Rightarrow A_1 = -V_0$
 $I_{L_1}(0) = 0 \Rightarrow -\frac{\alpha A_1}{\omega_d} = A_2$

$\Rightarrow A_2 = \frac{V_0 \alpha}{\omega_d} = 0.5 V \times (-0.025)$
 $t > 0$

Answer to (a):

Answer to (b):