EE10 Final Examination

Department of Electrical Engineering, UCLA

Winter 2012

Instructor: Prof. Puneet Gupta

DURATION: 2 hours 30 minutes

- 1. Exam is closed book. You are allowed to use a calculator and one double-sided cheat-sheet.
- 2. Cross out *everything* that you don't want me to see. Points will be deducted for anything wrong!
- 3. Do NOT use Laplace Transforms to solve any problems.
- 4. Answers with ANY invalid, missing or incomplete reasoning or explanation will not receive full credit.

SOLUTIONS --

5. Indicate proper units to all quantities (voltages, currents, power, etc.).

Name:

Student ID:

Student on Left:

Student on Right:

Student in Front:

PROBLEM	MAXIMUM SCORE	YOUR SCORE
1	10	
2	16	
3	12	
4	5	
5	7	
Total	50	

(a) Two networks N1 and N2 are described by their *i-v* characteristics as shown in Figure 1 below. All currents are in Amperes and voltages in Volts.

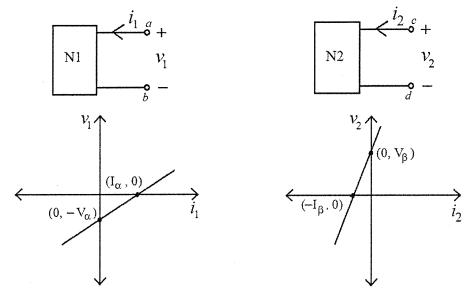
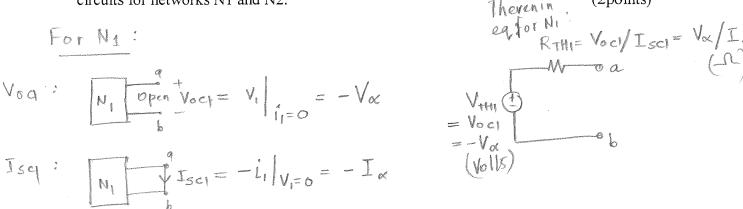


Figure 1

(i) Using the information from the above characteristics, derive the Thevenin equivalent circuits for networks N1 and N2. (2points)



For N2:

$$V_{6C2}$$
:

 V_{6C2} :

 V_{2} | V_{3} | V_{4} | V_{4} | V_{5} | V

Page 2 of 13

(ii) Networks N1 and N2 of Figure 1 are connected to form a circuit as shown in Figure 2 below. Find v_1 and v_2 . Leave your answers in terms of I_{α} , V_{α} , I_{β} , V_{β} and R. (3 points)

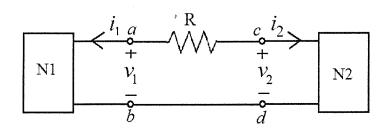


Figure 2

Using the Thevenin Equivalents for N1 & N2 that we found in the earlier part,

$$V_{1} = V_{1H1} - IR_{1H1} = -V_{\alpha} - \left(\frac{-V_{\alpha} - V_{\beta}}{V_{\alpha} + V_{\beta} + R}\right), \frac{V_{\alpha}}{I_{\alpha}}$$

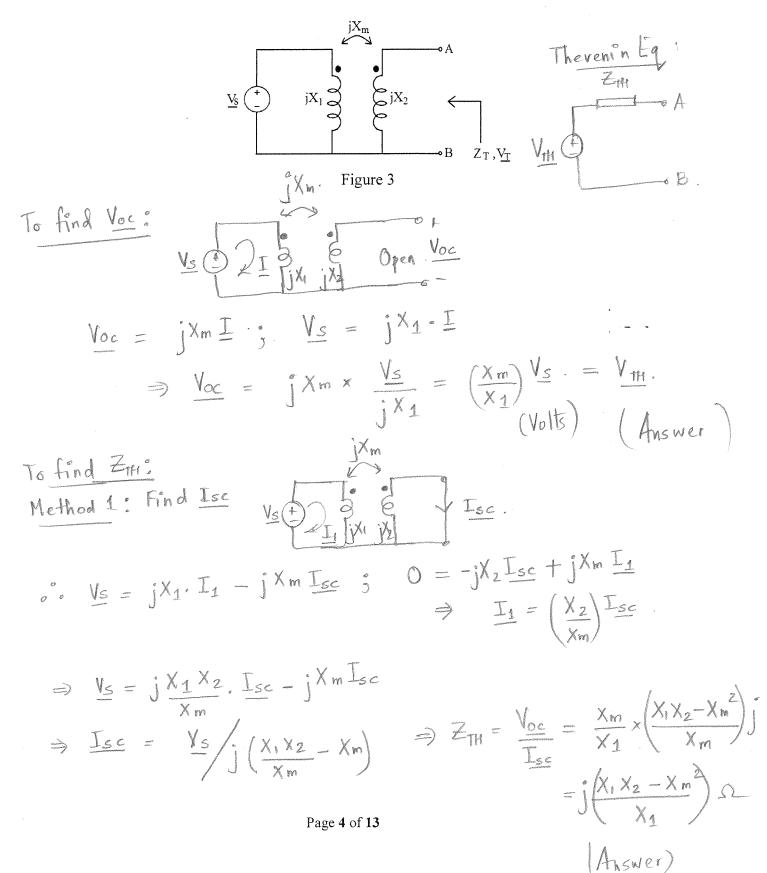
$$= -V_{\alpha} + \frac{V_{\alpha}^{2} + V_{\alpha} V_{\beta}}{V_{\alpha} + V_{\beta} \left(\frac{I_{\alpha}}{I_{\beta}}\right) + I_{\alpha}R} \quad V_{\alpha} = V_{\beta} + \frac{V_{\alpha} V_{\beta} + V_{\beta} + V_{\beta}}{V_{\alpha} + V_{\beta} + R} \quad V_{\alpha} = V_{\beta} + \frac{V_{\alpha} V_{\beta} + V_{\beta}}{V_{\alpha} I_{\beta}} \quad V_{\alpha} = V_{\beta} + \frac{V_{\alpha} V_{\beta} + V_{\beta}}{I_{\alpha}} \quad V_{\alpha} = V_{\beta} + \frac{V_{\alpha} V_{\beta} + V_{\beta}}{I_{\alpha}} \quad V_{\alpha} = V_{\beta} + \frac{V_{\alpha} V_{\beta} + V_{\beta}}{I_{\alpha}} \quad V_{\alpha} = V_{\beta} + \frac{V_{\alpha} V_{\beta} + V_{\beta}}{I_{\alpha}} \quad V_{\alpha} = V_{\beta} + \frac{V_{\alpha} V_{\beta} + V_{\beta}}{I_{\alpha}} \quad V_{\alpha} = V_{\beta} + \frac{V_{\alpha} V_{\beta} + V_{\beta}}{I_{\alpha}} \quad V_{\alpha} = V_{\beta} + \frac{V_{\alpha} V_{\beta}}{I_{\alpha}} \quad V_{\alpha} = V_{\alpha} + \frac{V_{\alpha} V_{\beta}}{I_{\alpha}} \quad V_{\alpha} = V_{\alpha$$

8a,
$$V_2 = V_{TH2} + IR_{TH2} = V_B + \frac{-V_{\alpha} - V_B}{V_{\alpha} + V_B + R} \times \frac{V_B}{I_B} = V_B - \frac{V_{\alpha}V_B + V_B^2}{V_{\alpha}I_B} \times \frac{V_{\alpha}I_B}{I_{\alpha}} + V_{\beta}I_{\beta}R$$

(Answer)

Page 3 of 13

(b) Derive the Thevenin equivalent network (between terminals A and B) of the simple circuit in sinusoidal steady state shown in Figure 3 below. Note that $V_S(t)$ is a sinusoidal signal of a particular frequency and $\underline{V_S}$ is its phasor representation. (5points)



Method2: Find ZH by deactivating independent sources 8 then applying a test voltage.

$$G_{j}X_{2} \oplus V_{T}$$

$$G_{j}X_{3} \oplus V_{T}$$

$$G_{j}X_{4} \oplus V_{T}$$

$$G_{j}X_$$

$$\begin{cases} X_{T} = jX_{2}I_{T} + jX_{m}I_{1}. \\ = jX_{2}I_{T} - jX_{m}^{2}.I_{T}. \\ \frac{X_{1}X_{2} - X_{m}^{2}}{X_{1}} = j\left(\frac{X_{1}X_{2} - X_{m}^{2}}{X_{1}}\right) \Omega \end{cases}$$

(a) Consider the circuit given in Figure 4 below. Assume $\omega = 10^4$ rad/s. The circuit is operating in sinusoidal steady state.

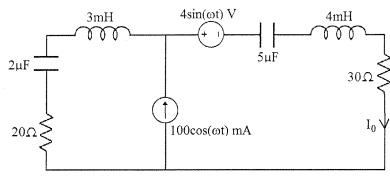
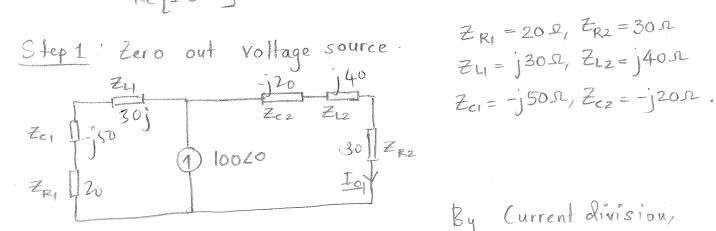


Figure 4

(i) Use the Principle of Superposition to obtain the current $I_0(t)$ as labeled in the circuit above. Express your answer both in phasor notation and in time domain, the current being in milliamps. Clearly write your reference signal and its phasor.



100/0
$$\frac{Z_2 = Z_{c2} + Z_{L2} + Z_{R2}}{Z_1 = 30 + j20}$$
.
 $Z_1 = Z_{c1} + Z_{L1} + Z_{R1}$
 $Z_2 = Z_{c2} + Z_{L2} + Z_{R2}$
 $Z_3 = Z_{c1} + Z_{L1} + Z_{R1}$
 $Z_4 = Z_{c1} + Z_{L1} + Z_{R1}$
 $Z_5 = Z_{c2} + Z_{L2} + Z_{R2}$
 $Z_6 = Z_{c2} + Z_{L2} + Z_{R2}$
 $Z_7 = Z_{c2} + Z_{L2} + Z_{R2}$
 $Z_7 = Z_{c1} + Z_{L1} + Z_{R1}$
 $Z_7 = Z_{c1} + Z_{L1} + Z_{R1}$

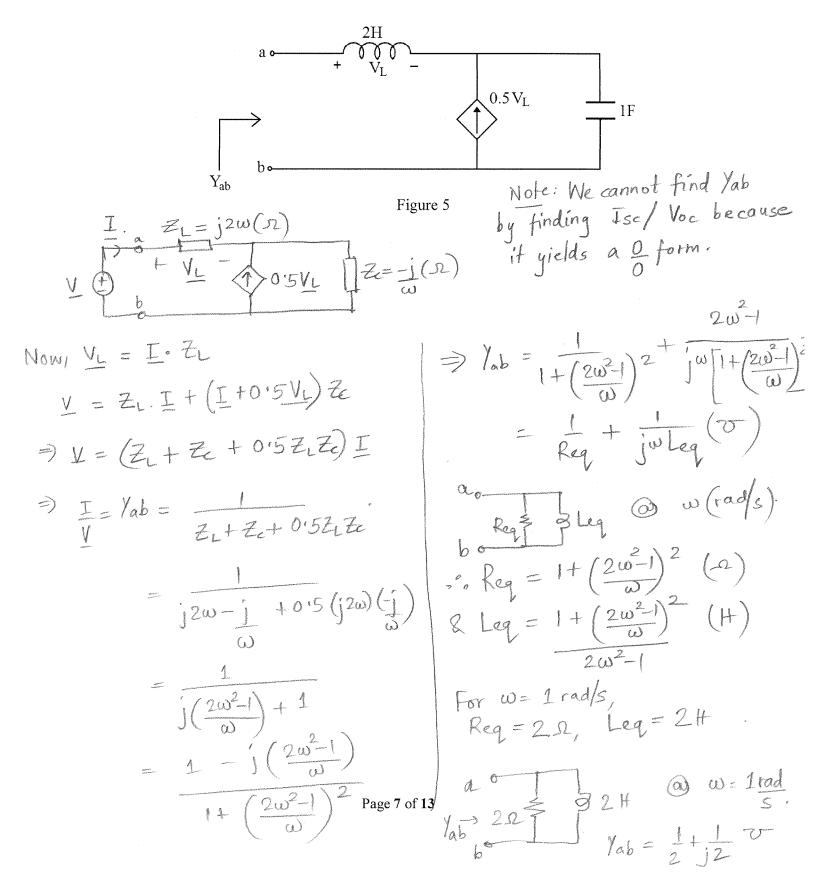
$$Z_{R_1} = 20\Omega$$
, $Z_{R_2} = 30\Omega$
 $Z_{L_1} = j30\Omega$, $Z_{L_2} = j40\Omega$
 $Z_{C_1} = -j50\Omega$, $Z_{C_2} = -j20\Omega$

Zeroing out Current Source: $4 \frac{290^{\circ}}{4 \frac{290^{\circ}}{4}} \Rightarrow \overline{I_{02}} = \frac{4 \frac{290^{\circ}}{4}}{50} A$ $= \frac{4000 \frac{290^{\circ}}{40} MA}{50}$ $= 80 \frac{290^{\circ}}{40} MA$ $= 80 \frac{290^{\circ}}{40} MA$ $= 40 + \frac{1}{40}$ $= 40 + \frac{1}{40}$ $= 40 \sqrt{2} \left(45^{\circ} MA\right)$ $= 40 \sqrt{2} \left(45^{\circ} MA\right)$ where $w = 10^{\circ}$ rad/s

(ii) Draw a phasor diagram showing the voltages across the 30Ω resistor, the 4mH inductor and the $5\mu F$ capacitor. Clearly show your reference phasor. (3 points)

Choosing cos wt as reference signal => Reference phasor = $1 \angle 0^\circ$ $= Re [1e]^{WT}$ Im $VR_2 = 1.2\sqrt{2} \angle 45^\circ$ $VR_2 = 1.2\sqrt{2} \angle 45^\circ$ (omplex Plane) $V_R = 1.2\sqrt{2} \angle 45^\circ$ Valtage & (urrent (Volts)) $V_R = 30. I_0 = 1.2\sqrt{2} \angle 45^\circ$ Volts $V_R = 30. I_0 = 0.8\sqrt{2} \angle 45^\circ$ Volts $V_R = 30. I_0 = 1.6\sqrt{2} \angle 135^\circ$ Volts

(b) For the network shown in Figure 5 below, find the equivalent input admittance Y_{ab} in sinusoidal steady state at a frequency ω (rad/s). Draw this admittance as a parallel combination of a resistance R and an inductance L. Next, clearly indicate the values of R and L for a sinusoidal frequency of $\omega = 1$ rad/s. (5 points)



Consider the circuit shown in Figure 6 below. The switch S opens at time t = 0s. For t < 0s, the circuit is operating in DC steady state.

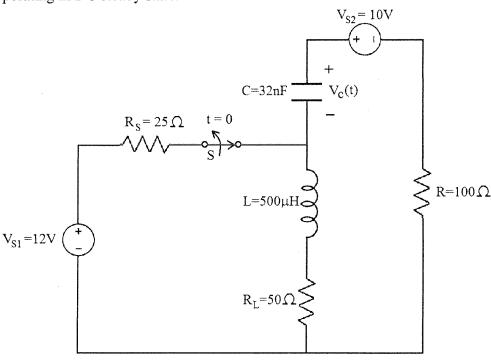


Figure 6

(a) Write and solve the differential equation for voltage across the capacitor, $V_C(t)$ for $t \ge 0$ s. (8 points)

for
$$t \ge 0$$
s, or $V_c + Ldi + i(R_L + R) = V_{S2}$

$$i = \frac{CdV_c}{dt}$$

$$l = \frac{CdV_c}{dt} + (R_L + R_L) \frac{CdV_c}{dt} + V_c = V_{S2}$$

$$R_c = \frac{1}{16} \frac{V_c}{V_c} + \frac{1}{16} \frac{V_c}{V_c}$$

$$V_{cn}(t) = e^{-ct} \left(A\cos\omega t + B\sin\omega t\right).$$

$$= 10 + e^{-ct} \left(A\cos\omega t + B\sin\omega t\right). \quad V_{o}IIS$$

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$$= 10 + e^{-ct} \left(A\cos\omega t + B\sin\omega t\right). \quad V_{o}IIS$$

$$= 12V + e^{-ct} \left(A\cos\omega t + B\sin\omega t\right). \quad V_{o}IIS$$

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$$10 + A = 2 \Rightarrow A = -8V$$

$$2 i_L(\sigma) = \frac{1}{25} \left[\frac{1}{$$

$$3.84 \times 10^{-2} + BWC = 0.16.$$

$$B = 19.0 \text{ V}$$

$$V_{c}(t) = 10 + e^{-\sigma t} \left(-8\cos\omega t + 19\sin\omega t\right) (V_{o}(t))$$

$$Where \sigma = -V_{o}(t) \times V_{o}(t)$$

$$2 w = 2 \times 10^{5} \text{ rad/s}.$$
(Answer)

(b) Find the energy stored in the inductor in steady state ($t = \infty$). Also, find the energy stored

in the capacitor in steady state
$$(t = \infty)$$
.

in the capacitor in steady state $(t = \infty)$.

 $i(\infty) = 0$ A (Due to the open capacitor in DC Steady $i(\infty) = 0$ A (Due to the open capacitor in DC S

o' o
$$W_{c} = \frac{1}{2}C(V_{c}(\infty))^{2}$$

$$= \frac{1}{2}X^{3}2X^{10}X^{9}X^{10}$$

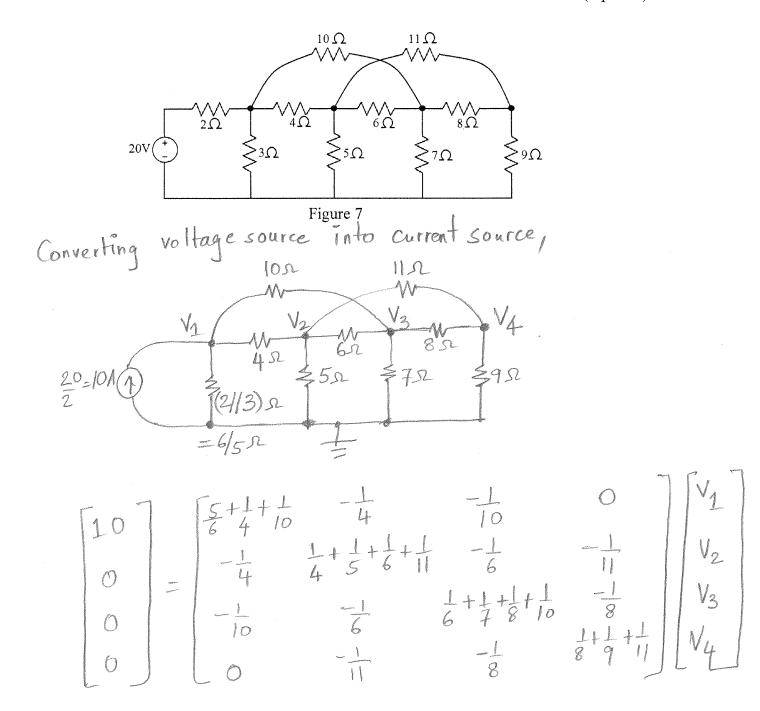
$$= \frac{1}{2}K^{3}2X^{10}X^{9}X^{10}$$

$$= \frac{1}{2}L(160)^{2} = 0$$

$$W_{L} = \frac{1}{2}L(160)^{2} = 0$$

For the circuit shown in Figure 7 below, label the nodes and write down the matrix equation needed to solve this circuit by the node method. You need not solve the matrix equation.

(5 points)



Question 5:

Note: No points will be awarded to answers without proper explanation.

(a) A complicated RLC circuit has a steady state current response of $10\cos(100 \text{ t} + 0.2)$ amperes when a voltage source of 10cos(100t) volts is applied to it. Now, instead of the original source, a voltage source of $20\cos(100t + 0.1)$ volts is applied to the circuit. Can you find the new current response? Is this information enough to find it? If yes, find the new current response. If not, give reasons justifying your answer. In either case, explain your answer.

We can find the new steady state current response if the following conditions are satisfied:

(1) The RLC circuit in question is linear (i.e. made of linear Rs, Ls & Cs)

(2) 10 cos (100t) is the only independent source in the circuit.

If the above two are true then we can find the new current response by simple scaling & time shift. The response would be = 20 cos(100t+0.2

= 20 cos (100t +0.3) A.

If (1) & (2) hold, then the entire RLC circuit can be replaced by a single impedance Z (in the phasor domain).

with cosloot as reference signal

$$= 20.20^{-3}$$
=) $I(t) = 20 \cos (100t + 0.3) A$

- (b) Suppose you designed a special capacitor for which the charge (Q) and voltage (V) are related as: $Q = C \cdot V^{1.5}$ where C is a constant.
 - (i) Can you use Superposition to solve for voltages and currents in circuits containing this capacitor? Explain giving appropriate reasons. (2 points)

this capacitor? Explain giving appropriate reasons. (2 points)
$$i = \frac{dQ}{dt} = C(1.5)V^{0.5}\frac{dV}{dt} = 1.5C\sqrt{V} \cdot \frac{dV}{dt}$$

If
$$i_1(t) = 1.5 C \sqrt{V_1} \frac{dV_1}{dt} & i_2(t) = 1.5 C \sqrt{V_2} \frac{dV_2}{dt}$$

then
$$i_1(t) + i_2(t) = 1.5 \left(\left[\sqrt{V_1} \frac{dV_1}{dt} + \sqrt{V_2} \frac{dV_2}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right] + 1.5 \left(\left[\sqrt{V_1 + V_2} \frac{d(V_1 + V_2)}{dt} \right]$$

The i-v relation for this capacitor is not linear & hence one cannot use Superposition to solve for voltages & currents incircuits using this capacitor.

(ii) Can you use Kirchhoff's Voltage and Current equations to solve for voltages and currents in circuits containing this capacitor? Explain giving appropriate reasons.

(2 points)

Ves | Kirchhoff's Voltage and Current equations originate from Classical Electromagnetics and hold for all lumped circuits (linear or non-linear) - Hence, we can use KVL and KCL to solve for voltages & currents in circuits involving this capacitor.