

①

a) Decimal digits in BCD

16,000,000

4 bits per single decimal digit, 8 decimal digits

44444444

$$4 \times 8 = 32 \text{ bits}$$

b) Hexadecimal representation

$$(16,000,000)_{10} \rightarrow (F42400)_{16}$$

6 bits

Hex is more efficient because it uses less bits than the BCD representation. In BCD, each decimal number corresponds to 4 bits, while in the hex representation, digits 0-9 as well as A-F are used, so fewer total bits are required for the hexadecimal system.

②

Radix	Value	Value in Decimal
16	(517)	1303
8	(517)	335

$$(517)_{16} \rightarrow \text{decimal:}$$

$$7 \times 16^0 + 1 \times 16^1 + 5 \times 16^2 \\ = 7 + 16 + 1280 \\ = 1303$$

$$(517)_8 \rightarrow \text{decimal}$$

$$7 \times 8^0 + 1 \times 8^1 + 5 \times 8^2 \\ = 7 + 8 + 320 \\ = 335$$

a) False; bubble on output means active LOW

b) True

c) True

$$\textcircled{3} \quad F(a,b,c,d) = \overline{(ab+c)} (ac + \overline{(b+c+abd)}) + a(\overline{(b+c)(b+d)+c})$$

Simplify:

$$= (\overline{a+b} \cdot \bar{c}) (ac + \overline{(b+c+abd)}) + a(\overline{(b+c)(b+d)+c})$$

$$= (\overline{a+b} \cdot \bar{c}) (ac + \overline{b\bar{c}(\overline{a\bar{c}d})}) + a(\overline{(b+c)(b+d)+c})$$

$$= (\overline{a+b} \cdot \bar{c}) (ac + bc(a+\bar{c}\bar{d})) + a(\overline{(b+c)(b+d)+c})$$

$$= (\overline{a+b}\bar{c}) (ac + bc(a\bar{c}\bar{d})) + a(\overline{(b+c) + (b+d) \cdot \bar{c}})$$

$$= (\overline{a+b}\bar{c}) (ac + bc(a+\bar{c}\bar{d})) + a(\overline{bc + b\bar{d}})\bar{c}$$

$$= \bar{a}\bar{c}[ac + bc(a+\bar{c}\bar{d})] + \bar{b}\bar{c}[ac + bc(a+\bar{c}\bar{d})] + a(\overline{bc + b\bar{d}})\bar{c}$$

$$= \bar{a}\bar{c}ac + \bar{a}\bar{c}bc(a+\bar{c}\bar{d}) + \bar{b}\bar{c}ac + \bar{b}\bar{c}bc(a+\bar{c}\bar{d}) + a(\overline{bc + b\bar{d}})\bar{c}$$

$$\underbrace{\quad \quad \quad \times \quad \quad \times \quad \quad \times \quad \quad \times \quad \quad \quad}_{\text{all cancel out}}$$

$$= \bar{a}\bar{c}\bar{b}\bar{c} + \bar{a}\bar{c}\bar{b}\bar{d}$$

$$= \bar{a}\bar{c}\bar{b} + \bar{a}\bar{c}\bar{b}\bar{d}$$

$$= \boxed{\bar{a}\bar{c}\bar{b}}$$

#5

④

a) Gray code is unique because it is unweighted, allowing it to have a cyclical property. Essentially, the hamming distance between adjacent numbers is 1, so as you progress through each cycle, each number only changes by 1 bit. Once you reach the end of the cycle, the final number will be 1 bit different than the starting number (e.g. for $n=4$ bits, $15 = 1000$ & $0 = 0000$).

↑
end ↑
 beginning

For converting GC to binary, the MSB stays the same, and you add the digits from left to right, neglecting the carry with each addition.

For example:

Gray Code: 1011 1100
 ↓ ↓
 ↙ ↘ ↙ ↘
Binary: 1101 0000

b) X-3 code is essentially the same as BCD but shifted by 3. You derive X-3 code from BCD by adding 3, but because you add 3, it isn't valid to represent -3 to -1 or 10 to 12 in X-3 code. Since X-3 code is only valid from 0 to 9, it's self-complementary, meaning two X-3 codes whose sum is 9 (the max value) are complements of each other. In other words, 1's complement of X-3 code is 9's complement of the corresponding decimal.

For example:

Decimal	X-3 code
2	0101
7	1010
+ ↗ complements	
<hr/>	
9	

④ c) $(25)_{10} - (-25)_8$

Convert to binary:

$(25)_{10} \Rightarrow 25/2 = 12 \text{ R}1$
 $12/2 = 6 \text{ R}0$
 $6/2 = 3 \text{ R}0$
 $3/2 = 1 \text{ R}1$
 $1/2 = 0 \text{ R}1$
 $= (11001)_2$

from table
 $(25)_8 = (10101)_2$

2's complement: $00010101 \rightarrow 111101010$

sign bit

$$\begin{array}{r} 111101010 \\ + 11110101 \\ \hline 11110101 \end{array}$$
 2's complement of $(25)_8$

$$\begin{array}{r} 00011001 \\ - 11110101 \\ \hline \end{array} \rightarrow 2's \text{ complement again:}$$

$$\begin{array}{r} 111101011 \\ + 000010100 \\ \hline 00010101 \end{array}$$

$$\begin{array}{r} 00011001 \\ + 00001010 \\ \hline 000101110 \end{array} = (46)_{10}$$

sign bit

$(000101110)_2 = (46)_{10}$

d) XOR gate

A	B	Y
0	0	0
0	1	1
1	0	1
1	1	0

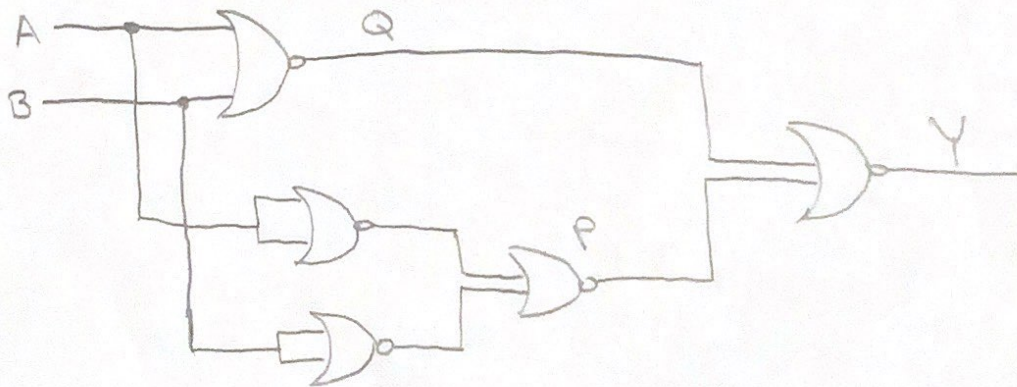
$$Y = \bar{A}B + A\bar{B} + (\bar{A}\bar{A} + \bar{B}\bar{B})$$

$$= (A+B)(\bar{A}+\bar{B})$$

$$= \overline{(A+B)(\bar{A}+\bar{B})}$$

$$= \overline{(A+B)} + \overline{(\bar{A}+\bar{B})}$$

Q P



6) b) $F = (A, B, C, D) = \bar{A}\bar{B}\bar{D} + \bar{A}CD + \bar{A}BC$, $d(A, B, C, D) = \bar{A}B\bar{C}D + ACD + A\bar{B}\bar{D}$

AB \ CD	$\bar{C}\bar{D}$	$\bar{C}D$	CD	$C\bar{D}$
$\bar{A}\bar{B}$	1	0	1	1
$\bar{A}B$	0	X	1	1
$A\bar{B}$	0	0	X	0
AB	X	0	X	X

$F(A, B, C, D) = \sum(0, 2, 3, 6, 7)$
 $d(A, B, C, D) = \sum(5, 8, 11, 10, 15)$

c) $F = \sum(0, 1, 2, 5, 8, 9, 10)$

AB \ CD	$\bar{C}\bar{D}$	$\bar{C}D$	CD	$C\bar{D}$
$\bar{A}\bar{B}$	1	1	0	1
$\bar{A}B$	0	1	0	0
$A\bar{B}$	0	0	0	0
AB	1	1	0	1

SOP

from k-map (in dotted pencil)

$\Rightarrow F(A, B, C, D) = (\bar{A}\bar{C}D) + (\bar{B}\bar{C}) + (\bar{B}\bar{D})$

POS

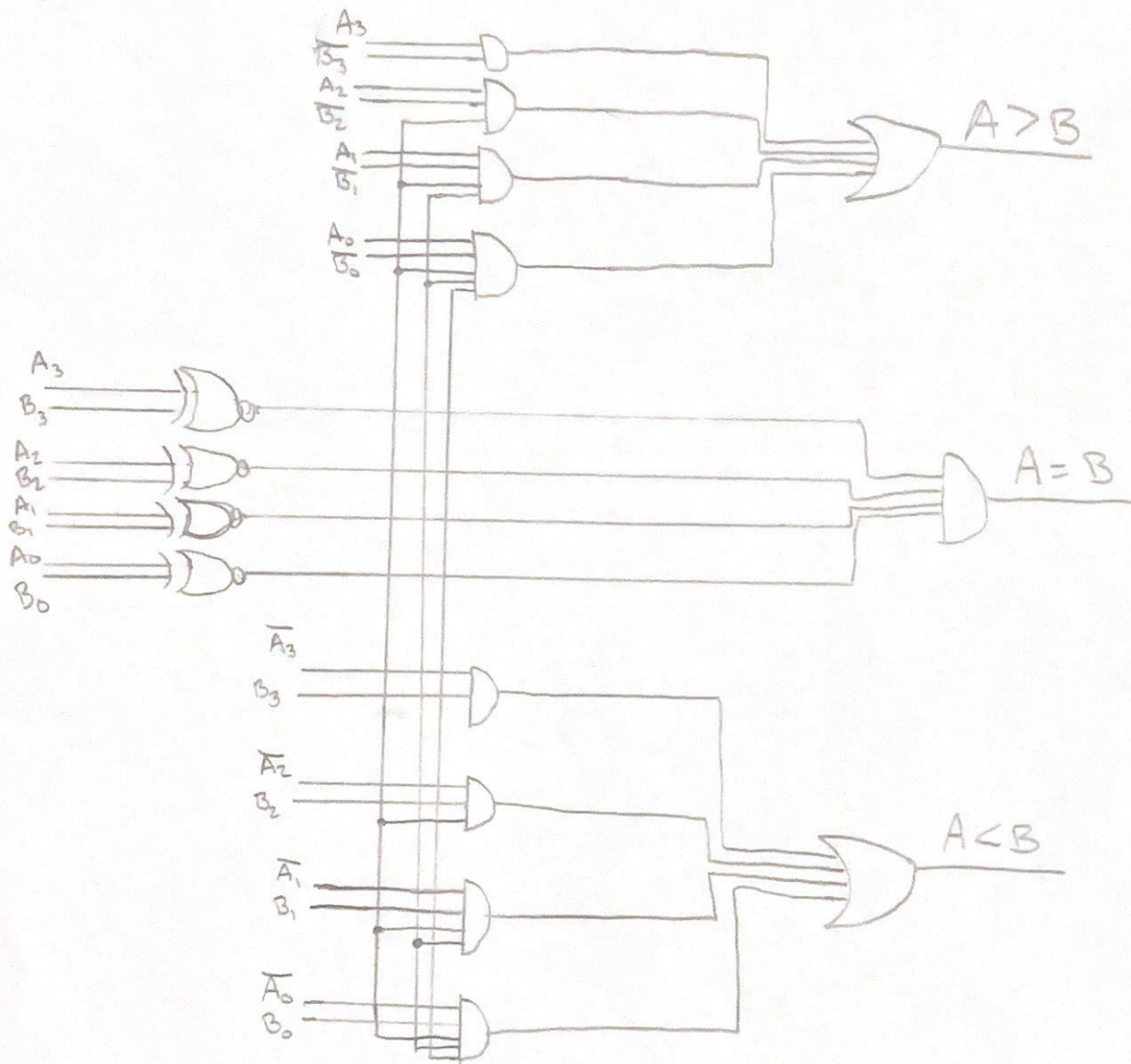
from k-map (in red pen)

$\Rightarrow \bar{Y} = \bar{B}\bar{D} + AB + CD$

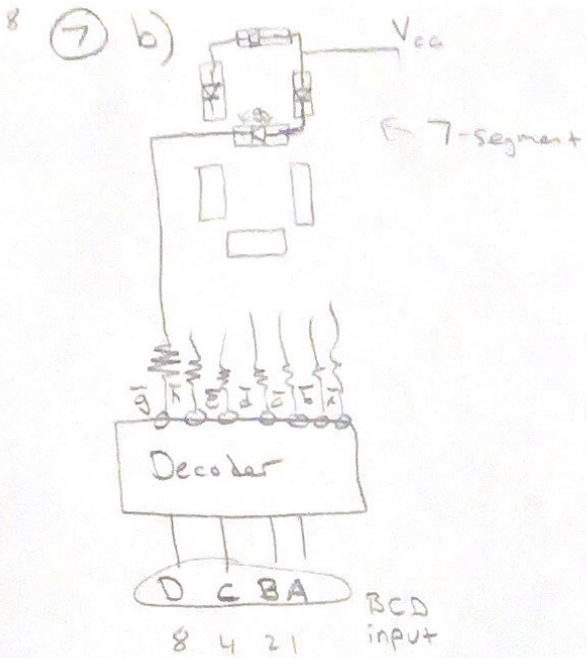
$Y = \overline{\bar{B}\bar{D} + AB + CD}$
 $= (\overline{\bar{B}\bar{D}})(\overline{AB})(\overline{CD})$

$= (\bar{B} + D)(\bar{A} + \bar{B})(\bar{C} + \bar{D})$

7 a)

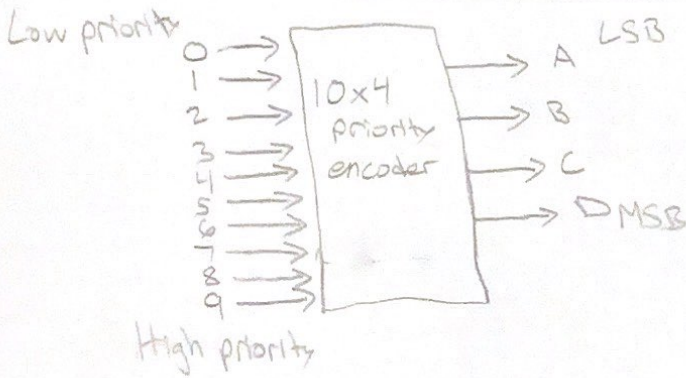


IF $A \neq B$, the comparator sequentially checks the bits from MSB and cascades down to the LSB. In the $>$ portion, the bits for B are inverted so that the and-gate will only be satisfied if $A > B$, otherwise the output will be 0. In the diagram, if $A_3 = B_3$, then $A_2 \neq B_2$ are compared next, and if those are equal, then $A_1 \neq B_1$, all the way to LSB. The extra lines are inhibitors in case a more significant bit is evaluated to be greater or less so you don't have to go through every single bit.



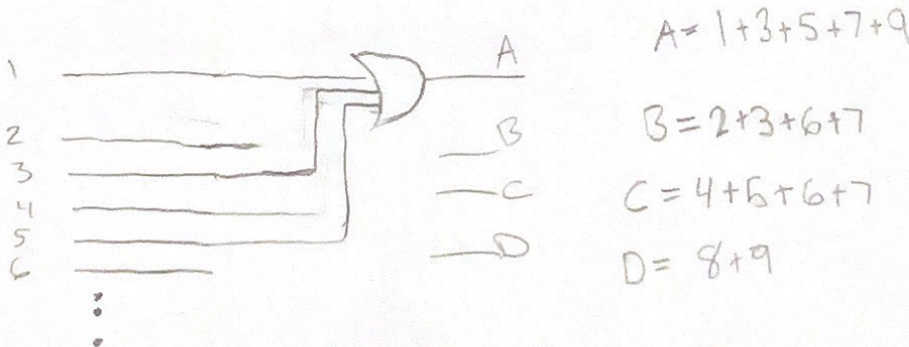
Within the decoder, the logic is constructed so that all numbers that utilize segment g (2,3,4,5,6,7,8,9) produce an active low output for g. This allows current to flow through the diodes in segment g from the V_{cc} due to the voltage potential created. The BCD inputs determine whether or not g is high or low, which in turn causes segment g to be either turned on or off.

c)



Input		Output			
G	3	D	C	B	A
0	0	0	0	0	0
0	0	0	0	0	1
0	0	0	0	1	0
0	0	0	0	1	1
0	1	0	0	1	0
0	x	0	0	1	0
1	x	0	1	0	1
x	x	0	1	1	0
					⋮

To prevent multiple inputs from being read at the same time, you can prioritize inputs, which in this problem is prioritizing from highest decimal to lowest. If a higher priority input is used, then the rest of the inputs with lower priority are disregarded, hence the 'x' in the truth table.



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M	S, S ₀	Expression for F _i	Expression for C _{i+1}
0	00	$0 \oplus [(0 \oplus A_i) \oplus (0)]$	$[(0)(0 \oplus A_i)] \cdot [(0)(0 \oplus A_i) \oplus (0)]$
0	01	$0 \oplus [(1 \oplus A_i) \oplus (0)]$	$[(0)(1 \oplus A_i)] \cdot [(0)(1 \oplus A_i) \oplus (0)]$
0	10	$B_i \oplus [(0 \oplus A_i) \oplus (0)]$	$[(0)(0 \oplus A_i)] \cdot [(1)(0 \oplus A_i) \oplus (0)]$
1	10	$B_i \oplus [(0 \oplus A_i) \oplus (C_i)]$	$[(C_i)(0 \oplus A_i)] \cdot [(1)(0 \oplus A_i) \oplus (C_i)]$
1	11	$B_i \oplus [(1 \oplus A_i) \oplus (C_i)]$	$[(C_i)(1 \oplus A_i)] \cdot [(1)(1 \oplus A_i) \oplus (C_i)]$

general expression for F_i: $S_i B_i \oplus [(S_0 \oplus A_i) \oplus (M C_i)]$

" " " C_{i+1}: $[(M C_i)(S_0 \oplus A_i)] \cdot [S_i B_i \oplus [(S_0 \oplus A_i) \oplus (M C_i)]]$